



SUFFIX

PURPOSE:

To aid the operator in testing or diagnosing failures associated with all Sigma 7 Byte String (excluding EBC), Stack, Multiple and Convert instructions.

REQUIRED CONFIGURATION:

Any Sigma 7 with card or paper tape reader.

PREREQUISITES:

The operations and functions tested by AUTO must be successfully operating.

LOADING INSTRUCTIONS:

CONTROL MODE in LOCAL, WATCHDOG TIMER in NORMAL, INTERLEAVE SELECT in NORMAL, AUDIO in ON, PARITY ERROR MODE in CONT, SENSE all OFF.

1. Clear memory
2. Perform standard LOAD procedure
3. Program execution begins automatically if LOAD is successful.

SUCCESS INDICATIONS:

SS3 and SS4 OFF - Continued execution of the program without a WAIT or ALARM indication.

SS3 ON - A typed REPORT or a WAIT (INSTRUCTION ADDRESS indicators = 1EO16) if typewriter is OFF see SS3 option.

SS4 ON - No error messages if typewriter is ON.

ERROR INDICATIONS AND PROCEDURES:

SS4 OFF - typewriter ON - Error message followed by WAIT (INSTRUCTION ADDRESS indicators = 1EO16).

SS4 OFF - typewriter OFF - WAIT executed (1EO16).

SS4 ON - SS3 ON - Error message if typewriter is ON.

SS4 ON - SS3 OFF - No error indications.

See section entitled "ERRORS"

OPTIONS:

Suffix has an error counter which can be displayed on demand and also when an error occurs. A pass counter is also included. The pass counter indicates the number of completed runs of the program from its most recent load time, and the number of individual tests performed during the present pass.

The system provides several modes of operation. The selection of these modes is done with the Sense Switches, and are described below.

SS1 ON = Short Loop
 OFF = Normal operation

The short loop provides a means of accessing any "Object Instruction"* at the highest possible constant frequency to allow the operator the ability to observe signal levels relative to the instruction with the aid of an oscilloscope. An error WAIT will not occur. The loop consists of those instructions necessary to initialize the pertinent registers ** of the Object Instruction, a means of executing the Object Instruction, and those instructions necessary to test and respond to the Sense Switch settings. SENSE switches 2, 3, and 4 are inoperative when SS1 is ON.

SS2 ON = Long Loop
 OFF = Normal Operation

This loop does the following: Reinitializes all the registers ** of the Object Instruction, executes the Object Instruction, tests all the pertinent registers ** and then loops back.

When a catastrophic error occurs such as the Instruction being destroyed, or when the contents of an initializing table in core are destroyed, the short loop will not sufficiently reinitialize the Object Instruction. Therefore, Long Loop must be employed.

*The term "Object Instruction" is understood to mean an instruction which is the object of the present TEST being performed.

** "Registers" includes; FAST MEMORY, MEMORY and PROGRAM STATUS WORDS.

SS3 ON = Report

OFF = Normal Operation

Report is the Display of pertinent information. This information is only available at the end of each object test. The program description will state what information is being displayed.

If the typewriter is ON, the program will WAIT at 1EO₁₆ after printing unless SS4 is ON.

SS4 ON = No Halt or Errors

OFF = Halt on Errors

The ability to bypass error halts is particularly useful when used with the "Long Loop" option (refer SS2). The counters and the visual and audio error indicators provided by the system assure the operator that no error will go unnoticed when the error halts are being inhibited.

TYPEWRITER ON = TYPE ERROR MESSAGE

OFF = NO MESSAGE

- There are two types of output: (1) Error Display, and (2) Report.

The Error Display occurs when an error is detected and the typewriter switch is on.

The Report occurs when Sense Switch 3 is set and the typewriter is on.

Both outputs yield the Object Data Block address, the Error Counter, the Pass Counter and the Object Instruction. The Error Display also prints an Error Identifier, the erroneous result, the predetermined result and the differences between the two results.

TEST SELECTION:

If the INTERRUPT button is depressed at any time during program execution, a WAIT will be executed with the INSTRUCTION ADDRESS indicator = FE₁₆. A specific test may be continuously executed by inserting the test address into bits 12 thru 31 of register 1 setting SENSE switch 1 or 2 and clearing the WAIT. Normal operation can be restored by turning off SENSE switch 1 and 2.

PROGRAM DESCRIPTION: The memory parity interrupt is constantly monitored. If a parity error occurs, the program will execute a WAIT with P equal to $(EC)_{16}$. Information concerning which plain the error occurred on can be found in register 4.

The interrupt button is used to reset the Object Test in the event of a Catastrophic failure. A WAIT will be executed with P equal to $(FE)_{16}$. By setting SELECT ADDRESS to 273_{16} , ADDR STOP to ON and allowing the program to continue to that point, the operator can single step or single phase, through the execution of the Object Test Instruction for a more precise observation. This procedure is outlined under "ERRORS".

→ The interrupt button can also be used to change the Object Test by inserting the desired test address into the address bits of Register One.

The program consists of a "DRIVE PROGRAM" and a "DATA FIELD". The Data Field is comprised of many DATA BLOCKS, each of which contain pre-settings of programmable registers, the instruction to be tested and the pre-determined result with which to test the registers. The Driver provides the "CONTROLS" to use the information in the Data Field for Error detection and Display.

The Driver consists of 5 major portions. They are (1) INITIALIZE, (2) SET UP, (3) EXECUTE, (4) TEST, (5) and CONTROL. These names are used to facilitate the description of the program.

SUFFIX features a wide variety of modes of operation and controls as well as a high degree of error discernibility. The discriminating bit configurations are generated from logic layouts and will provide ERROR DETECTION to the "signal" level.

The program is designed to initialize itself at load time. This initialization resets the Error and Pass Counters to zero. A DATA

BLOCK pointer is set to point to the Object Data Block. The DATA BLOCKS vary in length depending on the Object instruction.

The INITIALIZE portion of the program moves the Object Data Block into a working area called TABLE. The TABLE is always cleared prior to this move. The first word of the data block is a number which indicates how many words are in the Block. The format of the Block is as follows:

- TABLE + 0 Negative Count
- 1 Object Instruction
 - 2 $(CC)_{co-3} + (FC)_{(4-8)} + (MS+DM, AM)_{(8-11)} + LINKAGE_{(12-3)}$
 - 3 PSWI out
 - 4 Reg 12 in-Index
 - 5 Reg 12 out
 - 6 Memory in/Reg 0 in
 - 7 Memory out/Reg 0 out
 - 8 Reg 13 in-Indirect Address
 - 9 Reg 13 out
 - 10 Memory +1 in/Reg 1 in
 - 11 Memory +1 out/Reg 1 out

The first 12 bits of the third word are used to initialize PSWI bits 0 through 11. The rest of the bits are used for initializing address modification for trap conditions.

After INITIALIZE, comes SET UP and PERFORM. When the move is completed, the programmable registers are set up (SETUP), and the Object instruction is performed (PERFORM). The Sense Switches are then tested and if Short Loop is called for, Registers 12 and 13, memory and memory +1 and the Program status words are reinitialized and the object instruction is performed. (In the case of the TBS instruction a table is also reinitialized.) This process continues until sense switch 1 is reset. This allows the program to continue on to TEST.

During TEST, all the programmable registers are tested against pre-determined results. Each register is tested in a subroutine called ERROR. If an error is detected, the program looks to see if the typewriter is on-line and not busy. If it is busy and on-line the program waits. If it is on-line and not busy, the program proceeds to a subroutine called EDIT which edits the data for typewriter output and then proceeds to another subroutine called OUTPUT. This subroutine types out the data and then the program proceeds to the error halt test. There it halts or proceeds, depending on Sense Switch 3, and exits ERROR.

After all the registers have been tested, the Sense Switches are interrogated for SENSE Switch 4 (REPORT), and then for SENSE switch 2 (LONG LOOP). If the long loop is called for the program branches back to SET UP. If not, the program continues on to INITIALIZE. These last decisions are made in the CONTROL portion of the program.

ERRORS:

The program runs until a memory parity or a normal error WAIT (P equal to $(\underline{EC})_{16}$ or $(\underline{IEO})_{16}$ respectively) is encountered. If the program loses control, depress the interrupt button. A halt should occur at Location 273_{16} . Allow program to proceed to that point. By single stepping through EXECUTE, the operator may learn the nature of the failure. If this does not work, reload the program with Sense Switch 4 set. This will cause the REPORT WAIT or typeout to occur after each test. By interference, the operator may determine which test is causing the loss of control.

The normal error WAIT is at Location $(\underline{IDF})_{16}$. The contents of the registers is as follows:

R1 Present List Address

R2 Errors

R3 Passes (Bits 0-15)/Module (Bits 16-31)

R4 Instruction

R5 Error Identifier and Address

10000000 = Instruction

20000000 = Location +1 of the execution location

30000000 = Indirect Address word

4000000X = Index Register - RI

50000001 = PSW1

50000002 = PSW2

6000000X = Register X. X=0 through F(15₁₀)

7000WXYZ = Memory word in Location WXYZ

7100WXYZ = Memory word in Location WXYZ (FMT table*)

7200WXYZ = Memory word in Location WXYZ (VMT table **)

Also, in connection with locations (WXYZ), the actual data may be found in tables MT1 through MT4 - actual locations may be determined from listing (e. g., if the identifier is 7100 WXYZ, and according to the object BLOCK containing the instruction that failed, MT1 was the object table for FMT, the data word in question would be found in location MT1 + WXYZ - FMT.).

R6 Erroneous Result

R7 Predetermined Result

R8 Difference between R6 and R7 (Exclusive OR)

Explanation of Identifiers:

The programmable registers that are most likely to fail if a machine malfunction is to occur are:

- (1) The location occupied by the instruction, because - the instruction is pointed out by an EXU instruction. The memory address register is pointing at the object location during a large portion of the EXU instruction.
- (2) The location following the EXU instruction, because - its address is in the Q register during the execution of the Object Instruction.

*FMT - Fixed Memory Table - usually a source table

**VMT - Variable Memory Table - usually a destination table

- (3) The Indirect Address word, because - its address is in P register during much of the Object Instructions execution if the IA bit is set.
- (4) The Index Register - Register i is the only register referenced for Indexing. (R_i is a variable.)
- (5) The program status words, because - these reflect the state of the machine and must reflect the proper response.
- (6) Registers 0, 12, and 13, because - they are the only registers referred to by the instructions R field and must thereby be monitored.
- (7) The contents of the effective tables, because - these addresses are in the P register during the major portion of the Object instructions execution.

PROGRAM LOADER:

All 9 CPU Diagnostic programs use the Diagnostic Loader and are in the diagnostic load format.

The loader is designed to use a minimum amount of the total system's capability in order to provide the best possibility of a successful load.

The first 24 bits of each card contain the byte address of the location in which the card will be loaded. The Address is right justified. The next 8 bits contain a byte count of the bytes on the card. This will usually be 74_H bytes (29_D words). The next 116_D bytes will be the program in binary.

The loader makes use of this format and employs command chaining to simplify the loader and eliminate use of the adder. The first command pair reads the first 3 bytes of the card into the address portion of the third command pair; the second command pair reads the next byte into the byte count of the third command pair; and, the third command pair reads the rest of the card into its designated memory locations.

The last card loads a branch into the loader which transfers control to the designated location in the program. The loader uses the AUTOMATIC FILL LOADER by loading register 0 with a pointer to the above mentioned command chaining pairs and branching back to location 27_H of the AUTO FILL LOADER.

27	SIO, 0	*25 _H	
28	TIO, 0	*25 _H	Loaded by AUTO FILE
29	BCS, C _H	28 _H	
2A	LW, 0	2C _H	
2B	BCR, 0	27 _H	
2C	PZE, 0	17(2E _H)	DA = Double Word Address DA(2E _H) = 17 _H
2E	Rd	BA(32 _H +1)*	BA = Byte Address BA(32 _H +1) = C9 _H
2F		3	BA(32 _H +3) = CF _H
30	Rd	BA(33 _H +3)*	BA(33 _H +3) = CF _H
31		1	
32	Rd	()	
33		()	

*The +1, +3 refer to the 1st and 3rd byte of the word respectively.

ASIGMET EI,LO

1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34

00000000

00000000

00000000

```

SYSTEM SIG7FDP
TITLE 'SUFFIX(2)'
SOCW
FORMS THE ADDRESS FIELD SPECIFIES HOW THE WORD IS DIVIDED AN
      HOW MANY BITS THERE WILL BE IN EACH PART OF THE WORD.
      EFFECTIVE AT ASSEMBLY TIME ONLY.
      I FORM 4,28
      J FORM 8,24
      K FORM 4,4,4,20
      *
      PRCS EFFECTIVE AT ASSEMBLY TIME ONLY.
      P SPECIFIES A DOUBLEWORD ADDRESS
      *
      P CNAME
      PROC
      LF GEN,32 DA(AF(1))
      PEND
      *
      JJ CONVERTS TO BYTE ADDRESS
      CNAME
      PROC
      LF GEN,8,24 AF(1),BA(AF(2))
      PEND
      *
      FILL FILLS ALL LOCATIONS BETWEEN AF AND $ WITH ZEROS
      CNAME
      PROC
      LF EQU $
      DB ABSVAL(AF)-ABSVAL($)
      GEN,32 0
      FIN
      PEND
      *

```

35
36
37
38
39
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56
57
58
59
60
61
62
63
64
65
66
67
68
69
70

SUFFIX(2)
PAGE

*
* SUFFIX - SIGMA CPU DIAGNOSTIC SYSTEM
*

* SENSE SWITCH DEFINITIONS

* SS1 SET=SHORT LOOP
* RESET=NORMAL OPERATION
*

* SS2 SET=LONG LOOP
* RESET=NORMAL OPERATION
*

* SS3 SET=REPORT
* RESET=NORMAL OPERATION
*

* SS4 SET=NO HALT ON ERRORS
* RESET=HALT ON ERRORS
*

* REGISTER CONTENTS ON ERROR HALT

* R1 PRESENT LIST ADDRESS
* R2 ERRORS
* R3 PASSES
* R4 INSTRUCTION
* R5 ERROR IDENTIFIER AND ADDRESS:
* 10000000 = INSTRUCTION
* 20000000 = LOCATION+1 OF THE EXECUTION LOCATION
* 30000000 = INDIRECT ADDRESS
* 40000001 = INDEX REGISTER-R1
* 5000000X = PROGRAM STATUS WORD X; X=1 OR 2
* 6000000X = REGISTER X; X=0 THROUGH F(1111)
* 7000WXYZ = MEMORY WORD IN LOCATION WXYZ (WXYZ=0-FFFF)
* 7100WXYZ = MEMORY WORD IN LOCATION WXYZ (FMT TABLE)
* 7200WXYZ = MEMORY WORD IN LOCATION WXYZ (VMT TABLE)
* R6 ERRONEOUS RESULT
* R7 PREDETERMINED RESULT
* R8 DIFFERENCE BETWEEN R6 AND R7

		SUFFIX(2)				
		PAGE	ORG			TRAP LOCATIONS
71						
72	1 00040			X'40'		
	1 00040					
73	1 00040	0F00005E	NABTR	XPSD,0	NAB	NONALLOWED OPERATION
74	1 00041	0F000072	UIITR	XPSD,0	UII	UNIMPLEMENTED INSTRUCTION
75	1 00042	0F000078	SLTR	XPSD,0	SL	STACK LIMIT
76	1 00043	0F00007E	FXPTR	XPSD,0	FXPO	OVERFLOW ON CONVERT BY ADDITION
77	1 00044	0F000084	FLPFTR	XPSD,0	FLPF	FLOATING POINT
78	1 00045	0F00008A	DFTR	XPSD,0	DF	NONALLOWED DIGIT CONFIGURATION
79	1 00046	0F000090	WDTRTR	XPSD,0	WDTR	WATCHDOG TIMER RUNOUT
80	1 00047	0F0003D8	BRANCH	XPSD,0	RETURN	BRANCH RETURN
81	1 00048	0F000096	CAL1TR	XPSD,0	CAL1	CALL ONE
82	1 00049	0F0000AA	CAL2TR	XPSD,0	CAL2	CALL TWO
83	1 0004A	0F0000BE	CAL3TR	XPSD,0	CAL3	CALL THREE
84	1 0004B	0F0000D2	CAL4TR	XPSD,0	CAL4	CALL FOUR
85			*			INTERRUPT LOCATIONS
86	1 0004C	00000000		FILL	X'54'	
	1 0004D	00000000				
	1 0004E	00000000				
	1 0004F	00000000				
	1 00050	00000000				
	1 00051	00000000				
	1 00052	00000000				
	1 00053	00000000				
87	1 00054	331003E4		MTW,1	CNT3CP	
88	1 00055	331003E5		MTW,1	CNT4CP	
89	1 00056	0F0000E6		XPSD,0	PARITY	MEMORY PARITY
90	1 00057	00000000		FILL	X'5A'	
	1 00058	00000000				
	1 00059	00000000				
91	1 0005A	0F000360		XPSD,0	CNT3Z	
92	1 0005B	0F000360		XPSD,0	CNT4Z	
93	1 0005C	0F0000EE		XPSD,0	INOUT	I/O
94	1 0005D	0F0000F6		XPSD,0	RESET	INTERRUPT BUTTON

SUFFIX(2)
PAGE

Line	Op	Hex	Op	Op	Op	Op	Op	Op	Op	Op
95										
96			*							
97	1	0005E			BOUND	8				NON-ALLOWED OPERATION TRAP
98	1	0005E	00000000	A	NA0		PZE			
99	1	0005F	00000000	A			PZE			
100	1	00060	00000062				PZE,0	\$+2		
101	1	0C061	00000000	A			PZE			
102	1	00062	0F0003D8		NA0RET		XPSD,0	RETURN		NONALLOWD OPERATION TRACC#1 19
103	1	00063	0F0003D8		MPVRET		XPSD,0	RETURN		MEMORY PROTECT VIOLATION TRACC#1 19
104	1	00064	0F0003D8		MVRET		XPSD,0	RETURN		MODE VIOLATION TRACC#2 19
105	1	00065	0F0003D8				XPSD,0	RETURN		
106	1	00066	0F0003D8		NEARET		XPSD,0	RETURN		NONEXISTANT ADDRESS TRACC#4 19
107	1	00067	0F0003D8				XPSD,0	RETURN		
108	1	00068	0F0003D8				XPSD,0	RETURN		
109	1	00069	0F0003D8				XPSD,0	RETURN		
110	1	0006A	0F0003D8		NEIRET		XPSD,0	RETURN		NONEXISTANT INSTRUCTION TRACC#8 19
111	1	0006B	0F0003D8				XPSD,0	RETURN		
112	1	0006C	0F0003D8				XPSD,0	RETURN		
113	1	0006D	0F0003D8				XPSD,0	RETURN		
114	1	0C06E	0F0003D8				XPSD,0	RETURN		
115	1	0006F	0F0003D8				XPSD,0	RETURN		
116	1	00070	0F0003D8				XPSD,0	RETURN		
117	1	00071	0F0003D8				XPSD,0	RETURN		
118			*							
119	1	00072				BOUND	8			UNIMPLIMENTED INSTRUCTION TRAP
120	1	00072	00000000	A	UII		PZE			
121	1	00073	00000000	A			PZE			
122	1	00074	00000076				PZE,0	\$+2		
123	1	00075	00000000	A			PZE			
124	1	00076	0F0003D8		UIIRET		XPSD,0	RETURN		
125			*							
126	1	00078				BOUND	8			STACK LIMIT REACHED TRAP
127	1	00078	00000000	A	SL		PZE			
128	1	00079	00000000	A			PZE			
129	1	0007A	0000007C				PZE,0	\$+2		
130	1	0007B	00000000	A			PZE			
131	1	0007C	0F0003D8		SLRET		XPSD,0	RETURN		

SUFFIX(2)
PAGE

132										
133				*						FIXED POINT ARITHMETIC OVERFLOW TRAP
134	1	0007E				BOUND 8				
135	1	0007E	00000000	A	FXP0	PZE				
136	1	0007F	00000000	A		PZE				
137	1	00080	00000082			PZE,0	\$+2			
138	1	00081	00000000	A		PZE				
139	1	00082	7020007E			LC	FXP0			
140	1	00083	0F0003D8		FP0RET	XPSD,0	RETURN			
141				*						FLOATING POINT ARITHMETIC FAULT TRAP
142	1	00084				BOUND 8				
143	1	00084	00000000	A	FLPF	PZE				
144	1	00085	00000000	A		PZE				
145	1	00086	00000088			PZE,0	\$+2			
146	1	00087	00000000	A		PZE				
147	1	00088	70200084			LC	FLPF			
148	1	00089	0F0003D8		FPFRET	XPSD,0	RETURN			
149				*						DECIMAL ARITHMETIC FAULT TRAP
150	1	0008A				BOUND 8				
151	1	0008A	00000000	A	DF	PZE				
152	1	0008B	00000000	A		PZE				
153	1	0008C	0000008E			PZE,0	\$+2			
154	1	0008D	00000000	A		PZE				
155	1	0008E	7020008A			LC	DF			
156	1	0008F	0F0003D8		DFRET	XPSD,0	RETURN			
157				*						WATCHDOG TIMER RUNOUT TRAP
158	1	00090	00000000	A	WDTR	PZE				
159	1	00091	00000000	A		PZE				
160	1	00092	00000094			PZE,0	\$+2			
161	1	00093	00000000	A		PZE				
162	1	00094	0F0003D8		WDTRET	XPSD,0	RETURN			

136				SUFFIX(2)		
187				PAGE		
188	1	00CAA		*	BOUND 8	CALL 2 TRAP
189	1	000AA	00000000	A CAL2	PZE	
190	1	000AB	00000000	A	PZE	
191	1	00CAC	000000AE		PZE,0	\$+2
192	1	00CAD	00000000	A	PZE	
193	1	000AE	0F0003D8	C2RET	XPSD,0	RETURN TRACC=0
194	1	000AF	0F0003D8		XPSD,0	RETURN TRACC=1 I9=1
195	1	000B0	0F0003D8		XPSD,0	RETURN TRACC=2 I9=1
196	1	000B1	0F0003D8		XPSD,0	RETURN TRACC=3 I9=1
197	1	000B2	0F0003D8		XPSD,0	RETURN TRACC=4 I9=1
198	1	000B3	0F0003D8		XPSD,0	RETURN TRACC=5 I9=1
199	1	000B4	0F0003D8		XPSD,0	RETURN TRACC=6 I9=1
200	1	00CB5	0F0003D8		XPSD,0	RETURN TRACC=7 I9=1
201	1	00CB6	0F0003D8		XPSD,0	RETURN TRACC=8 I9=1
202	1	00CB7	0F0003D8		XPSD,0	RETURN TRACC=9 I9=1
203	1	00CB8	0F0003D8		XPSD,0	RETURN TRACC=10 I9=1
204	1	00CB9	0F0003D8		XPSD,0	RETURN TRACC=11 I9=1
205	1	000BA	0F0003D8		XPSD,0	RETURN TRACC=12 I9=1
206	1	000BB	0F0003D8		XPSD,0	RETURN TRACC=13 I9=1
207	1	000BC	0F0003D8		XPSD,0	RETURN TRACC=14 I9=1
208	1	000BD	0F0003D8		XPSD,0	RETURN TRACC=15 I9=1

		SUFFIX(2)			
		PAGE			
209					
210					
211	1 000BE			BOUND 8	CALL 3 TRAP
212	1 000BE	00000000	A	CAL3 PZE	
213	1 000BF	00000000	A	PZE	
214	1 000C0	000000C2		PZE,0 \$+2	
215	1 000C1	00000000	A	PZE	
216	1 000C2	0F0003D8		CSRET XPSD,0	TRACC=0
217	1 000C3	0F0003D8		XPSD,0	TRACC=1 I9=1
218	1 000C4	0F0003D8		XPSD,0	TRACC=2 I9=1
219	1 000C5	0F0003D8		XPSD,0	TRACC=3 I9=1
220	1 000C6	0F0003D8		XPSD,0	TRACC=4 I9=1
221	1 000C7	0F0003D8		XPSD,0	TRACC=5 I9=1
222	1 000C8	0F0003D8		XPSD,0	TRACC=6 I9=1
223	1 000C9	0F0003D8		XPSD,0	TRACC=7 I9=1
224	1 000CA	0F0003D8		XPSD,0	TRACC=8 I9=1
225	1 000CB	0F0003D8		XPSD,0	TRACC=9 I9=1
226	1 000CC	0F0003D8		XPSD,0	TRACC=10 I9=1
227	1 000CD	0F0003D8		XPSD,0	TRACC=11 I9=1
228	1 000CE	0F0003D8		XPSD,0	TRACC=12 I9=1
229	1 000CF	0F0003D8		XPSD,0	TRACC=13 I9=1
230	1 000D0	0F0003D8		XPSD,0	TRACC=14 I9=1
231	1 000D1	0F0003D8		XPSD,0	TRACC=15 I9=1

SUFFIX(2)
PAGE

232								
233								
234	1	000D2			BOUND 8			CALL 4 TRAP
235	1	000D2	00000000	A	CAL4	PZE		
236	1	000D3	00000000	A		PZE		
237	1	000D4	000000C6			PZE,0	\$+2	
238	1	000D5	00000000	A		PZE		
239	1	000D6	0F0003D8		CARET	XPSD,0	RETURN	TRACC=0
240	1	000D7	0F0003D8			XPSD,0	RETURN	TRACC=1 I9=1
241	1	000D8	0F0003D8			XPSD,0	RETURN	TRACC=2 I9=1
242	1	000D9	0F0003D8			XPSD,0	RETURN	TRACC=3 I9=1
243	1	000DA	0F0003D8			XPSD,0	RETURN	TRACC=4 I9=1
244	1	000DB	0F0003D8			XPSD,0	RETURN	TRACC=5 I9=1
245	1	000DC	0F0003D8			XPSD,0	RETURN	TRACC=6 I9=1
246	1	000DD	0F0003D8			XPSD,0	RETURN	TRACC=7 I9=1
247	1	000DE	0F0003D8			XPSD,0	RETURN	TRACC=8 I9=1
248	1	000DF	0F0003D8			XPSD,0	RETURN	TRACC=9 I9=1
249	1	000E0	0F0003D8			XPSD,0	RETURN	TRACC=10 I9=1
250	1	000E1	0F0003D8			XPSD,0	RETURN	TRACC=11 I9=1
251	1	000E2	0F0003D8			XPSD,0	RETURN	TRACC=12 I9=1
252	1	000E3	0F0003D8			XPSD,0	RETURN	TRACC=13 I9=1
253	1	000E4	0F0003D8			XPSD,0	RETURN	TRACC=14 I9=1
254	1	000E5	0F0003D8			XPSD,0	RETURN	TRACC=15 I9=1

SUFFIX(2)
PAGE

10

255										
256				*						PARITY INTERRUPT SERVICE ROUTINE
257	1	000E6			BOUND 8					
258	1	000E6	00000000 A	PARITY	PZE,0	0				
259	1	000E7	00000000 A		PZE,0	0				
260	1	000E8	000000EA		PZE,0	PARITY+4				
261	1	000E9	00000000 A		PZE,0	0				
262	1	000EA	6C400010 A		RD,4	X'10'				RECORD PARITY ERROR PLANS
263	1	000EB	2E000000 A		WAIT,0	0				
264	1	000EC	0E3003E0		LPSD,3	REPEAT				RELEASE PARITY INTERRUPT REPEAT TESTS
265				*						INPUT/OUTPUT TRAP
266	1	000EE			BOUND 8					
267	1	000EE	00000000 A	INOUT	PZE					
268	1	000EF	00000000 A		PZE					
269	1	000F0	00000CF2		PZE,0	8+2				
270	1	000F1	00000000 A		PZE					
271	1	000F2	6E000001 A		AIO,0	1				ACKNOWLEDGE INTERRUPT
272	1	000F3	0E3003DC		LPSD,3	I0REL				
273	1	000F4	703000EE		LCF	INOUT				
274	1	000F5	0F0003D8	I0RET	XPSD,0	RETURN				
275				*						INTERRUPT BUTTON SERVICE ROUTINE
276	1	000F6			BOUND 8					
277	1	000F6	00000000 A	RESET	PZE,0	0				
278	1	000F7	00000000 A		PZE,0	0				
279	1	000F8	000000FA		PZE,0	RESET+4				
280	1	000F9	00000000 A		PZE,0	0				
281	1	000FA	323003E7		LW,3	PASSES				RESET PASSES TO LAST SETTING
282	1	000FB	322003E6		LW,2	ERRORS				RESET ERRORS TO LAST SETTING
283	1	000FC	32100113		LW,1	SAVE				REINITIALIZE LOAD WITH LAST SETTING
284	1	000FD	2E000000 A		WAIT,0	0				
285	1	000FE	0E3003E0		LPSD,3	REPEAT				REPEAT LAST TEST
286	1	000FF	00000000 A		FILL	X'100'				

			SUFFIX(2)		
287			PAGE		
288	1	00100	32200360	START	LW,2 ZERO PRESET ERROR COUNT
289	1	00101	32300360		LW,3 ZERO PRESET PASS COUNT
290	1	00102	32500327		LW,5 PCPINT PARITY AND CONTROL PANEL INTERRUPT
291	1	00103	6D501200 A		WD,5 X'1200' ARM AND ENABLE
292	1	00104	32100319		LW,1 NEG51 INITIALIZE LINE COUNT
293	1	00105	3510031B		STW,1 LINE
294	1	00106	32100341		LW,1 NEG2
295	1	00107	3510031C		STW,1 FIRST
296	1	00108	321002FA	CYCLE	LW,1 LOAD RESET FIRST PASS COUNTER
297	1	00109	32400360		LW,4 ZERO PRESET LOAD FROM LIST
298	1	0010A	35100113		STW,1 SAVE
299	1	0010B	32500326		LW,5 NOTAE
300	1	0010C	6D501100 A		WD,5 X'1100' ALL BUT PARITY AND PANEL INTERRUPT
301	1	0010D	6D501500 A		WD,5 X'1500' DISARM
302	1	0010E	352003E6		STW,2 ERRORS DISABLE
303	1	0010F	353003E7		STW,3 PASSES
304	1	0C110	32600317		LW,6 NEG12
305	1	00111	354C03C4		STW,4 TABLE+12,6 STORE ZEROS IN TABLE
306	1	00112	65600111		BIR,6 \$-1
307	1	00113	32400610	SAVE	LW,4 LIST+C
308	1	00114	69300118		BCS,3 NOTEND TEST FOR MODULE END
309	1	00115	4B30030D		AND,3 M1G15 DELETE MODULE COUNT
310	1	00116	20310C00 A		AI,3 X'10000' INCREMENT PASS COUNT
311	1	00117	68000108		BCR,0 CYCLE PREPARE TO SET TABLE
312	1	00118	325002FB	NOTEND	LW,5 STORE
313	1	0C119	32600004 A		LW,6 4 COUNT
314	1	0011A	3510011C	MOVE	STW,1 FROM MOVE LIST TO TABLE
315	1	0011B	3550011D		STW,5 TO
316	1	0011C	32400610	FROM	LW,4 LIST+C
317	1	0011D	354003B8	TO	STW,4 TABLE+I
318	1	0011E	20100001 A		AI,1 1
319	1	0011F	20500001 A		AI,5 1
320	1	00120	6560011A		BIR,6 MOVE
321	1	00121	3510011C		STW,1 FROM
322	1	00122	32100318		LW,1 NEG16 SET MODULE POINTER = NEXT MODULE
323	1	00123	3242040C		LW,4 RT2 TO VRTR

362	1	0014A	68300174	
363	1	0014B	320002E5	
364	1	0014C	450002E5	
365	1	0014D	6800015F	
366	1	0014E	320003C6	
367	1	0014F	320002E1	
368	1	00150	458002E2	
369	1	00151	68000159	
370	1	00152	21000001	A
371	1	00153	68000159	
372	1	00154	22100002	A
373	1	00155	35000156	
374	1	00156	320003EB	
375	1	00157	35000004	A
376	1	00158	354002EF	
377	1	00159	321003C7	
378	1	0015A	3500015B	
379	1	0015B	320203EB	
380	1	0015C	3502053D	
381	1	0015D	6410015B	
382	1	0015E	68000174	
383	1	0015F	322003C6	
384	1	00160	3200030A	
385	1	00161	321003C7	
386	1	00162	68300168	
387	1	00163	72D40000	A
388	1	00164	75D60000	A
389	1	00165	20200001	A
390	1	00166	20000001	A
391	1	00167	64100163	
392	1	00168	324003C2	
393	1	00169	354002EF	
394	1	0016A	324003C3	
395	1	0016B	329002E6	
396	1	0016C	458002F7	
397	1	0016D	68300170	
398	1	0016E	450002E8	
399	1	0016F	69000171	

SUFFIX(2)

BCR,3	SETSHT
LB,9	BYTENS
CR,8	BYTENS
BCR,3	BYTEST
LW,5	TABLE+14
LW,9	MSK2B
CS,8	MSK09
BCR,3	\$+8
CI,1	1
BCR,2	\$+6
LI,1	2
STW,5	\$+1
LB,13	RT1-1,1
STW,13	4
STW,4	INDX
LW,1	TABLE+15
STW,5	\$+1
LW,13	RT1-1,1
STW,13	VRFR-1,1
BDR,1	\$-2
BCR,0	SETSHT
LW,2	TABLE+14
LW,3	VNTRCH
LW,1	TABLE+15
BCR,3	\$+6
LB,13	0,2
STB,13	0,3
AI,2	1
AI,3	1
BCR,1	\$-4
LW,4	TABLE+10
STW,4	INDX
LW,4	TABLE+11
LW,9	INSMK
CS,8	EBSINS
BCR,3	\$+3
CS,8	TBSINS
BCS,3	\$+2

BRANCH IF BYTE INSTRUCTION
STORE CHANGES TO ATR(0000FE) STA

SKIP IF PSW

BRANCH IF NOT OVER 1

LB,13 RT1-1,1 RT3-1,1

CHANGES TO VINTR-BYTE CHANGING

BRANCH IF EBS

BRANCH IF NOT TDS

			SUFFIX(2)			
400	1	00170	326002EC	LW,6	SHT1RT	SHORT 1 RETURN IF EDS OR TDS
401	1	00171	458002E9	CS,8	CBSINS	
402	1	00172	69300174	BCS,3	\$+2	
403	1	00173	327002EA	LW,7	CBSLW	PRESET CHANGE IF CBS
404	1	00174	3540053F	STW,4	VRTR+1	
405	1	00175	35600278	STW,6	SHTRET	STORE SHORT 1 OR SHORT 2 RETURN
406	1	00176	357C0269	STW,7	VHTCH	STORE VMT CHANGE OR BRANCH
407	1	00177	32C003B9	LW,12	TABLE+1	INSTRUCTION
408	1	00178	35C003CD	STW,12	INSTR	
409	1	00179	32C00047	LW,12	XPSD	XPSD 0, RETURN
410	1	0017A	35C00275	STW,12	LOC+1	
411	1	0017B	32C003CE	LW,12	IA	INDIRECT ADDRESS
412	1	0017C	35C002EB	STW,12	WKIA	
413	1	0017D	32800308	LW,8	LOC2AD	
414	1	0017E	358003DA	STW,8	RETURN+2	SET RETURN TO LOC+2
415	1	0017F	324003BA	LW,4	TABLE+2	PSW1 IN
416	1	00180	4B40030C	AND,4	LINKADD	SELECT LINK ADDRESS AND DELETE MS.
417	1	00181	354003E2	STW,4	PSW1	
418	1	00182	32500360	LW,5	ZERO	CLEAR R5
419	1	00183	4B40030B	AND,4	COND	CLEAR ADDRESS PORTION OF PSW1
420	1	00184	0E0003E2	LPSD,0	PSW1	LINKAGE

SETSHT

→244

SUFFIX(2)
PAGE

421
 422
 423 1 00185 32500310
 424 1 00186 4850032B
 425 1 00187 35500040
 426 1 00188 4840032C
 427 1 00189 35400060
 428 1 0018A 68000244
 429
 430 1 0018B 4840032D
 431 1 0018C 35400074
 432 1 0018D 68000244
 433
 434 1 0018E 4840032E
 435 1 0018F 3540007A
 436 1 00190 68000244
 437
 438 1 00191 4840032F
 439 1 00192 35400080
 440 1 00193 68000244
 441
 442 1 00194 48400330
 443 1 00195 35400086
 444 1 00196 6800018C
 445
 446 1 00197 48400331
 447 1 00198 3540008C
 448 1 00199 6800018C

*
 SI9NA0 LW,5 I9
 RI9NA0 EOR,5 NA0XD
 STW,5 NA0TR
 EOR,4 NA0AD
 STW,4 NA0+2
 BCR,0 SETPSW
 *
 UIISW EOR,4 UIIAD
 STW,4 UII+2
 BCR,0 SETPSW
 *
 SLSW EOR,4 SLAD
 STW,4 SL+2
 BCR,0 SETPSW
 *
 EXP0SW EOR,4 FXPAD
 STW,4 FXP0+2
 BCR,0 SETPSW
 *
 FLPSW EOR,4 FLPFAD
 STW,4 FLPF+2
 BCR,0 UIISW+1
 *
 DFSW EOR,4 DFAD
 STW,4 DF+2
 BCR,0 UIISW+1

SET XPSD FOR NA0 TRAP
 SET I9=1
 SET I9=0

SET PSW1 BITS 0-11 FOR NA0 TRAP

SET PSW1 BITS 0-11 FOR UII TRAP

SET PSW1 BITS 0-11 FOR SL TRAP

SET PSW1 BITS 0-11 FOR EXP0 TRAP

SET PSW1 BITS 0-11 FOR FLPF TRAP

SET PSW1 BITS 0-11 FOR DF TRAP

482
483

1	001B5	00000000	A
1	001B6	00000000	A
1	001B7	00000000	A
1	001B8	00000000	A
1	001B9	00000000	A
1	001BA	00000000	A
1	001BB	00000000	A
1	001BC	00000000	A
1	001BD	00000000	A
1	001BE	00000000	A
1	001BF	00000000	A
1	001C0	00000000	A
1	001C1	00000000	A
1	001C2	00000000	A
1	001C3	00000000	A
1	001C4	00000000	A
1	001C5	00000000	A
1	001C6	00000000	A
1	001C7	00000000	A
1	001C8	00000000	A
1	001C9	00000000	A
1	001CA	00000000	A
1	001CB	00000000	A
1	001CC	00000000	A
1	001CD	00000000	A

SUFFIX(2)
PAGE
FILL X'1CE'

484
485
486
487
488
489
490
491
492
493
494

1	001CE		
1	001CE	00000000	A
1	001CF	00000000	A
1	001D0	000001D2	
1	001D1	00000000	A
1	001D2	32800006	A
1	001D3	48800007	A
1	001D4	693001D6	
1	001D5	0E0001CE	
1	001D6	652001D7	

ERROR

TSTDVC

BOUND 8
PZE
PZE
PZE,0 ERROR??
PZE
LW,8 6
EOR,8 7
BCS,3 TSTDVC
LPSD,0 ERROR
BIR,2 \$+1

ERROR LEVEL 0

PICK UP RESULT
COMPARE WITH PREDETERMINED RESULT
DIFF 0

INCREMENT ERROR COUNTER

495	1	001D7	60000041	A
496	1	001D8	40000001	A
497	1	001C9	4000030E	
498	1	001DA	600001E1	
499	1	001DB	4000030E	
500	1	001DC	600001D8	
501	1	001DD	60000000	A
502	1	001DE	601001E0	
503	1	0C1DF	20000000	A
504	1	001E0	600001CE	

SUFFIX(2)

WD,0	X'411
T10,11	TYPE
AND,11	603
BCR,3	EDIT
EOR,11	603
BCR,3	5-4
RD,0	0
BCS,1	NOHALT
WAIT	
LPSD,0	ERROR

HLTEST

NOHALT

18

TURN ON ALARM
 PREPARE FOR TYPEWRITER USAGE
 SELECT TYPEWRITER STATUS INFO
 TYPEWRITER READY

TYPEWRITER BUSY
 KEAD SENSE SWITCHES

COMMON ERROR HALT

203

SUFFIX(2)
PAGE

505							
506							
507	1	001E1	32C002FC	EDIT	LW,12	STRMC1	EDIT-LEVEL 3
508	1	001E2	32900315		LW,9	NEG8	SET STORAGE WORD
509	1	001E3	32A00005 A		LW,10	5	
510	1	001E4	693001E6		BCS,3	REPORT+1	TEST R5=0
511	1	001E5	3290033F	REPORT	LW,9	NEG4	DISPLAY
512	1	001E6	32A002F5		LW,10	L0ADR	
513	1	001E7	35A001E8		STW,10	LDREG	SET REGISTER PICKUP
514	1	001E8	32A00001 A	LDREG	LW,10	1	PICK UP REGISTER 1 THRU 8(DIG) ON A
515	1	001E9	35A003E8		STW,10	WORD	
516	1	001EA	359003E9		STW,9	COUNT	
517	1	001EB	32F00341		LW,15	NEG2	
518	1	001EC	320002F6		LW,0	LWN	SET WORD COUNT(N)=2
519	1	001ED	32A002F8		LW,10	LWFRH	SET N=4, 4
520	1	001EE	32B002F0		LW,11	ANFR0	SET FRAME = F0F0F0F0, F0F0F0F0
521	1	001EF	4B900313		AND,9	ONE	SET CHAR POSITIONS 1234,5678
522	1	001F0	693001F5		BCS,3	ODD	TEST COUNT EVEN
523	1	001F1	32F00314	EVEN	LW,15	NEG3	
524	1	001F2	320002F7		LW,0	LWN2	SET WORD COUNT(N)=3
525	1	001F3	32A002F9		LW,10	LWFRH2	SET N= 2, 1, 2
526	1	001F4	32B002F1		LW,11	ANFR2	SET FRAME=40F0F0F0, F0F0F0F0, F0F0F0F0
527	1	001F5	32E002F2	ODD	LW,14	ANR4	SET CHAR POSITIONS 12,345,78
528	1	001F6	350001F9	WORDS	STW,0	SETN	SET DIGIT PICK = BITS 0-3, 4-7, ETC
529	1	001F7	35A001FA		STW,10	SETFRH	SET NUMBER PICK UP
530	1	001F8	35C00215		STW,12	STRND	SET FRAME PICK UP
531	1	001F9	3200033F	SETN	LW,0	NUMBER	SET STORAGE LOCATION
532	1	001FA	32D00344	SETFRH	LW,13	FRAME	SET NUMBER
533	1	001FB	35B00210	BYTES	STW,11	ANDE	SET FRAME (FINAL CHAR BYTE POSITION)
534	1	001FC	35D003EB		STW,13	WKO	SET FILTER
535	1	001FD	35E00205		STW,14	ANDM	SAVE FRAME
536	1	001FE	32A002F3		LW,10	LWBIY	SET MASK (ORIGINAL CHAR POSITION)
537	1	001FF	32C0033F		LW,12	NEG4	SET BIT PICK UP
538	1	00200	32D00350		LW,13	ZERO	SET BIT COUNT
							SET CHAR GENERATOR TO ZERO

			SUFFIX(2)			
			PAGE			
539						
540	1	00201	35A00203	BITS	STW,10	LWB
541	1	00202	35A00208		STW,10	LWBT
542	1	00203	3290033B	LWB	LW,9	BIT
543	1	00204	4B9003E8		AND,9	WORD
544	1	00205	4B900349	ANDM	AND,9	MASK
545	1	00206	69300208		BCS,3	LWBT
546	1	00207	0E0003DE		LPSD,0	BUMP
547	1	00208	3290033B	LWBT	LW,9	BIT
548	1	00209	48D00009 A		EOR,13	9
549	1	0020A	65A0020B	BUMPER	BIR,10	\$+1
550	1	0020B	65C00201		BIR,12	BITS
551	1	0020C	4BD00350		AND,13	F
552	1	0020D	48D002F4		EOR,13	LWBYTE
553	1	0020E	35D0020F		STW,13	LWBY
554	1	0020F	32D00360	LWBY	LW,13	BYTE
555	1	00210	4BD00351	ANDF	AND,13	FILTER
556	1	00211	48D003EB		EOR,13	WKO
557	1	00212	65B00213		BIR,11	\$+1
558	1	00213	65E00214		BIR,14	\$+1
559	1	00214	650001FB		BIR,0	BYTES
560	1	00215	35D003A4	STRWD	STW,13	IMAGE+1
561	1	00216	320001F9		LW,0	SETN
562	1	00217	32A001FA		LW,10	SETFRM
563	1	00218	32C00215		LW,12	STRWD
564	1	00219	6500021A		BIR,0	\$+1
565	1	0021A	65A0021B		BIR,10	\$+1
566	1	0021B	65C0021C		BIR,12	\$+1
567	1	0021C	65F001F6		BIR,15	WORDS
568	1	0021D	329003E9		LW,9	COUNT
569	1	0021E	32A001E8		LW,10	LDREG
570	1	0021F	65A00220		BIR,10	\$+1
571	1	00220	659001E7		BIR,9	LDREG-1

PICK UP BIT 1,2,4OR 8
 (BIT)*(WORD)
 (BIT)*(WORD)*(MASK)
 LWBT IF WORD HAS BIT IN MASK POSITION

PICK UP BIT
 MERGE BIT WITH CHAR BEING CONSTRUCTED
 INCREMENT BIT PICK UP
 CONTINUE CHAR CONSTRUCTION
 PICK OF BINARY 4 BIT CHAR.
 CONSTRUCT BYTE PICK UP WORD

PICK UP CONVERTED BYTE
 SELECT DESIRED TYTE
 MERGE BYTE INTO CONVERTED WORD
 INCREMENT CONVERTED BYTE POSITION
 INCREMENT SELECTED CHAR POSITION
 TEST FOR N CHAR'S
 STORE CONVERTED WORD
 SET NUMBER PICK UP
 SET FRAME PICK UP
 PICK UP STORE WORD
 INCREMENT NUMBER PICK UP
 INCREMENT FRAME PICK UP
 INCREMENT STORE LOCATION
 INCREMENT M - BRANCH IF M NOT ZERO
 SET COUNT
 SET REGISTER PICK UP
 INCREMENT REGISTER PICK UP
 BRANCH TO LDREG IF COUNT NOT ZERO

572
 573
 574 1 00221 32F0031B
 575 1 00222 32E0031C
 576 1 00223 65E00227
 577 1 00224 65F0022C
 578 1 00225 32000370
 579 1 00226 0F000238
 580 1 00227 32000371
 581 1 00228 0F000238
 582 1 00229 32000372
 583 1 0022A 0F000238
 584 1 0022B 32F00319
 585 1 0022C 32000374
 586 1 0022D 32A00005 A
 587 1 0022E 69300230
 588 1 0022F 32000373
 589 1 00230 0F000238
 590 1 00231 35F0031B
 591 1 00232 35E0031C
 592 1 00233 6D000040 A
 593 1 00234 0E000236
 594 1 00236
 595 1 00236 003001DD
 596 1 00237 00000000 A

SUFFIX(2)
 PAGE
 LW,15 LINE
 LW,14 FIRST
 BIR,14 SKIP6
 BIR,15 LINOUT
 LW,0 PSIXCR
 XPSD,0 PRINT
 LW,0 PTITLE
 XPSD,0 PRINT
 LW,0 PHEAD
 XPSD,0 PRINT
 LW,15 NEG51
 LINOUT LW,0 PLONGL
 LW,10 5
 BCS,3 IO
 LW,0 PSHRTL
 XPSD,0 PRINT
 IO
 STW,15 LINE
 STW,14 FIRST
 WD,0 X'40'
 LPSD,0 GETOUT
 BOUND 8
 GETOUT PZE,3 HLTEST
 PZE

OUTPUT-LEVEL 3
 LINE COUNT (-51)
 =-2 FIRST TIME
 FIRST TIME THRU
 LINE COUNT NOT ZERO
 OUTPUT 6 CAR. RET.
 NEW PAGE TITLE
 NEW HEADING
 RESET LINE COUNT
 SET UP DISPLAY PRINTOUT
 PICK UP IDENTIFIER
 SET UP RECORD PRINTOUT
 SAVE NEW LINE COUNT
 SAVE NEW FIRST PASS INDICATOR
 TURN OFF ALARM

not done
not done

597
 598
 599 1 00238
 600 1 00238
 601 1 00239
 602 1 0023A
 603 1 0023B
 604 1 0023C
 605 1 0023D
 606 1 0023E
 607 1 0023F
 608 1 00240
 609 1 00241
 610 1 00242
 611 1 00243

00000000 A
 00000000 A
 0000023C
 00000000 A
 4CB00001 A
 4BB0030E
 65300243
 48B0030E
 6830023C
 4FB00001 A
 2E000000 A
 0E000238 ←

PRINT
 BUSY
 EXITIO

SUFFIX(2)
 PAGE

BOUND 8
 PZE
 PZE
 PZE,0 PRINT+4
 PZE
 SI0,11 TYPE
 AND,11 6Q3
 BCR,3 EXITIO
 EOR,11 6Q3
 BCR,3 BUSY
 HI0,11 TYPE
 WAIT
 LPSD,0 PRINT

PRINT LEVEL 4

SELECT TYPEWRITER FOR OUTPUT
 READY
 BUSY
 UNAVAILABLE OR INOPERATIVE

231

612
613 1 00244 324003BA
614 1 00245 4B40030B
615 1 00246 48400309
616 1 00247 354003E2
617 1 00248 32100318
618 1 00249 32D2040C
619 1 0024A 35D2053E
620 1 0024B 65100249
621 1 0024C 32C003BD
622 1 0024D 32D003C1
623 1 0024E 15C0054A
624 1 0024F 3210031A
625 1 00250 32C003C4
626 1 00251 35C00252
627 1 00252 3272045C
628 1 00253 3572058E
629 1 00254 65100252
630 1 00255 320003EC
631 1 00256 122003EE
632 1 00257 124003F0
633 1 00258 126003F2
634 1 00259 128003F4
635 1 0025A 12A003F6
636 1 0025B 12E003FA
637 1 0025C 122003FE
638 1 0025D 12400400
639 1 0025E 12600402
640 1 0025F 12800404
641 1 00260 12A00406
642 1 00261 12E0040A
643 1 00262 320003BE
644 1 00263 3210031A
645 1 00264 32D00305
646 1 00265 35D00266
647 1 00266 32D2045C
648 1 00267 35D205D0

SUFFIX(2)
PAGE
SETPSW LW,4 TABLE+2
AND,4 COND
EOR,4 L0CADD
STW,4 PSW1
LW,1 NEG16
LW,13 RT2+16,1
STW,13 VRT+16,1
BIR,1 \$-2
LW,12 TABLE+5
LW,13 TABLE+9
STD,12 VRTR+12
LW,1 NEG64
LW,12 TABLE+12
STW,12 \$+1
LW,7 MT1+64,1
STW,7 FMT+64,1
BIR,1 \$-2
LW,0 RT1
LD,2 RT1+2
LD,4 RT1+4
LD,6 RT1+6
LD,8 RT1+8
LD,10 RT1+10
LD,14 RT1+14
LD,2 RT2+2
LD,4 RT2+4
LD,6 RT2+6
LD,8 RT2+8
LD,10 RT2+10
LD,14 RT2+14
LW,0 TABLE+6
SHORT1 LW,1 NEG64
LW,13 TABLE+13
STW,13 \$+1
LW,13 MT1+64,1
STW,13 VMT+64,1

CLEAR ADDRESS
SET ADDRESS TO L0C

RT2 TO BRT

R120 AND R130 TO VRTR

PRESET FMT

LW,7 MT1+64,1+DP000,1

LOAD
RT1 TABLE
IF
STW
PSM
OR PSW

PRESET VMT

LW,13 MT1+64,1/EP1+64,1

SHORT2

649	1	00268	65100266
650	1	00269	321003C7
651	1	0026A	32D203C7
652	1	0026B	35D2058F
653	1	0026C	6410028A
654	1	0026D	32C003DE
655	1	0026E	32D00302
656	1	0026F	15C00304
657	1	00270	321002EF
658	1	00271	32C0038C
659	1	00272	32D00300
660	1	00273	0E000322
661	1	00274	67D0030D
662	1	00275	0F00030B
663	1	00276	6C000000
664	1	00277	68800262
665	1	00278	68000263
666	1	00279	3500052E
667	1	0027A	3510052F
668	1	0027B	15200530
669	1	0027C	15400532
670	1	0027D	15600534
671	1	0027E	15800536
672	1	0027F	15A00538
673	1	00280	15C0053A
674	1	00281	15E0053C
675	1	00282	321000F8
676	1	00283	351003DA
677	1	00284	32100118
678	1	00285	32200386
679	1	00286	32300387
680	1	00287	324002B9
681	1	00288	3250031D
682	1	00289	32700389
683	1	0028A	3240030D
684	1	0028B	0F00010E
685	1	0028C	3250031E
686	1	0028D	32700317

SUFFIX(2)

VMTCH	BIR,1	\$-2
	LW,1	TABLE+15
	LW,13	TABLE+15,1
	STW,13	VMT-1,1
	BDR,1	\$-2
SHORT2	LW,12	TABLE+6
	LW,13	TABLE+10
	STD,12	MEMORY
	LW,1	INDEX
	LW,12	TABLE+4
	LW,13	TABLE+8
	LPD,0	PSW
LOC	EXU	INSTR
	XPSD,0	RETURN
	RD,0	0
	BER,8	TESTS
SHORT1	BER,0	SHORT1
STRG	STW,0	VRT
	STW,1	VRT+1
	STD,2	VRT+2
	STD,4	VRT+4
	STD,6	VRT+6
	STD,8	VRT+8
	STD,10	VRT+10
	STD,12	VRT+12
	STD,14	VRT+14
TESTS	LW,1	RESET
	STW,1	RETURN
	LW,1	SAVE
	LW,2	ERRORS
	LW,3	PASSES
	LW,4	TABLE+1
	LW,5	INSTID
	LW,7	TABLE+1
	LW,6	INDEX
	XPSD,0	ERR
	LW,5	XPSD
	LW,7	XPSD

(LW,1 TABLE+15)/BCR,1

MEMORY 1 IN
MEMORY 2 IN.

R12/R1 IN
R12 IN
R13 IN

EXECUTE INSTRUCTION

READ BREAKPOINTS
BRANCH IF BREAKPOINT 1 RESET
BCR,0 SHORT1/SHORT2

SET RETURN TO BERT+2
LIST POINTER
ERROR COUNT
NUMBER OF PASSES
INSTRUCTION
TEST INSTRUCTION

TEST XPSD

687	1	0028E	32600275
688	1	0028F	0F0001CE
689	1	00290	3250031F
690	1	00291	327003CE
691	1	00292	326002EB
692	1	00293	0F0001CE
693	1	00294	32500320
694	1	00295	3270053F
695	1	00296	3260052F
696	1	00297	0F0001CE
697	1	00298	32500321
698	1	00299	327003BB
699	1	0029A	326003D8
700	1	0029B	0F0001CE
701	1	0029C	20500001 A
702	1	0029D	327003E3
703	1	0029E	326003D9
704	1	0029F	0F0001CE
705	1	002A0	32100318
706	1	002A1	32500322
707	1	002A2	351003EA
708	1	002A3	3272054E
709	1	002A4	3262053E
710	1	002A5	32100313
711	1	002A6	0F0001CE
712	1	002A7	20500001 A
713	1	002A8	321003EA
714	1	002A9	65100272
715	1	002AA	3280031D
716	1	002AB	32900225
717	1	002AC	45800225
718	1	002AD	683002B7
719	1	002AE	32100313
720	1	002AF	32500323
721	1	002B0	327003FF
722	1	002B1	326003D4
723	1	002B2	0F0001CE
724	1	002B3	20500001 A

STCNT1

SUFFIX(2)	
LW,6	LOC+1
XPSD,0	ERROR
LW,5	IAID
LW,7	IA
LW,6	WKIA
XPSD,0	ERROR
LW,5	IXID
LW,7	VRTR+1
LW,6	VRT+1
XPSD,0	ERROR
LW,5	PSDWID
LW,7	TABLE+3
LW,6	RETURN
XPSD,0	ERROR
AI,5	1
LW,7	PSW2
LW,6	RETURN+1
XPSD,0	ERROR
LW,1	NEG16
LW,5	REGID
STR,1	SVECHT
LW,7	VRTR+16,1
LW,6	VRT+16,1
LW,1	SAVE
XPSD,0	ERROR
AI,5	1
LW,1	SVECHT
BIR,1	STCNT1
LW,8	TABLE+1
LW,9	BYTINS
CS,8	BYTINS
BCR,3	TSTCNT
LW,1	SAVE
LW,5	MEMID
LW,7	TABLE+7
LW,6	MEMORY
XPSD,0	ERROR
AI,5	1

TEST INDIRECT ADDRESSING

TEST INDEXING

TEST PCOM1

TEST PCOM2

TEST REGISTER

BRANCH BY UNIT INSTRUCTION

TEST MEMORY HOLD 1

TEST MEMORY HOLD 2

725 1 002B4 327003C3
 726 1 002B5 326003D5
 727 1 002B6 0F0001CE
 728 1 002B7 3210031A
 729 1 002B8 32C003C4
 730 1 002B9 35C002BC
 731 1 002BA 32500324
 732 1 002BB 351003EA
 733 1 002BC 3272045C
 734 1 002BD 3262058E
 735 1 002BE 32100113
 736 1 002BF 0F0001CE
 737 1 002C0 20500001 A
 738 1 002C1 321003EA
 739 1 002C2 651002BB
 740 1 002C3 3210031A
 741 1 002C4 32500325
 742 1 002C5 351003EA
 743 1 002C6 32720610
 744 1 002C7 326205D0
 745 1 002C8 32100113
 746 1 002C9 0F0001CE
 747 1 002CA 20500001 A
 748 1 002CB 321003EA
 749 1 002CC 651002C5
 750 1 002CD 32500360
 751 1 002CE 32600307
 752 1 002CF 356001CE
 753 1 002D0 32100113
 754 1 002D1 6C000000 A
 755 1 002D2 692001D8
 756 1 002D3 6D000040 A
 757 1 002D4 653002D5
 758 1 002D5 6C000000 A
 759 1 002D6 69400109
 760 1 002D7 3210011C
 761 1 002D8 0E0003E0

TSTFMT

STCNT2

TSTVMT

STCNT3

END

SUFFIX(2)
 LW,7 TABLE+11
 LW,6 MEMORY+1
 XPSD,0 ERROR
 LW,1 NEG64
 LW,12 TABLE+12
 STW,12 \$+3
 LW,5 FMTID
 STW,1 SVECNT
 LW,7 MT1+64,1
 LW,6 FMT+64,1
 LW,1 SAVE
 XPSD,0 ERROR
 AI,5 1
 LW,1 SVECNT
 BIR,1 STCNT2
 LW,1 NEG64
 LW,5 VMTID
 STW,1 SVECNT
 LW,7 VMTR+64,1
 LW,6 VMT+64,1
 LW,1 SAVE
 XPSD,0 ERROR
 AI,5 1
 LW,1 SVECNT
 BIR,1 STCNT3
 LW,5 ZERO
 LW,6 RETEND
 STW,6 ERROR
 LW,1 SAVE
 RD,0 0
 BCS,2 TSTDVCR2
 WD,0 X'40'
 BIR,3 \$+1
 RD,0 0
 BCS,4 CYCLE+1
 LW,1 FROM
 LPSD,0 MODULE

TEST FMT

LW,7 MT1+64,1/DP+64,1

TEST VMT

SET IDENTIFIER FOR REPORT
SET RETURN FROM OUTPUT=END

SS3 # REPORT
TURN OFF ALARM
INCREMENT MODULE COUNTER
READ SENSE SWITCHES

RESTORE NEW LIST ADDRESS
UPDATEN POINTER

SUFFIX(2)
PAGE

CONSTANTS AND WORKING STORAGE

762								
763								
764	1	002D9	00000000	A	MEM210	DATA	0,-1	
	1	002DA	FFFFFFFF	A				
765	1	002DB	0000041C		LWMT12	PZE,0	HT1	
766	1	002DC	0000045C			PZE,0	MT2	
767	1	002DD	6880027A		STRGP1	BCR,8	STRG+1	
768	1	002DE	68800282		BCRTST	BCR,8	TESTS	
769	1	002DF	68800279		STOREG	BCR,8	STRG	
770	1	002E0	00000000	A	STMSK	J	11,0	
771	1	002E1	2B000000	A	MSK2B	J	X'2B',0	
772	1	002E2	09000000	A	MSK09	J	9,0	
773	1	002E3	7E000000	A	CVMASK	DATA	X'7E000000'	
774	1	002E4	28000000	A	CVINST	DATA	X'28000000'	
775	1	002E5	40000000	A	BYTINS	DATA	X'40000000'	
776	1	002E6	7F000C00	A	INSMSK	DATA	X'7F000C00'	
777	1	002E7	63000000	A	EBSINS	DATA	X'63000000'	
778	1	002E8	41000000	A	TBSINS	DATA	X'41000000'	
779	1	002E9	60000000	A	CBSINS	DATA	X'60000000'	
780	1	002EA	321003C7		CBSLW	LW,1	TABLE+15	
781	1	002EB	00000000	A	WKIA	PZE		
782	1	002EC	68000263		SHT1RT	BCR,0	SHORT1	
783	1	002ED	6800026D		SHT2RT	BCR,0	SHORT2	
784	1	002EE	00020000	A	IXMSK	DATA	X'20000'	
785	1	002EF	00000000	A	INDX	PZE		
786	1	002F0	4BD00351		ANFR0	AND,13	FILTER	PICK OFF CONVERTED BYTE
787	1	002F1	4BC00353		ANFR2	AND,13	FILTER+2	
788	1	002F2	4B900349		ANMK	AND,9	MASK	PICK OFF CHAR. TO BE CONVERTED
789	1	002F3	3290033B		LWBIT	LW,9	BIT	PICK UP MARCHING BITS
790	1	002F4	32D00360		LWBYTE	LW,13	BYTE	SELECT BYTE
791	1	002F5	32A00001	A	LOADR	LW,10	1	PICK UP REGISTERS FOR OUTPUT
792	1	002F6	3200033F		LWN	LW,0	NUMBER	PICK UP NUMBERS
793	1	002F7	32000341		LWN2	LW,0	NUMBER+2	
794	1	002F8	32D00344		LWFRM	LW,13	FRAME	PICK UP FRAMES
795	1	002F9	32D00346		LWFRM2	LW,13	FRAME+2	
796	1	002FA	32400610		LOAD	LW,4	LIST	LOAD TRANSFER VEHICAL WITH DATA MODL
797	1	002FB	354003B8		STORE	STW,4	TABLE	SET TABLE

			SUFFIX(2)			
798	1	002FC	35D003A4	STRMG1	STW,13	IMAGE+1
799	1	002FD	324003ED	LWRT1	LW,4	RT1+1
800	1	002FE	324003FD	LWRT2	LW,4	RT2+1
801	1	002FF	68000263	LDRT1	BCR,0	SHORT1
802	1	00300	122003FE	LDRT2	LD,2	RT2+2
803	1	00301	00000590	IACV	PZE,0	VMT
804	1	00302	00C003D4	IAPUPU	PZE,0	MEMORY
805	1	00303	0000052E	IASTM	PZE,0	VRT
806	1	00304	0000040C	IALM	PZE,0	RT3
807	1	00305	40000000 A	LMSTM	DATA	X'40000000'
808	1	00306	49000000 A	STM	DATA	X'49000000'
809	1	00307	000002D3	RETEND	PZE,0	END
810	1	00308	00000276	L0C2AD	PZE,0	L0C+2
811	1	00309	00000274	L0CADD	PZE,0	L0C
812	1	0030A	00001740	VMTRCH	J	0,BA(VMTR)
813	1	0030B	FFF00000 A	C0ND	DATA	X'FFF00000'
814	1	0030C	FF3FFFFFF A	LINKADD	DATA	X'FF3FFFFFF'
815	1	0030D	FFFF0000 A	M1015	DATA	X'FFFF0000'
816	1	0030E	60000000 A	603	DATA	X'60000000'
817	1	0030F	20000000 A	203	DATA	X'20000000'
818	1	00310	00400000 A	I9	DATA	X'400000'
819	1	00311	00B0B000 A	SEP	DATA	X'B0B000'
820	1	00312	0000000C A	CEE	DATA	12
821	1	00313	00000001 A	ONE	DATA	1
822	1	00314	FFFFFFFD A	NEG3	DATA	-3
823	1	00315	FFFFFFF8 A	NEG8	DATA	-8
824	1	00316	FFFFFFF6 A	NEG10	DATA	-10
825	1	00317	FFFFFFF4 A	NEG12	DATA	-12
826	1	00318	FFFFFFF0 A	NEG16	DATA	-16
827	1	00319	FFFFFFCD A	NEG51	DATA	-51
828	1	0031A	FFFFFFC0 A	NEG64	DATA	-64
829	1	0031B	FFFFFFCD A	LINE	DATA	-51
830	1	0031C	FFFFFFFE A	FIRST	DATA	-2
831	1	0031D	10000000 A	INSTID	I	1,0
832	1	0031E	20000275	XPSDID	I	2,L0C+1
833	1	0031F	30000000 A	IAID	I	3,0
834	1	00320	40000001 A	IXID	I	4,1
835	1	00321	50000001 A	PSDWID	I	5,1

RETURN TO END

INSTRUCTION
LOCATION+1
INDIRECT ADDRESS
INDEX
PROGRAM STATUS DOUBLEWORD

					SUFFIX(2)	REGISTERS	
836	1	00322	60000000	A	REGID	I 6,0	REGISTERS
837	1	00323	700003D4		MEMID	I 7, MEMORY	MEMORY WORDS
838	1	00324	7100054E		FMTID	CB,0 FMT	
839	1	00325	72000590		VMTID	LB,0 VMT	
840	1	00326	0000F7EF	A	NOTAE	DATA X'F7EF'	
841	1	00327	00000810	A	PCPINT	DATA X'810'	
842	1	00328	00000020	A	I0INT	DATA X'20'	
843	1	00329	00C000F4		I0AD	PZE,0 I0RET-1	
844	1	0032A	000001EA		MEMAD	P MEMORY	
845	1	0032B	0F00005E		NA0XD	XPSD,0 NA0	
846	1	0032C	00000062		NA0AD	PZE,0 NA0+4	
847	1	0032D	00000076		UIIAD	PZE,0 UII+4	
848	1	0032E	0000007C		SLAD	PZE,0 SL+4	
849	1	0032F	00000082		FXP0AD	PZE,0 FXP0+4	
850	1	00330	00000088		FLPFAD	PZE,0 FLPF+4	
851	1	00331	0000008E		DFAD	PZE,0 DF+4	
852	1	00332	00000094		WDTRAD	PZE,0 WDTR+4	
853	1	00333	0F000096		CAL1XD	XPSD,0 CAL1	
854	1	00334	0F0000AA		CAL2XD	XPSD,0 CAL2	
855	1	00335	0F0000BE		CAL3XD	XPSD,0 CAL3	
856	1	00336	0F0000D2		CAL4XD	XPSD,0 CAL4	
857	1	00337	0000009A		CAL1AD	PZE,0 CAL1+4	
858	1	00338	000000AE		CAL2AD	PZE,0 CAL2+4	
859	1	00339	000000C2		CAL3AD	PZE,0 CAL3+4	
860	1	0033A	000000D6		CAL4AD	PZE,0 CAL4+4	
861	1	0033B	11111111	A	BIT	DATA X'11111111'	
862	1	0033C	22222222	A		DATA X'22222222'	
863	1	0033D	44444444	A		DATA X'44444444'	
864	1	0033E	88888888	A		DATA X'88888888'	
865	1	0033F	FFFFFFFF	A	NUMBER	DATA -4, -4, -2, -4, -2	
	1	00340	FFFFFFFF	A			
	1	00341	FFFFFFFE	A			
	1	00342	FFFFFFFC	A			
	1	00343	FFFFFFFE	A			
866	1	00344	F0F0F0F0	A	FRAME	DATA X'F0F0F0F0'	2 WORDS
867	1	00345	F0F0F0F0	A		DATA X'F0F0F0F0'	
868	1	00346	4040F0F0	A		DATA X'4040F0F0'	3 WORDS
869	1	00347	F0F0F0F0	A		DATA X'F0F0F0F0'	

870	1	00348	F0F04040	A
871	1	00349	F0000000	A
872	1	0034A	0F000000	A
873	1	0034B	00F00000	A
874	1	0034C	000F0000	A
875	1	0034D	0000F000	A
876	1	0034E	00000F00	A
877	1	0034F	000000F0	A
878	1	00350	0000000F	A

MASK

	SUFFIX(2)
DATA	X'F0F04040'
DATA	X'F0000000'
DATA	X'0F000000'
DATA	X'00F00000'
DATA	X'000F0000'
DATA	X'0000F000'
DATA	X'00000F00'
DATA	X'000000F0'
DATA	X'0000000F'

				SUFFIX(2)	
879				PAGE	
880	1	00351	FF000000 A	FILTER	DATA X'FF000000'
881	1	00352	00FF0000 A		DATA X'FF00000'
882	1	00353	0000FF00 A		DATA X'FF000'
883	1	00354	000000FF A		DATA X'FF'
884	1	00355	FF000000 A		DATA X'FF000000'
885	1	00356	00FF0000 A		DATA X'FF00000'
886	1	00357	0000FF00 A		DATA X'FF000'
887	1	00358	000000FF A		DATA X'FF'
888	1	00359	FF000000 A		DATA X'FF000000'
889	1	0035A	00FF0000 A		DATA X'FF00000'
890	1	0035B	00000000 A		FILL X'360'
	1	0035C	00000000 A		
	1	0035D	00000000 A		
	1	0035E	00000000 A		
	1	0035F	00000000 A		

			SUFFIX(2)		
891				PAGE	
892	1	00360	00000000 A	DATA	0
893	1	00361	01010101 A	DATA	X'01010101'
894	1	00362	02020202 A	DATA	X'02020202'
895	1	00363	03030303 A	DATA	X'03030303'
896	1	00364	04040404 A	DATA	X'04040404'
897	1	00365	05050505 A	DATA	X'05050505'
898	1	00366	06060606 A	DATA	X'06060606'
899	1	00367	07070707 A	DATA	X'07070707'
900	1	00368	08080808 A	DATA	X'08080808'
901	1	00369	09090909 A	DATA	X'09090909'
902	1	0036A	31313131 A	DATA	X'31313131'
903	1	0036B	32323232 A	DATA	X'32323232'
904	1	0036C	33333333 A	DATA	X'33333333'
905	1	0036D	34343434 A	DATA	X'34343434'
906	1	0036E	35353535 A	DATA	X'35353535'
907	1	0036F	36363636 A	DATA	X'36363636'
908					
909	1	00370	000001BB	PSIXCR	P SIXCR
910	1	00371	000001BC	PTITLE	P TITLE
911	1	00372	000001BD	PHEAD	P HEAD
912	1	00373	000001BE	PSHRTL	P SHRTL
913	1	00374	000001BF	PLONGL	P LONGL
914	1	00376			BOUND B
915	1	00376	01000E00	SIXCR	JJ 1, TTL
916	1	00377	08000006 A	DATA	X'80000006'
917	1	00378	01000E00	TITLE	JJ 1, TTL
918	1	00379	0800003A A	DATA	X'8000003A'
919	1	0037A	01000E3C	HEAD	JJ 1, HDG
920	1	0037B	08000050 A	DATA	X'80000050'
921	1	0037C	01000E8C	SHRTL	JJ 1, IMAGE
922	1	0037D	0800002C A	DATA	X'8000002C'
923	1	0037E	01000E8C	LONGL	JJ 1, IMAGE
924	1	0037F	08000054 A	DATA	X'80000054'

COMMAND PAIRS

SUFFIX(2)

				PAGE	
925					
926	1	00380	15151515 A	TTL	DATA X'15151515'
927	1	00381	15151540 A		DATA X'15151540'
928	1	00382	40404040 A		DATA X'40404040'
929	1	00383	40404040 A		DATA X'40404040'
930	1	00384	40404040 A		DATA X'40404040'
931	1	00385	40404040 A		DATA X'40404040'
932	1	00386	40404040 A		DATA X'40404040'
933	1	00387	40404040 A		DATA X'40404040'
934	1	00388	40404040 A		DATA X'40404040'
935	1	00389	40E2E4C6 A		DATA X'40E2E4C6'
936	1	0038A	C6C9E740 A		DATA X'C6C9E740'
937	1	0038B	C5D9D9D6 A		DATA X'C5D9D9D6'
938	1	0038C	D940C4C9 A		DATA X'D940C4C9'
939	1	0038D	E2D7D3C1 A		DATA X'E2D7D3C1'
940	1	0038E	E8404040 A		DATA X'E8404040'
941	1	0038F	15404040 A	HDG	DATA X'15404040'
942	1	00390	40D3C9E2 A		DATA X'40D3C9E2'
943	1	00391	E3404040 A		DATA X'E3404040'
944	1	00392	4040C5D9 A		DATA X'4040C5D9'
945	1	00393	D9D6D9E2 A		DATA X'D9D6D9E2'
946	1	00394	40404040 A		DATA X'40404040'
947	1	00395	D7C1E2E2 A		DATA X'D7C1E2E2'
948	1	00396	C5E24040 A		DATA X'C5E24040'
949	1	00397	404040C9 A		DATA X'404040C9'
950	1	00398	D5E2E340 A		DATA X'D5E2E340'
951	1	00399	4040C9C4 A		DATA X'4040C9C4'
952	1	0039A	C5D5E3C9 A		DATA X'C5D5E3C9'
953	1	0039B	C6C9C5D9 A		DATA X'C6C9C5D9'
954	1	0039C	40404040 A		DATA X'40404040'
955	1	0039D	C9E24040 A		DATA X'C9E24040'
956	1	0039E	4040E2C8 A		DATA X'4040E2C8'
957	1	0039F	D6E4D3C4 A		DATA X'D6E4D3C4'
958	1	003A0	40C2C540 A		DATA X'40C2C540'
959	1	003A1	404040C4 A		DATA X'404040C4'
960	1	003A2	C9C6C615 A		DATA X'C9C6C615'
961	1	003A3	40404015 A	IMAGE	DATA X'40404015'

962

1	003A4	00000000	A
1	003A5	00000000	A
1	003A6	00000000	A
1	003A7	00000000	A
1	003A8	00000000	A
1	003A9	00000000	A
1	003AA	00000000	A
1	003AB	00000000	A
1	003AC	00000000	A
1	003AD	00000000	A
1	003AE	00000000	A
1	003AF	00000000	A
1	003B0	00000000	A
1	003B1	00000000	A
1	003B2	00000000	A
1	003B3	00000000	A
1	003B4	00000000	A
1	003B5	00000000	A
1	003B6	00000000	A

SUFFIX(2)
FILL \$+19

SUFFIX(2)
PAGE
BOUND 8
FILL \$+20

963
964 1 003B8
965 1 003B8 00000000 A
1 003B9 00000000 A
1 003BA 00000000 A
1 003BB 00000000 A
1 003BC 00000000 A
1 003BD 00000000 A
1 003BE 00000000 A
1 003BF 00000000 A
1 003C0 00000000 A
1 003C1 00000000 A
1 003C2 00000000 A
1 003C3 00000000 A
1 003C4 00000000 A
1 003C5 00000000 A
1 003C6 00000000 A
1 003C7 00000000 A
1 003C8 00000000 A
1 003C9 00000000 A
1 003CA 00000000 A
1 003CB 00000000 A
966 1 003CC 00000000 A
1 003CD 00000000 A
1 003CE 00000000 A
1 003CF 00000000 A
1 003D0 00000000 A
1 003D1 00000000 A
1 003D2 00000000 A
1 003D3 00000000 A
1 003D4 00000000 A
1 003D5 00000000 A
1 003D6 00000000 A
1 003D7 00000000 A
967 1 003D8 00000000 A
968 1 003D9 00000000 A
969 1 003DA 00000276

TABLE

TEST

FILL \$+12

RETURN

PZE
PZE
PZE,0 L00+2

```

970 1 003DB 00000000 A
971 1 003DC 000000F4
972 1 003DD 00000000 A
973 1 003DE 0000020A
974 1 003DF 00000000 A
975 1 003E0 00000109
976 1 003E1 00000000 A
977 1 003E2 00000000 A
978 1 003E3 00000000 A
979 1 003E4 00000000 A
980 1 003E5 00000000 A
981 1 003E6 00000000 A
982 1 003E7 00000000 A
983 1 003E8 00000000 A
984 1 003E9 00000000 A
985 1 003EA 00000000 A
986 1 003EB 00000000 A
987 1 003EC
988
989
990 1 003EC 00000008
1 003EC 01234567 A
1 003ED FEDCBA98 A

991
989
990 1 003EE 01234567 A
1 003EF FEDCBA98 A

991
989
990 1 003FO 01234567 A
1 003F1 FEDCBA98 A

991
989
990 1 003F2 01234567 A
1 003F3 FEDCBA98 A

991
989
990 1 003F4 01234567 A
1 003F5 FEDCBA98 A

```

```

          SUFFIX(2)
I0REL  PZE
        PZE,0 I0RET-1
BUMP   PZE
        PZE,0 BUMPER
REPEAT PZE
        PZE,0 CYCLE+1
PSW1   PZE
PSW2   PZE
CNT3CP PZE
CNT4CP PZE
ERRORS PZE
PASSES PZE
WORD   PZE
COUNT PZE
SVECNT PZE
WKG    PZE
BOUND  8
RT1    FILL
        $
        D0 8
        DATA X'1234567',X'FEDCBA98'

ELSE
D0 8
DATA X'1234567',X'FEDCBA98'

ELSE
D0 8
DATA X'1234567',X'FEDCBA98'

ELSE
D0 8
DATA X'1234567',X'FEDCBA98'

ELSE
D0 8
DATA X'1234567',X'FEDCBA98'

```


1006			
1004			
1005	1 00452	FOFOFOFO A	
	1 00453	OFOFOFOF A	
1006			
1004			
1005	1 00454	FOFOFOFO A	
	1 00455	OFOFOFOF A	
1006			
1004			
1005	1 00456	FOFOFOFO A	
	1 00457	OFOFOFOF A	
1006			
1004			
1005	1 00458	FOFOFOFO A	
	1 00459	OFOFOFOF A	
1006			
1004			
1005	1 0045A	FOFOFOFO A	
	1 0045B	OFOFOFOF A	
1006			
1007			
1008			
1009	1 0045C	00000001 A	
1010	1 0045D	00000002 A	
1011	1 0045E	00000004 A	
1012	1 0045F	00000008 A	
1013	1 00460	00000010 A	
1014	1 00461	00000020 A	
1015	1 00462	00000040 A	
1016	1 00463	00000080 A	
1017	1 00464	00000100 A	
1018	1 00465	00000200 A	
1019	1 00466	00000400 A	
1020	1 00467	00000800 A	
1021	1 00468	00001000 A	
1022	1 00469	00002000 A	
1023	1 0046A	00004000 A	

SUFFIX(2)	
ELSE	
D0	32
DATA	X'FOFOFOFOFO',X'FOFOFOFO'
ELSE	
D0	32
DATA	X'FOFOFOFOFO',X'FOFOFOFO'
ELSE	
D0	32
DATA	X'FOFOFOFOFO',X'FOFOFOFO'
ELSE	
D0	32
DATA	X'FOFOFOFOFO',X'FOFOFOFO'
ELSE	
D0	32
DATA	X'FOFOFOFOFO',X'FOFOFOFO'
ELSE	
D0	32
DATA	X'FOFOFOFOFO',X'FOFOFOFO'
ELSE	
D0	32
DATA	X'FOFOFOFOFO',X'FOFOFOFO'
ELSE	
FIN	
DATA	1
DATA	2
DATA	4
DATA	8
DATA	X'10'
DATA	X'20'
DATA	X'40'
DATA	X'80'
DATA	X'100'
DATA	X'200'
DATA	X'400'
DATA	X'800'
DATA	X'1000'
DATA	X'2000'
DATA	X'4000'

*
MT2

MT2 USED WITH CVA-CVS

1024	1	0046B	00008000	A
1025	1	0046C	00010000	A
1026	1	0046D	00020000	A
1027	1	0046E	00040000	A
1028	1	0046F	00080000	A
1029	1	00470	00100000	A
1030	1	00471	00200000	A
1031	1	00472	00400000	A
1032	1	00473	00800000	A
1033	1	00474	01000000	A
1034	1	00475	02000000	A
1035	1	00476	04000000	A
1036	1	00477	08000000	A
1037	1	00478	10000000	A
1038	1	00479	20000000	A
1039	1	0047A	40000000	A
1040	1	0047B	80000000	A
1041	1	0047C	F0F0F0F0	A
1042	1	0047D	0F0F0F0F	A
1043	1	0047E	00000000	A
1044	1	0047F	00000001	A
1045	1	00480	FFFFFFFF	A
1046	1	00481	0F0F0F0F	A
1047	1	00482	F0F0F0F0	A
1048	1	00483	00000001	A
1049	1	00484	FFFFFFFF	A
1050	1	00485	F0F0F0F0	A
1051	1	00486	0F0F0F12	A
1052	1	00487	0F0F0F0F	A
1053	1	00488	0F0F0F0F	A
1054	1	00489	D2D2D2D2	A
1055	1	0048A	F0F0F0F0	A
1056	1	0048B	1E1E1E1F	A
1057	1	0048C	00000000	A
1058	1	0048D	00000001	A
1059	1	0048E	00000000	A
1060	1	0048F	80000000	A
1061	1	00490	00000000	A

SUFFIX(2)

DATA	X'8000'
DATA	X'10000'
DATA	X'20000'
DATA	X'40000'
DATA	X'80000'
DATA	X'100000'
DATA	X'200000'
DATA	X'400000'
DATA	X'800000'
DATA	X'1000000'
DATA	X'2000000'
DATA	X'4000000'
DATA	X'8000000'
DATA	X'10000000'
DATA	X'20000000'
DATA	X'40000000'
DATA	X'80000000'
DATA	X'F0F0F0F0'
DATA	X'0F0F0F0F'
DATA	0
DATA	1
DATA	-1
DATA	X'F0F0F0F0'
DATA	X'F0F07070'
DATA	1
DATA	-1
DATA	X'F0F0F0F0'
DATA	X'F0F0F12'
DATA	X'F0F0F0F0'
DATA	X'F0F0F0F0'
DATA	X'D2D2D2D2'
DATA	X'F0F0F0F0'
DATA	X'1E1E1E1F'
DATA	0
DATA	1
DATA	0
DATA	X'80000000'
DATA	0

1062	1	00491	00000001	A
1063	1	00492	00000000	A
1064	1	00493	FFFFFFFF	A
1065	1	00494	00000000	A
1066	1	00495	00000001	A
1067	1	00496	00000000	A
1068	1	00497	F0F0F0F0	A
1069	1	00498	00000000	A
1070	1	00499	00000001	A
1071	1	0049A	00000000	A
1072	1	0049B	0F0F0F0F	A
1073	1	0049C	0696968B	A
1074	1	0049E		
1075	1	0049E	F0F0F0F0	A
1076	1	0049F	0F0F0F1F	A
1077	1	004A0	F0F0F2F0	A
1078	1	004A1	F0F4F0F0	A
1079	1	004A2	F8F0F0F0	A
1080	1	004A3	F0F0F0E0	A
1081	1	004A4	F0F0D0F0	A
1082	1	004A5	F0B0F0F0	A
1083	1	004A6	70F0F0F0	A
1084	1	004A7	F0F0F0F0	A
1085	1	004A8	F0F0F0F0	A
1086	1	004A9	F0F0F0F0	A
1087	1	004AA	0F0FF0F0	A
1088	1	004AC		
1089				
1090		00000000		
1091				
1092				
1093				
1094				
1095				
1096				
1097				
1098				
1099				
1099				

SUFFIX(2)	
DATA	1
DATA	0
DATA	-1
DATA	0
DATA	1
DATA	0
DATA	X'F0F0F0F0'
DATA	0
DATA	1
DATA	0
DATA	X'F0F0F0F0'
DATA	X'696968B'
BOUND	8
DATA	X'F0F0F0F0'
DATA	X'F0F0F1F'
DATA	X'F0F0F2F0'
DATA	X'F0F4F0F0'
DATA	X'F8F0F0F0'
DATA	X'F0F0F0E0'
DATA	X'F0F0D0F0'
DATA	X'F0B0F0F0'
DATA	X'70F0F0F0'
DATA	X'F0F0F0F0'
DATA	X'F0F0F0F0'
DATA	X'F0F0F0F0'
DATA	X'F0F0F0F0'
DATA	X'F0FF0F0'
BOUND	8
FILL	\$
CRATIC	
PROC	
FORM	8,24
SET	0
SET	X'010000'
DO	64
F	X,XX
SET	X+4
SET	XX+X'010100'
ELSE	

NT4

NT3
XXX

F
X
XX

X
XX

1100
1101
1102

1	004AC	00010203	A
1	004AD	04050607	A
1	004AE	08090A0B	A
1	004AF	0C0D0E0F	A
1	004B0	10111213	A
1	004B1	14151617	A
1	004B2	18191A1B	A
1	004B3	1C1D1E1F	A
1	004B4	20212223	A
1	004B5	24252627	A
1	004B6	28292A2B	A
1	004B7	2C2D2E2F	A
1	004B8	30313233	A
1	004B9	34353637	A
1	004BA	38393A3B	A
1	004BB	3C3D3E3F	A
1	004BC	40414243	A
1	004BD	44454647	A
1	004BE	48494A4B	A
1	004BF	4C4D4E4F	A
1	004C0	50515253	A
1	004C1	54555657	A
1	004C2	58595A5B	A
1	004C3	5C5D5E5F	A
1	004C4	60616263	A
1	004C5	64656667	A
1	004C6	68696A6B	A
1	004C7	6C6D6E6F	A
1	004C8	70717273	A
1	004C9	74757677	A
1	004CA	78797A7B	A
1	004CB	7C7D7E7F	A
1	004CC	80818283	A
1	004CD	84858687	A
1	004CE	88898A8B	A
1	004CF	8C8D8E8F	A

SUFFIX(2)
FIN
PEND
XXX

SUFFIX(2)

1 004D0 90919293 A
 1 004D1 94959697 A
 1 004D2 98999A9B A
 1 004D3 9C9D9E9F A
 1 004D4 A0A1A2A3 A
 1 004D5 A4A5A6A7 A
 1 004D6 A8A9AAAB A
 1 004D7 ACADAEAF A
 1 004D8 B0B1B2B3 A
 1 004D9 B4B5B6B7 A
 1 004DA B8B9BABB A
 1 004DB BCBDBEBF A
 1 004DC C0C1C2C3 A
 1 004DD C4C5C6C7 A
 1 004DE C8C9CACB A
 1 004DF CCCDCECF A
 1 004E0 D0D1D2D3 A
 1 004E1 D4D5D6D7 A
 1 004E2 D8D9DADB A
 1 004E3 DCDDDEDF A
 1 004E4 E0E1E2E3 A
 1 004E5 E4E5E6E7 A
 1 004E6 E8E9EAE8 A
 1 004E7 ECEDEEEF A
 1 004E8 F0F1F2F3 A
 1 004E9 F4F5F6F7 A
 1 004EA F8F9FAF8 A
 1 004EB FCFDFEFF A
 1 004EC 00212020 A
 1 004EE 00000000 A
 1 004EF 00000000 A
 1 004F0 00000000 A
 1 004F1 00000000 A
 1 004F2 00000000 A
 1 004F3 00000000 A
 1 004F4 00000000 A
 1 004F5 00000000 A

1103
 1104
 1105

EPI
 ERR1

DATA X1212020
 ESUND 8
 FILL \$+64

SUFFIX(2)

1	004F6	00000000	A
1	004F7	00000000	A
1	004F8	00000000	A
1	004F9	00000000	A
1	004FA	00000000	A
1	004FB	00000000	A
1	004FC	00000000	A
1	004FD	00000000	A
1	004FE	00000000	A
1	004FF	00000000	A
1	00500	00000000	A
1	00501	00000000	A
1	00502	00000000	A
1	00503	00000000	A
1	00504	00000000	A
1	00505	00000000	A
1	00506	00000000	A
1	00507	00000000	A
1	00508	00000000	A
1	00509	00000000	A
1	0050A	00000000	A
1	0050B	00000000	A
1	0050C	00000000	A
1	0050D	00000000	A
1	0050E	00000000	A
1	0050F	00000000	A
1	00510	00000000	A
1	00511	00000000	A
1	00512	00000000	A
1	00513	00000000	A
1	00514	00000000	A
1	00515	00000000	A
1	00516	00000000	A
1	00517	00000000	A
1	00518	00000000	A
1	00519	00000000	A
1	0051A	00000000	A
1	0051B	00000000	A

SUFFIX(2)

1 0051C 00000000 A
 1 0051D 00000000 A
 1 0051E 00000000 A
 1 0051F 00000000 A
 1 00520 00000000 A
 1 00521 00000000 A
 1 00522 00000000 A
 1 00523 00000000 A
 1 00524 00000000 A
 1 00525 00000000 A
 1 00526 00000000 A
 1 00527 00000000 A
 1 00528 00000000 A
 1 00529 00000000 A
 1 0052A 00000000 A
 1 0052B 00000000 A
 1 0052C 00000000 A
 1 0052D 00000000 A
 1 0052E 00000000 A
 1 0052F 00000000 A
 1 00530 00000000 A
 1 00531 00000000 A
 1 00532 00000000 A
 1 00533 00000000 A
 1 00534 00000000 A
 1 00535 00000000 A
 1 00536 00000000 A
 1 00537 00000000 A
 1 00538 00000000 A
 1 00539 00000000 A
 1 0053A 00000000 A
 1 0053B 00000000 A
 1 0053C 00000000 A
 1 0053D 00000000 A
 1 0053E 00000000 A
 1 0053F 00000000 A
 1 00540 00000000 A
 1 00541 00000000 A

1106

VRT

FILL

\$*10

1107

VRT

FILL

\$*10

SUFFIX(2)

1 00542 00000000 A
 1 00543 00000000 A
 1 00544 00000000 A
 1 00545 00000000 A
 1 00546 00000000 A
 1 00547 00000000 A
 1 00548 00000000 A
 1 00549 00000000 A
 1 0054A 00000000 A
 1 0054B 00000000 A
 1 0054C 00000000 A
 1 0054D 00000000 A
 1 0054E 00000000 A
 1 0054F 00000000 A
 1 00550 00000000 A
 1 00551 00000000 A
 1 00552 00000000 A
 1 00553 00000000 A
 1 00554 00000000 A
 1 00555 00000000 A
 1 00556 00000000 A
 1 00557 00000000 A
 1 00558 00000000 A
 1 00559 00000000 A
 1 0055A 00000000 A
 1 0055B 00000000 A
 1 0055C 00000000 A
 1 0055D 00000000 A
 1 0055E 00000000 A
 1 0055F 00000000 A
 1 00560 00000000 A
 1 00561 00000000 A
 1 00562 00000000 A
 1 00563 00000000 A
 1 00564 00000000 A
 1 00565 00000000 A
 1 00566 00000000 A
 1 00567 00000000 A

1103

FMT

FILL

\$+66

SUFFIX(2)

1	00568	00000000	A
1	00569	00000000	A
1	0056A	00000000	A
1	0056B	00000000	A
1	0056C	00000000	A
1	0056D	00000000	A
1	0056E	00000000	A
1	0056F	00000000	A
1	00570	00000000	A
1	00571	00000000	A
1	00572	00000000	A
1	00573	00000000	A
1	00574	00000000	A
1	00575	00000000	A
1	00576	00000000	A
1	00577	00000000	A
1	00578	00000000	A
1	00579	00000000	A
1	0057A	00000000	A
1	0057B	00000000	A
1	0057C	00000000	A
1	0057D	00000000	A
1	0057E	00000000	A
1	0057F	00000000	A
1	00580	00000000	A
1	00581	00000000	A
1	00582	00000000	A
1	00583	00000000	A
1	00584	00000000	A
1	00585	00000000	A
1	00586	00000000	A
1	00587	00000000	A
1	00588	00000000	A
1	00589	00000000	A
1	0058A	00000000	A
1	0058B	00000000	A
1	0058C	00000000	A
1	0058D	00000000	A

SUFFIX(2)

1103

1 0058E 00000000 A
 1 0058F 00000000 A
 1 00590 00000000 A
 1 00591 00000000 A
 1 00592 00000000 A
 1 00593 00000000 A
 1 00594 00000000 A
 1 00595 00000000 A
 1 00596 00000000 A
 1 00597 00000000 A
 1 00598 00000000 A
 1 00599 00000000 A
 1 0059A 00000000 A
 1 0059B 00000000 A
 1 0059C 00000000 A
 1 0059D 00000000 A
 1 0059E 00000000 A
 1 0059F 00000000 A
 1 005A0 00000000 A
 1 005A1 00000000 A
 1 005A2 00000000 A
 1 005A3 00000000 A
 1 005A4 00000000 A
 1 005A5 00000000 A
 1 005A6 00000000 A
 1 005A7 00000000 A
 1 005A8 00000000 A
 1 005A9 00000000 A
 1 005AA 00000000 A
 1 005AB 00000000 A
 1 005AC 00000000 A
 1 005AD 00000000 A
 1 005AE 00000000 A
 1 005AF 00000000 A
 1 005B0 00000000 A
 1 005B1 00000000 A
 1 005B2 00000000 A
 1 005B3 00000000 A

VMT

FILL

\$+5%

9:30 AM

SUFFIX(2)

1 005B4 00000000 A
1 005B5 00000000 A
1 005B6 00000000 A
1 005B7 00000000 A
1 005B8 00000000 A
1 005B9 00000000 A
1 005BA 00000000 A
1 005BB 00000000 A
1 005BC 00000000 A
1 005BD 00000000 A
1 005BE 00000000 A
1 005BF 00000000 A
1 005C0 00000000 A
1 005C1 00000000 A
1 005C2 00000000 A
1 005C3 00000000 A
1 005C4 00000000 A
1 005C5 00000000 A
1 005C6 00000000 A
1 005C7 00000000 A
1 005C8 00000000 A
1 005C9 00000000 A
1 005CA 00000000 A
1 005CB 00000000 A
1 005CC 00000000 A
1 005CD 00000000 A
1 005CE 00000000 A
1 005CF 00000000 A
1 005D0 00000000 A
1 005D1 00000000 A
1 005D2 00000000 A
1 005D3 00000000 A
1 005D4 00000000 A
1 005D5 00000000 A
1 005D6 00000000 A
1 005D7 00000000 A
1 005D8 00000000 A
1 005D9 00000000 A

1110

VATR

FILL

\$+64

SUFFIX(2)

1	005DA	00000000	A
1	005DB	00000000	A
1	005DC	00000000	A
1	005DD	00000000	A
1	005DE	00000000	A
1	005DF	00000000	A
1	005E0	00000000	A
1	005E1	00000000	A
1	005E2	00000000	A
1	005E3	00000000	A
1	005E4	00000000	A
1	005E5	00000000	A
1	005E6	00000000	A
1	005E7	00000000	A
1	005E8	00000000	A
1	005E9	00000000	A
1	005EA	00000000	A
1	005EB	00000000	A
1	005EC	00000000	A
1	005ED	00000000	A
1	005EE	00000000	A
1	005EF	00000000	A
1	005F0	00000000	A
1	005F1	00000000	A
1	005F2	00000000	A
1	005F3	00000000	A
1	005F4	00000000	A
1	005F5	00000000	A
1	005F6	00000000	A
1	005F7	00000000	A
1	005F8	00000000	A
1	005F9	00000000	A
1	005FA	00000000	A
1	005FB	00000000	A
1	005FC	00000000	A
1	005FD	00000000	A
1	005FE	00000000	A
1	005FF	00000000	A

SUFFIX(2)

1	00600	00000000	A
1	00601	00000000	A
1	00602	00000000	A
1	00603	00000000	A
1	00604	00000000	A
1	00605	00000000	A
1	00606	00000000	A
1	00607	00000000	A
1	00608	00000000	A
1	00609	00000000	A
1	0060A	00000000	A
1	0060B	00000000	A
1	0060C	00000000	A
1	0060D	00000000	A
1	0060E	00000000	A
1	0060F	00000000	A

SUFFIX(2)
PAGE

1111							
1112							
1113	1	00610	FFFFFFFF0 A	LIST	DATA	-16	LM COUNT
1114	1	00611	2A00040C		LM,0	RT3	INSTRUCTION
1115	1	00612	07300244		K	0,7,3,SETPSW	PSW1 IN
1116	1	00613	07300276		K	0,7,3,LOC+2	PSW1 OUT
1117	1	00614	FEDCBA98 A		DATA	X'FEDCBA98'	R12 IN
1118	1	00615	00000000 A		DATA	0	R12 OUT
1119	1	00616	FFFFFFF A		DATA	-1	M1 IN
1120	1	00617	FFFFFFF A		DATA	-1	M1 OUT
1121	1	00618	01234567 A		DATA	X'1234567'	R13 IN
1122	1	00619	FFFFFFF A		DATA	-1	R13 OUT
1123	1	0061A	00000000 A		DATA	0	M2 IN
1124	1	0061B	00000000 A		DATA	0	M2 OUT
1125	1	0061C	3272045C		LW,7	MT1+64,1	FMT
1126	1	0061D	3202049C		LW,13	MT2+64,1	VMT/VMTR
1127	1	0061E	3202040B		LW,13	RT3-1,1	VRTRCH
1128	1	0061F	00000010 A		DATA	16	RC
1129							LM-INDEXING
1130	1	00620	FFFFFFFF0 A		DATA	-16	COUNT
1131	1	00621	2A02040D		LM,0	RT3+1,1	INSTRUCTION
1132	1	00622	F0300244		K	15,0,3,SETPSW	PSW1 IN
1133	1	00623	F0300276		K	15,0,3,LOC+2	PSW1 OUT
1134	1	00624	FFFFFFF A		DATA	-1	R12 IN-INDEX
1135	1	00625	00000000 A		PZE		R12 OUT
1136	1	00626	00000000 A		PZE		M1 IN
1137	1	00627	00000000 A		PZE		M1 OUT
1138	1	00628	FEDCBA98 A		DATA	X'FEDCBA98'	R13 IN
1139	1	00629	FFFFFFF A		DATA	-1	R13 OUT
1140	1	0062A	FFFFFFF A		DATA	-1	M2 IN
1141	1	0062B	FFFFFFF A		DATA	-1	M2 OUT
1142	1	0062C	3272045C		LW,7	MT1+64,1	FMT
1143	1	0062D	3202049C		LW,13	MT2+64,1	VMT/VMTR
1144	1	0062E	3202040B		LW,13	RT3-1,1	VRTRCH
1145	1	0062F	0000000F A		DATA	15	RC
1146							LM-INDIRECT ADDRESSING
1147	1	00630	FFFFFFFF0 A		DATA	-16	COUNT

Address	Count	Hex	Mask	SUFFIX(2)	Instruction
1148	1	00631	AA0002EB	LM,0 *WKIA	INSTRUCTION
1149	1	00632	17000244	K 1,7,0,0,SETPSW	PSW1 IN
1150	1	00633	17000276	K 1,7,0,0,LOC+2	PSW1 OUT
1151	1	00634	FFFFFFFF A	DATA -1	R12 IN
1152	1	00635	FFFFFFFF A	DATA -1	R12 OUT
1153	1	00636	FFFFFFFF A	DATA -1	M1 IN
1154	1	00637	FFFFFFFF A	DATA -1	M1 OUT
1155	1	00638	0000040C	PZE,0 RT3	R13 IN-INDIRECT ADDRESS
1156	1	00639	0000040C	PZE,0 RT3	R13 OUT
1157	1	0063A	00000000 A	PZE	M2 IN
1158	1	0063B	00000000 A	PZE	M2 OUT
1159	1	0063C	32D2045C	LW,7 MT1+64,1	FMT
1160	1	0063D	32D2049C	LW,13 MT2+64,1	VMT/VMTTR
1161	1	0063E	32D2040B	LW,13 RT3-1,1	VRTTRCH
1162	1	0063F	00000001 A	DATA 1	RC
1163					LM-INDIRECT ADDRESSING-INDEXING
1164	1	00640	FFFFFFFF0 A	DATA -16	COUNT
1165	1	00641	AA0202EB	LM,0 *WKIA,1	INSTRUCTION
1166	1	00642	E0000244	K 14,0,0,0,SETPSW	PSW1 IN
1167	1	00643	E0000276	K 14,0,0,0,LOC+2	PSW1 OUT
1168	1	00644	FFFFFFFF A	DATA -1	R12 IN-INDEX
1169	1	00645	00000000 A	PZE	R12 OUT
1170	1	00646	00000000 A	PZE	M1 IN
1171	1	00647	00000000 A	PZE	M1 OUT
1172	1	00648	0000040D	PZE,0 RT3+1	R13 IN-INDIRECT ADDRESS
1173	1	00649	FFFFFFFF A	DATA -1	R13 OUT
1174	1	0064A	FFFFFFFF A	DATA -1	M2 IN
1175	1	0064B	FFFFFFFF A	DATA -1	M2 OUT
1176	1	0064C	32D2045C	LW,7 MT1+64,1	FMT
1177	1	0064D	32D2049C	LW,13 MT2+64,1	VMT/VMTTR
1178	1	0064E	32D2040B	LW,13 RT3-1,1	VRTTRCH
1179	1	0064F	0000000E A	DATA 14	RC
1180					STM
1181	1	00650	FFFFFFFF0 A	DATA -16	COUNT
1182	1	00651	2B00052E	STM,0 VRT	INSTRUCTION
1183	1	00652	07300244	K 0,7,3,0,SETPSW	PSW1 IN
1184	1	00653	07300276	K 0,7,3,0,LOC+2	PSW1 OUT
1185	1	00654	FEDCBA98 A	DATA XIFEDCBA98	R12 IN

Address	Count	Hex	Label	Disassembly	Comment
1186	1	00655	FEDCBA98	A	
1187	1	00656	01234567	A	
1188	1	00657	01234567	A	
1189	1	00658	FEDCBA98	A	
1190	1	00659	FEDCBA98	A	
1191	1	0065A	01234567	A	
1192	1	0065B	01234567	A	
1193	1	0065C	3272045C		
1194	1	0065D	32D2045C		
1195	1	0065E	32D203EB		
1196	1	0065F	00000010	A	
1197					
1198	1	00660	FFFFFFFF	A	
1199	1	00661	20E34A96		
1200	1	00662	20300244		
1201	1	00663	20300276		
1202	1	00664	FEDCBA98	A	
1203	1	00665	FEDCBA98	A	
1204	1	00666	01234567	A	
1205	1	00667	01234567	A	
1206	1	00668	01234567	A	
1207	1	00669	01234567	A	
1208	1	0066A	FEDCBA98	A	
1209	1	0066B	FEDCBA98	A	
1210	1	0066C	3272045C		
1211	1	0066D	32D2045C		
1212	1	0066E	32D203EB		
1213	1	0066F	00000001	A	
1214					
1215	1	00670	FFFFFFFF	A	
1216	1	00671	A8000000		
1217	1	00672	E7000244		
1218	1	00673	E7000276		
1219	1	00674	01234567	A	
1220	1	00675	01234567	A	
1221	1	00676	FEDCBA98	A	
1222	1	00677	FEDCBA98	A	
1223	1	00678	00000001	A	

SUFFIX(2)

Disassembly	Comment
DATA X'FEDCBA98'	R12 OUT
DATA X'1234567'	M1 IN
DATA X'1234567'	M1 OUT
DATA X'FEDCBA98'	R13 IN
DATA X'FEDCBA98'	R13 OUT
DATA X'1234567'	M2 IN
DATA X'1234567'	M2 OUT
LW,7 MT1+64,1	FMT
LW,13 MT2+64,1	VMT/VHTR
LW,13 RT1-1,1	VRTRCH
DATA 16	RC
	STM-INDEXING
DATA -16	COUNT
STM,14 VRT-X'BCA98',1	INSTRUCTION
K 2,0,3,SETPSW	PSW1 IN
K 2,0,3,LOC+2	PSW1 OUT
DATA X'FEDCBA98'	R12 IN-INDEX
DATA X'FEDCBA98'	R12 OUT
DATA X'1234567'	M1 IN
DATA X'1234567'	M1 OUT
DATA X'1234567'	R13 IN
DATA X'1234567'	R13 OUT
DATA X'FEDCBA98'	M2 IN
DATA X'FEDCBA98'	M2 OUT
LW,7 MT1+64,1	FMT
LW,13 MT2+64,1	VMT/VHTR
LW,13 RT1-1,1	VRTRCH
DATA 1	RC
	STM-INDEXING ADDRESS
DATA -16	COUNT
STM,0 +WKIA	INSTRUCTION
K 14,7,0,SETPSW	PSW1 IN
K 14,7,0,LOC+2	PSW1 OUT
DATA X'1234567'	R12 IN
DATA X'1234567'	R12 OUT
DATA X'FEDCBA98'	M1 IN
DATA X'FEDCBA98'	M1 OUT
PZE,0 V,1	R13 IN-INDEX ADDRESS

Address	Op	Op	Op	Op	Op	Op
1224	1	00679	0000052E	SUFFIX(2)		
1225	1	0067A	01234567 A	PZE,0	VRT	R13 OUT
1226	1	0067B	01234567 A	DATA	X'1234567'	M2 IN
1227	1	0067C	3272045C	DATA	X'1234567'	M2 OUT
1228	1	0067D	32D2049C	LW,7	MT1+64,1	FMT
1229	1	0067E	32D203EB	LW,13	MT2+64,1	VMT/VMTR
1230	1	0067F	0000000E A	LW,13	RT1-1,1	VRTRCH
1231				DATA	14	RC
1232	1	00680	FFFFFFFF0 A	DATA	-16	STM-INDIRECT ADDRESSING-INDEXING
1233	1	00681	AB0202EB	STM,0	*WKIA,1	COUNT
1234	1	00682	00000244	K	0,0,0,SETPSW	INSTRUCTION
1235	1	00683	00000276	K	0,0,0,L0C+2	PSW1 IN
1236	1	00684	000CBA98 A	DATA	X'CBA98'	PSW1 OUT
1237	1	00685	000CBA98 A	DATA	X'CBA98'	R12 IN-INDEX
1238	1	00686	00000000 A	PZE		R12 OUT
1239	1	00687	00000000 A	PZE		M1 IN
1240	1	00688	00014A96	PZE,0	VRT-X'CBA98'	M1 OUT
1241	1	00689	00014A96	PZE,0	VRT-X'CBA98'	R13 IN
1242	1	0068A	01234567 A	DATA	X'1234567'	R13 OUT
1243	1	0068B	01234567 A	DATA	X'1234567'	M2 IN
1244	1	0068C	3272045C	DATA	X'1234567'	M2 OUT
1245	1	0068D	32D2049C	LW,7	MT1+64,1	FMT
1246	1	0068E	32D203EB	LW,13	MT2+64,1	VMT/VMTR
1247	1	0068F	00000010 A	LW,13	RT1-1,1	VRTRCH
1248				DATA	16	RC
1249	1	00690	FFFFFFFF0 A	DATA	-16	PLW
1250	1	00691	08C003D4	PLW,12	MEMORY	COUNT
1251	1	00692	F7300244	K	15,7,3,SETPSW	INSTRUCTION
1252	1	00693	17300276	K	1,7,3,L0C+2	PSW1 IN
1253	1	00694	FFFFFFFF A	DATA	-1	PSW1 OUT
1254	1	00695	00000000 A	DATA	0	R12 IN
1255	1	00696	0000040C	PZE,0	RT3	R12 OUT
1256	1	00697	0000040B	PZE,0	RT3-1	M1 IN
1257	1	00698	FFFFFFFF A	DATA	-1	M1 OUT
1258	1	00699	FFFFFFFF A	DATA	-1	R13 IN
1259	1	0069A	00000001 A	DATA	X'1'	R13 OUT
1260	1	0069B	00010000 A	DATA	X'10000'	M2 IN
1261	1	0069C	3272045C	LW,7	MT1+64,1	M2 OUT
						FMT

Address	Op	Op Code	Op Name	Op Mode	SUFFIX(2)	Op Name
1262	1	0069D	32D2049C		LW,13	VMV/VMTR
1263	1	0069E	32D203BD		LW,13	VRTRCH
1264	1	0069F	00000001	A	DATA	RC
1265						PLW-ODD REGISTER-INDEXING
1266	1	006A0	FFFFFFF0	A	DATA	COUNT
1267	1	006A1	08D203D2		PLW,13	INSTRUCTION
1268	1	006A2	F0300244		K	PSW1 IN
1269	1	006A3	00300276		K	PSW1 OUT
1270	1	006A4	00000001	A	DATA	R12 IN-INDEX
1271	1	006A5	00000001	A	DATA	R12 OUT
1272	1	006A6	0000040C		PZE,0	M1 IN
1273	1	006A7	0000040B		PZE,0	M1 OUT
1274	1	006A8	FFFFFFFF	A	DATA	R13 IN
1275	1	006A9	00000000	A	DATA	R13 OUT
1276	1	006AA	00000002	A	DATA	M2 IN
1277	1	006AB	00010001	A	DATA	M2 OUT
1278	1	006AC	3272045C		LW,7	FMT
1279	1	006AD	32D2049C		LW,13	VMV/VMTR
1280	1	006AE	32D203BD		LW,13	VRTRCH
1281	1	006AF	00000001	A	DATA	RC
1282						PLW-INDIRECT ADDRESSING-INDEXING
1283	1	006B0	FFFFFFF0	A	DATA	COUNT
1284	1	006B1	88C202EB		PLW,12	INSTRUCTION
1285	1	006B2	07000244		K	PSW1 IN
1286	1	006B3	07000276		K	PSW1 OUT
1287	1	006B4	00000014	A	DATA	R12 IN-INDEX
1288	1	006B5	00000000	A	PZE	R12 OUT
1289	1	006B6	0000040C		PZE,0	M1 IN
1290	1	006B7	0000040B		PZE,0	M1 OUT
1291	1	006B8	000003AC		PZE,0	R13 IN-INDIRECT ADDRESS
1292	1	006B9	000003AC		PZE,0	R13 OUT
1293	1	006BA	0000FFFF	A	DATA	M2 IN
1294	1	006BB	0001FFFE	A	DATA	M2 OUT
1295	1	006BC	3272045C		LW,7	FMT
1296	1	006BD	32D2049C		LW,13	VMV/VMTR
1297	1	006BE	32D203BD		LW,13	VRTRCH
1298	1	006BF	00000001	A	DATA	RC
1299						PLW-INDIRECT ADDRESSING

Address	Op	Op Code	Op Mod	Op Data	SUFFIX(2)	Op Name
1300	1	006C0		FFFFFFFF	DATA -16	COUNT
1301	1	006C1		88C002E	PLW,12 *WKIA	INSTRUCTION
1302	1	006C2		0C0002E	K 0,0,0,SETPSW	PSW1 IN
1303	1	006C3		3000027E	K 3,0,0,LOC+2	PSW1 OUT
1304	1	006C4	A	FFFFFFFF	DATA -1	R12 IN
1305	1	006C5	A	FFFFFFFF	DATA -1	R12 OUT
1306	1	006C6		0000040C	PZE,0 RT3	M1 IN
1307	1	006C7		0000040C	PZE,0 RT3	M1 OUT
1308	1	006C8		000003D4	PZE,0 MEMORY	R13 IN-INDIRECT ADDRESS
1309	1	006C9		000003D4	PZE,0 MEMORY	R13 OUT
1310	1	006CA	A	00018000	DATA X'18000'	M2 IN
1311	1	006CB	A	00018000	DATA X'18000'	M2 OUT
1312	1	006CC		3272045C	LW,7 MT1+64,1	FMT
1313	1	006CD		32D2049C	LW,13 MT2+64,1	VMT/VMTR
1314	1	006CE		32D203BD	LW,13 TABLE+5,1	VRTRCH
1315	1	006CF	A	00000001	DATA 1	RC
1316						PLW
1317	1	006D0	A	FFFFFFFF	DATA -16	COUNT
1318	1	006D1		08C003D4	PLW,12 MEMORY	INSTRUCTION
1319	1	006D2		F0000244	K 15,0,0,SETPSW	PSW1 IN
1320	1	006D3		8000027E	K 8,0,0,LOC+2	PSW1 OUT
1321	1	006D4	A	FFFFFFFF	DATA -1	R12 IN
1322	1	006D5	A	FFFFFFFF	DATA -1	R12 OUT
1323	1	006D6		0000040C	PZE,0 RT3	M1 IN
1324	1	006D7		0000040C	PZE,0 RT3	M1 OUT
1325	1	006D8	A	00000000	PZE	R13 IN
1326	1	006D9	A	00000000	PZE	R13 OUT
1327	1	006DA	A	FFFFF0001	DATA X'FFFFF0001'	M2 IN
1328	1	006DB	A	FFFFF0001	DATA X'FFFFF0001'	M2 OUT
1329	1	006DC		3272045C	LW,7 MT1+64,1	FMT
1330	1	006DD		32D2049C	LW,13 MT2+64,1	VMT/VMTR
1331	1	006DE		32D203BD	LW,13 TABLE+5,1	VRTRCH
1332	1	006DF	A	00000001	DATA 1	RC
1333						PLW
1334	1	006E0	A	FFFFFFFF	DATA -16	COUNT
1335	1	006E1		08C003D4	PLW,12 MEMORY	INSTRUCTION
1336	1	006E2		83100244	K 8,3,1,SETPSW	PSW1 IN
1337	1	006E3		7310027E	K 7,3,1,LOC+2	PSW1 OUT

			SUFFIX(2)			
1338	1	006E4	FFFFFFFF	A	DATA -1	R12 IN
1339	1	006E5	FFFFFFFF	A	DATA -1	R12 OUT
1340	1	006E6	0000040C		PZE,0 RT3	M1 IN
1341	1	006E7	0000040C		PZE,0 RT3	M1 OUT
1342	1	006E8	00000000	A	PZE	R13 IN
1343	1	006E9	00000000	A	PZE	R13 OUT
1344	1	006EA	00008000	A	DATA X'8000'	M2 IN
1345	1	006EB	00008000	A	DATA X'8000'	M2 OUT
1346	1	006EC	3272045C		LW,7 MT1+64,1	FMT
1347	1	006ED	32D2049C		LW,13 MT2+64,1	VMT/VMTB
1348	1	006EE	32D203BD		LW,13 TABLE+5,1	VRTRCH
1349	1	006EF	00000001	A	DATA 1	RC
1350						PLW
1351	1	006F0	FFFFFFFF0	A	DATA -16	COUNT
1352	1	006F1	08C003D4		PLW,12 MEMORY	INSTRUCTION
1353	1	006F2	42200244		K 4,2,2,SETPSW	PSW1 IN
1354	1	006F3	B2200276		K 11,2,2,LDC+2	PSW1 OUT
1355	1	006F4	FFFFFFFF	A	DATA -1	R12 IN
1356	1	006F5	FFFFFFFF	A	DATA -1	R12 OUT
1357	1	006F6	0000040C		PZE,0 RT3	M1 IN
1358	1	006F7	0000040C		PZE,0 RT3	M1 OUT
1359	1	006F8	00000000	A	PZE	R13 IN
1360	1	006F9	00000000	A	PZE	R13 OUT
1361	1	006FA	FFFF8000	A	DATA X'FFFF8000'	M2 IN
1362	1	006FB	FFFF8000	A	DATA X'FFFF8000'	M2 OUT
1363	1	006FC	3272045C		LW,7 MT1+64,1	FMT
1364	1	006FD	32D2049C		LW,13 MT2+64,1	VMT/VMTB
1365	1	006FE	32D203BD		LW,13 TABLE+5,1	VRTRCH
1366	1	006FF	00000001	A	DATA 1	RC
1367						PLW-TRAP
1368	1	00700	FFFFFFFF0	A	DATA -16	COUNT
1369	1	00701	08C003D4		PLW,12 MEMORY	INSTRUCTION
1370	1	00702	C730018E		K 12,7,3,SLSH	PSW1 IN
1371	1	00703	C730007D		K 12,7,3,SLRETA	PSW1 OUT
1372	1	00704	FFFFFFFF	A	DATA -1	R12 IN
1373	1	00705	FFFFFFFF	A	DATA -1	R12 OUT
1374	1	00706	0000040C		PZE,0 RT3	M1 IN
1375	1	00707	0000040C		PZE,0 RT3	M1 OUT

1376	1	00708	C0000000	A
1377	1	00709	00000000	A
1378	1	0070A	00C10000	A
1379	1	0070B	00C10000	A
1380	1	0070C	3272045C	
1381	1	0070D	32D2049C	
1382	1	0070E	32D203BD	
1383	1	0070F	00000001	A
1384				
1385	1	00710	FFFFFFF0	A
1386	1	00711	08C003D4	
1387	1	00712	8730018E	
1388	1	00713	8730007D	
1389	1	00714	FFFFFFF0	A
1390	1	00715	FFFFFFF0	A
1391	1	00716	0000040C	
1392	1	00717	0000040C	
1393	1	00718	00000000	A
1394	1	00719	00000000	A
1395	1	0071A	00000000	A
1396	1	0071B	00000000	A
1397	1	0071C	3272045C	
1398	1	0071D	32D2049C	
1399	1	0071E	32D203BD	
1400	1	0071F	00000001	A
1401				
1402	1	00720	FFFFFFF0	A
1403	1	00721	08C203D0	
1404	1	00722	7730018E	
1405	1	00723	7730007D	
1406	1	00724	00000002	A
1407	1	00725	00000002	A
1408	1	00726	0000040C	
1409	1	00727	0000040C	
1410	1	00728	00000000	A
1411	1	00729	00000000	A
1412	1	0072A	7FFF0007	A
1413	1	0072B	7FFF0007	A

SUFFIX(2)

PZE	
PZE	
DATA	X'10000'
DATA	X'10000'
LW,7	MT1+64,1
LW,13	MT2+64,1
LW,13	TABLE+5,1
DATA	1
DATA	-16
PLW,12	MEMORY
K	8,7,3,SLSW
K	8,7,3,SLRET+1
DATA	-1
DATA	-1
PZE,0	RT3
PZE,0	RT3
PZE	
PZE	
PZE	
PZE	
LW,7	MT1+64,1
LW,13	MT2+64,1
LW,13	TABLE+5,1
DATA	1
DATA	-16
PLW,12	MEMORY+4,1
K	7,7,3,SLSW
K	7,7,3,SLRET+1
DATA	2
DATA	2
PZE,0	RT3
PZE,0	RT3
PZE	
PZE	
DATA	X'7FFF0007'
DATA	X'7FFF0007'

R13 IN
R13 OUT
M2 IN
M2 OUT
FMT
VMT/VMTB
VRTRCH
RC
PLW-TRAP
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
M1 IN
M1 OUT
R13 IN
R13 OUT
M2 IN
M2 OUT
FMT
VMT/VMTB
VRTRCH
RC
PLW-INDEX-TRAP
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN-INDEX
R12 OUT
M1 IN
M1 OUT
R13 IN
R13 OUT
M2 IN
M2 OUT

Address	Op	Op Code	Operand	Suffix(2)	Instruction
1414	1	0072C	3272045C	LW,7	MT1+64,1
1415	1	0072D	32D2049C	LW,13	MT2+64,1
1416	1	0072E	32D2038D	LW,13	TABLE+5,1
1417	1	0072F	00000001	DATA	1
1418					RC
1419	1	00730	FFFFFFF0	DATA	-16
1420	1	00731	88C002EB	PLW,12	*WKIA
1421	1	00732	4730018E	K	4,7,3,SLGW
1422	1	00733	4730007D	K	4,7,3,SLRET+1
1423	1	00734	FFFFFFFF	DATA	-1
1424	1	00735	FFFFFFFF	DATA	-1
1425	1	00736	0000040C	PZE,0	RT3
1426	1	00737	0000040C	PZE,0	RT3
1427	1	00738	000003D4	PZE,0	MEMORY
1428	1	00739	000003D4	PZE,0	MEMORY
1429	1	0073A	7FFF0000	DATA	X17FFF0000
1430	1	0073B	7FFF0000	DATA	X17FFF0000
1431	1	0073C	3272045C	LW,7	MT1+64,1
1432	1	0073D	32D2049C	LW,13	MT2+64,1
1433	1	0073E	32D2038D	LW,13	TABLE+5,1
1434	1	0073F	00000001	DATA	1
1435					RC
1436	1	00740	FFFFFFF0	DATA	-16
1437	1	00741	09C008D4	PSW,12	MEMORY
1438	1	00742	07300244	K	0,7,3,SETTOW
1439	1	00743	47300276	K	4,7,3,LSO02
1440	1	00744	01234567	DATA	X11234567
1441	1	00745	01234567	DATA	X11234567
1442	1	00746	0000052D	PZE,0	VRT+1
1443	1	00747	0000052E	PZE,0	VRT
1444	1	00748	00000600	DATA	0
1445	1	00749	00000600	DATA	0
1446	1	0074A	00010000	DATA	X110000
1447	1	0074B	00000001	DATA	1
1448	1	0074C	3272045C	LW,7	MT1+64,1
1449	1	0074D	32D2049C	LW,13	MT2+64,1
1450	1	0074E	32D2038D	LW,13	RT1,1,1
1451	1	0074F	00000010	DATA	16

FMT
 VMT/MVTR
 VRTRCH
 RC
 PLW-INDIRECT ADDRESSING-TRAP
 COUNT
 INSTRUCTION
 PSW1 IN
 PSW1 OUT
 R12 IN
 R12 OUT
 M1 IN
 M1 OUT
 R13 IN-INDIRECT ADDRESSING
 R13 OUT
 M2 IN
 M2 OUT
 FMT
 VMT/MVTR
 VRTRCH
 RC
 PSW
 COUNT
 INSTRUCTION
 PSW1 IN
 PSW1 OUT
 R12 IN
 R12 OUT
 M1 IN
 M1 OUT
 R13 IN
 R13 OUT
 M2 IN
 M2 OUT
 FMT
 VMT/MVTR
 VRTRCH
 RC

SUFFIX(2)

Address	Op	Op Code	Op Name	Op Type	Op Value	Op Description
1452						
1453	1	00750	FFFFFFF0	A	DATA	-16
1454	1	00751	09D203D0		PSW,13	MEMORY-4,1
1455	1	00752	F0300244		K	15,0,3,SETPSW
1456	1	00753	00300276		K	0,0,3,L0C+2
1457	1	00754	00000002	A	DATA	2
1458	1	00755	00000002	A	DATA	2
1459	1	00756	0000052D		PZE,0	VRT-1
1460	1	00757	0000052E		PZE,0	VRT
1461	1	00758	01234567	A	DATA	X'1234567'
1462	1	00759	01234567	A	DATA	X'1234567'
1463	1	0075A	FFFF0000	A	DATA	X'FFFF0000'
1464	1	0075B	FFFFE0001	A	DATA	X'FFFFE0001'
1465	1	0075C	3272045C		LW,7	MT1+64,1
1466	1	0075D	32D2049C		LW,13	MT2+64,1
1467	1	0075E	32D203EB		LW,13	RT1-1,1
1468	1	0075F	00000010	A	DATA	16
1469						
1470	1	00760	FFFFFFF0	A	DATA	-16
1471	1	00761	89C002EB		PSW,12	*WKIA
1472	1	00762	B7C00244		K	11,7,0,SETPSW
1473	1	00763	070C0276		K	0,7,0,L0C+2
1474	1	00764	01234567	A	DATA	X'1234567'
1475	1	00765	01234567	A	DATA	X'1234567'
1476	1	00766	0000052D		PZE,0	VRT-1
1477	1	00767	0000052E		PZE,0	VRT
1478	1	00768	000003D4		PZE,0	MEMORY
1479	1	00769	000003D4		PZE,0	MEMORY
1480	1	0076A	FFFF0000	A	DATA	X'FFFF0000'
1481	1	0076B	FFFFE0001	A	DATA	X'FFFFE0001'
1482	1	0076C	3272045C		LW,7	MT1+64,1
1483	1	0076D	32D2049C		LW,13	MT2+64,1
1484	1	0076E	32D203EB		LW,13	RT1-1,1
1485	1	0076F	00000010	A	DATA	16
1486						
1487	1	00770	FFFFFFF0	A	DATA	-16
1488	1	00771	89C202EB		PSW,12	*WKIA,1
1489	1	00772	D0000244		K	13,0,0,SETPSW

PSW-ODD REGISTER-INDEXING
COUNT

INSTRUCTION
PSW1 IN

PSW1 OUT
R12 IN-INDEX

R12 OUT
M1 IN

M1 OUT
R13 IN

R13 OUT
M2 IN

M2 OUT
FMT

VRT/VMTR
VRTRCH

RC

PSW-INDIRECT ADDRESSING
COUNT

INSTRUCTION
PSW1 IN

PSW1 OUT
R12 IN

R12 OUT
M1 IN

M1 OUT
R13 IN-INDIRECT ADDRESS

R13 OUT
M2 IN

M2 OUT
FMT

VRT/VMTR
VRTRCH

RC

PSW-INDIRECT ADDRESSING-INDEXING
COUNT

INSTRUCTION
PSW1 IN

ABORT

			SUFFIX(2)			
1528	1	00797	0000052D	PZE,0	VRT-1	M1 OUT
1529	1	00798	00000000 A	PZE		R13 IN
1530	1	00799	00000000 A	PZE		R13 OUT
1531	1	0079A	800C0000 A	DATA	X'80000000'	M2 IN
1532	1	0079B	80000000 A	DATA	X'80000000'	M2 OUT
1533	1	0079C	3272045C	LW,7	MT1+64,1	FMT
1534	1	0079D	32D2049C	LW,13	MT2+64,1	VMT/VMTR
1535	1	0079E	32D203FA	LW,13	RT2-2,1	VRTRCH
1536	1	0079F	00000010 A	DATA	16	RC
1537						PSW-ABORT
1538	1	007A0	FFFFFFFF0 A	DATA	-16	COUNT
1539	1	007A1	09C003D4	PSW,12	MEMORY	INSTRUCTION
1540	1	007A2	10000244	K	1,0,0,SETPSW	PSW1 IN
1541	1	007A3	E0000276	K	14,0,0,L9C+2	PSW1 OUT
1542	1	007A4	00000000 A	PZE	0	R12 IN
1543	1	007A5	00000000 A	PZE	0	R12 OUT
1544	1	007A6	0000052D	PZE,0	VRT-1	M1 IN
1545	1	007A7	0000052D	PZE,0	VRT-1	M1 OUT
1546	1	007A8	FFFFFFFF A	DATA	-1	R13 IN
1547	1	007A9	FFFFFFFF A	DATA	-1	R13 OUT
1548	1	007AA	8000FFFF A	DATA	X'8000FFFF'	M2 IN
1549	1	007AB	8000FFFF A	DATA	X'8000FFFF'	M2 OUT
1550	1	007AC	3272045C	LW,7	MT1+64,1	FMT
1551	1	007AD	32D2049C	LW,13	MT2+64,1	VMT/VMTR
1552	1	007AE	32D203FA	LW,13	RT2-2,1	VRTRCH
1553	1	007AF	00000010 A	DATA	16	RC
1554						PSW-TRAP
1555	1	007B0	FFFFFFFF0 A	DATA	-16	COUNT
1556	1	007B1	09C003D4	PSW,12	MEMORY	INSTRUCTION
1557	1	007B2	D730018E	K	13,7,3,SLSW	PSW1 IN
1558	1	007B3	D730007D	K	13,7,3,SLRET+1	PSW1 OUT
1559	1	007B4	FFFFFFFF A	DATA	-1	R12 IN
1560	1	007B5	FFFFFFFF A	DATA	-1	R12 OUT
1561	1	007B6	0000052D	PZE,0	VRT-1	M1 IN
1562	1	007B7	0000052D	PZE,0	VRT-1	M1 OUT
1563	1	007B8	00000000 A	PZE		R13 IN
1564	1	007B9	00000000 A	PZE		R13 OUT
1565	1	007BA	00027FFF A	DATA	X'27FFF'	M2 IN

Address	Op Code	Operand 1	Operand 2	Suffix(2)	Register
1566	1 007BB	00027FFF	A	DATA	M2 OUT
1567	1 007BC	3272045C		LW,7	FMT
1568	1 007BD	32D2049C		LW,13	VM1/VMTR
1569	1 007BE	32D203FA		LW,13	VRTCH
1570	1 007BF	00000010	A	DATA	RC
1571					PSW-TRAP
1572	1 007C0	FFFFFFFF0	A	DATA	COUNT
1573	1 007C1	09C003D4		PSW,12	INSTRUCTION
1574	1 007C2	3730018E		K	PSW1 IN
1575	1 007C3	3730007D		K	PSW1 OUT
1576	1 007C4	FEDCBA98	A	DATA	R12 IN
1577	1 007C5	FEDCBA98	A	DATA	R12 OUT
1578	1 007C6	0000052D		PZE,0	M1 IN
1579	1 007C7	0000052D		PZE,0	M1 OUT
1580	1 007C8	01234567	A	DATA	R13 IN
1581	1 007C9	01234567	A	DATA	R13 OUT
1582	1 007CA	000C0003	A	DATA	M2 IN
1583	1 007CB	00000003	A	DATA	M2 OUT
1584	1 007CC	3272045C		LW,7	FMT
1585	1 007CD	32D2049C		LW,13	VM1/VMTR
1586	1 007CE	32D203FA		LW,13	VRTCH
1587	1 007CF	00000010	A	DATA	RC
1588					PSW-INDEX-TRAP
1589	1 007D0	FFFFFFFF0	A	DATA	COUNT
1590	1 007D1	09C203FC		PSW,12	INSTRUCTION
1591	1 007D2	2730018E		K	PSW1 IN
1592	1 007D3	2730007D		K	PSW1 OUT
1593	1 007D4	FFFFFFEC	A	DATA	R12 IN-INDEX
1594	1 007D5	FFFFFFEC	A	DATA	R12 OUT
1595	1 007D6	0000052D		PZE,0	M1 IN
1596	1 007D7	0000052D		PZE,0	M1 OUT
1597	1 007D8	FFFFFFFF	A	DATA	R13 IN
1598	1 007D9	FFFFFFFF	A	DATA	R13 OUT
1599	1 007DA	00000000	A	PZE	M2 IN
1600	1 007DB	00000000	A	PZE	M2 OUT
1601	1 007DC	3272045C		LW,7	FMT
1602	1 007DD	32D2049C		LW,13	VM1/VMTR
1603	1 007DE	32D203FA		LW,13	VRTCH

Address	OpCode	Hex	OpCode	SUFFIX(2)	RC
1604	1 007DF	00000010 A	DATA	16	RC
1605					PSW-INDIRECT ADDRESS-TRAP
1606	1 007E0	FFFFFFF0 A	DATA	-16	COUNT
1607	1 007E1	89C002EB	PSW,12	*WKIA	INSTRUCTION
1608	1 007E2	1730018E	K	1,7,3,SLSW	PSW1 IN
1609	1 007E3	1730007D	K	1,7,3,SLRET+1	PSW1 OUT
1610	1 007E4	00000000 A	PZE		R12 IN
1611	1 007E5	00000000 A	PZE		R12 OUT
1612	1 007E6	0000052D	PZE,0	VRT-1	M1 IN
1613	1 007E7	0000052D	PZE,0	VRT-1	M1 OUT
1614	1 007E8	000003D4	PZE,0	MEMORY	R13 IN
1615	1 007E9	000003D4	PZE,0	MEMORY	R13 OUT
1616	1 007EA	00007FFF A	DATA	X17FFF	M2 IN
1617	1 007EB	00007FFF A	DATA	X17FFF	M2 OUT
1618	1 007EC	3272045C	LW,7	MT1+64,1	FMT
1619	1 007ED	32D2049C	LW,13	MT2+64,1	VMT/VHTR
1620	1 007EE	32D203FA	LW,13	RT2-2,1	VRTCH
1621	1 007EF	00000010 A	DATA	16	RC
1622					PLM
1623	1 007F0	FFFFFFF0 A	DATA	-16	COUNT
1624	1 007F1	0A0003D4	PLM,0	MEMORY	INSTRUCTION
1625	1 007F2	073C0274	K	0,7,3,SETPSW	PSW1 IN
1626	1 007F3	17300276	K	1,7,3,LOC+2	PSW1 OUT
1627	1 007F4	FFFFFFF0 A	DATA	-1	R12 IN
1628	1 007F5	00000000 A	DATA	0	R12 OUT
1629	1 007F6	0000041B	PZE,0	RT3+15	M1 IN
1630	1 007F7	0000040B	PZE,0	RT3-1	M1 OUT
1631	1 007F8	00000000 A	DATA	0	R13 IN
1632	1 007F9	FFFFFFF0 A	DATA	-1	R13 OUT
1633	1 007FA	00000010 A	DATA	16	M2 IN
1634	1 007FB	00100000 A	DATA	X11000000	M2 OUT
1635	1 007FC	3272045C	LW,7	MT1+64,1	FMT
1636	1 007FD	32D2049C	LW,13	MT2+64,1	VMT/VHTR
1637	1 007FE	32D2040B	LW,13	RT3-1,1	VRTCH
1638	1 007FF	00000010 A	DATA	16	RC
1639					PLM-INDEXING
1640	1 00800	FFFFFFF0 A	DATA	-16	COUNT
1641	1 00801	0A0203D3	PLM,0	MEMORY+2,1	INSTRUCTION

Address	Op	Op Code	Op Data	SUFFIX(2)	Op Description
1642	↑	0080	10300244	K	1,0,3,SETPSW PSW1 IN
1643	1	00803	00300276	K	0,0,3,L0C+2 PSW1 OUT
1644	1	0804	FFFFFFFF A	DATA	-1 R12 IN-INDEX
1645	1	00805	FFFFFFFF A	DATA	-1 R12 OUT
1646	1	00806	0000040C	PZE,0	RT3 M1 IN
1647	1	00807	0000040B	PZE,0	RT3-1 M1 OUT
1648	1	00808	FFFFFFFF A	DATA	-1 R13 IN
1649	1	00809	FFFFFFFF A	DATA	-1 R13 OUT
1650	1	0080A	0000000A A	DATA	10 M2 IN
1651	1	0080B	00010009 A	DATA	X'10009' M2 OUT
1652	1	0080C	3272045C	LW,7	MT1+64,1 FMT
1653	1	0080D	32D2049C	LW,13	MT2+64,1 VM1/VMTR
1654	1	0080E	32D2040B	LW,13	RT3-1,1 VRTRCH
1655	1	0080F	00000001 A	DATA	1 RC
1656					PLN-INDIRECT ADDRESSING
1657	1	00810	FFFFFFF0 A	DATA	-16 COUNT
1658	1	00811	8A0002EB	PLN,0	*WKIA INSTRUCTION
1659	1	00812	17000244	K	1,7,0,SETPSW PSW1 IN
1660	1	00813	07000276	K	0,7,0,L0C+2 PSW1 OUT
1661	1	00814	00000000 A	PZE	R12 IN
1662	1	00815	00000000 A	PZE	R12 OUT
1663	1	00816	0000040C	PZE,0	RT3 M1 IN
1664	1	00817	0000040B	PZE,0	RT3-1 M1 OUT
1665	1	00818	00000304	PZE,0	MEMORY R13 IN-INDIRECT ADDRESSING
1666	1	00819	00000304	PZE,0	MEMORY R13 OUT
1667	1	0081A	00030002 A	DATA	X'30002' M2 IN
1668	1	0081B	00040001 A	DATA	X'40001' M2 OUT
1669	1	0081C	3272045C	LW,7	MT1+64,1 FMT
1670	1	0081D	32D2049C	LW,13	MT2+64,1 VM1/VMTR
1671	1	0081E	32D2040B	LW,13	RT3-1,1 VRTRCH
1672	1	0081F	00000001 A	DATA	1 RC
1673					PLN-INDIRECT ADDRESSING
1674	1	00820	FFFFFFF0 A	DATA	-16 COUNT
1675	1	00821	8A0202EB	PLN,0	*WKIA INSTRUCTION
1676	1	00822	F0000244	K	15,0,0,SETPSW PSW1 IN
1677	1	00823	00000276	K	0,0,0,L0C+2 PSW1 OUT
1678	1	00824	FFFFFFFF A	DATA	-1 R12 IN-INDEX
1679	1	00825	00000000 A	PZE	R12 OUT

Address	Op	Op-Code	Hex	Mode	SUFFIX(2)	Register/Field
1680	1	00826	0000041A		PZE,0	RT3+14
1681	1	00827	0000040B		PZE,0	RT3-1
1682	1	00828	000003D6		PZE,0	MEMORY+2
1683	1	00829	FFFFFFFF	A	DATA	-1
1684	1	0082A	00000010	A	DATA	16
1685	1	0082B	000F0001	A	DATA	X'F0001'
1686	1	0082C	3272045C		LW,7	MT1+64,1
1687	1	0082D	32D2049C		LW,13	MT2+64,1
1688	1	0082E	32D2040B		LW,13	RT3-1,1
1689	1	0082F	0000000F	A	DATA	15
1690						RC
1691	1	00830	FFFFFFFF0	A	DATA	-16
1692	1	00831	8A0202EB		PLM,0	*WKIA,1
1693	1	00832	C0000244		K	12,0,0,SETPSW
1694	1	00833	10000276		K	1,0,0,LOC+2
1695	1	00834	FFFFFFFF	A	DATA	-1
1696	1	00835	FFFFFFFF	A	DATA	-1
1697	1	00836	00000417		PZE,0	RT3+11
1698	1	00837	0000040B		PZE,0	RT3-1
1699	1	00838	000003D6		PZE,0	MEMORY+2
1700	1	00839	000003D6		PZE,0	MEMORY+2
1701	1	0083A	0FFF0000	A	DATA	X'FFF0000'
1702	1	0083B	100B0000	A	DATA	X'100B0000'
1703	1	0083C	3272045C		LW,7	MT1+64,1
1704	1	0083D	32D2049C		LW,13	MT2+64,1
1705	1	0083E	32D2040B		LW,13	RT3-1,1
1706	1	0083F	00000000	A	DATA	12
1707						RC
1708	1	00840	FFFFFFFF0	A	DATA	-16
1709	1	00841	0AF003D4		PLM,15	MEMORY
1710	1	00842	D0000244		K	13,0,0,SETPSW
1711	1	00843	20000276		K	2,0,0,LOC+2
1712	1	00844	FFFFFFFF	A	DATA	-1
1713	1	00845	FFFFFFFF	A	DATA	-1
1714	1	00846	0000041A		PZE,0	RT3+14
1715	1	00847	0000041A		PZE,0	RT3+14
1716	1	00848	00000000	A	PZE	
1717	1	00849	00000000	A	PZE	

M1 IN
M1 OUT
R12 IN-INDIRECT ADDRESS
R13 OUT
M2 IN
M2 OUT
FMT
VMT/VMTR
RC
PLM-INDIRECT ADDRESSING-INDEXING
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN-INDEX
R12 OUT
M1 IN
M1 OUT
R13 IN-INDIRECT ADDRESS
R13 OUT
M2 IN
M2 OUT
FMT
VMT/VMTR
VRTRCH
RC
PLM-ABORT
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
M1 IN
M1 OUT
R13 IN
R13 OUT

Address	Count	Hex	Label	SUFFIX(2)	Description
1718	1	0084A	00028 A	DATA	X'2800C' M2 IN
1719	1	0084B	00028 A	DATA	X'2800C' M2 OUT
1720	1	0084C	32720	LW,7	MT1+64,1 FMT
1721	1	0084D	32D20	LW,13	MT2+64,1 VMT/VMTR
1722	1	0084E	32D20	LW,13	RT3-1,1 VRTRCH
1723	1	0084F	00000 A	DATA	0 RC
1724					PLM-ABORT
1725	1	00850	FFFFFFFF0 A	DATA	-16 COUNT
1726	1	00851	0AC00304	PLM,12	MEMORY INSTRUCTIONS
1727	1	00852	C0000204	K	12,0,0,SETPSW PSW1 IN
1728	1	00853	30000276	K	3,0,0,LOC+2 PSW1 OUT
1729	1	00854	00000000 A	PZE	R12 IN
1730	1	00855	00000000 A	PZE	R12 OUT
1731	1	00856	00000417	PZE,0	RT3+11 M1 IN
1732	1	00857	00000417	PZE,0	RT3+11 M1 OUT
1733	1	00858	FFFFFFFF A	DATA	-1 R13 IN
1734	1	00859	FFFFFFFF A	DATA	-1 R13 OUT
1735	1	0085A	00018000 A	DATA	X'18000' M2 IN
1736	1	0085B	00018000 A	DATA	X'18000' M2 OUT
1737	1	0085C	3272045C	LW,7	MT1+64,1 FMT
1738	1	0085D	32D2049C	LW,13	MT2+64,1
1739	1	0085E	32D2040B	LW,13	RT3-1,1 VRTRCH
1740	1	0085F	00000000 A	DATA	0 RC
1741					PLM-INDIRECT ADDRESSING-INDEXING
1742	1	00860	FFFFFFFF0 A	DATA	-16 COUNT
1743	1	00861	8A0202EB	PLM,0	*WKIA,1 INSTRUCTION ABORT
1744	1	00862	90000204	K	9,0,0,SETPSW PSW1 IN
1745	1	00863	60000276	K	6,0,0,LOC+2 PSW1 OUT
1746	1	00864	FFFFFFFF A	DATA	-1 R12 IN-INDEX
1747	1	00865	FFFFFFFF A	DATA	-1 R12 OUT
1748	1	00866	00000414	PZE,0	RT3+8 M1 IN
1749	1	00867	00000414	PZE,0	RT3+8 M1 OUT
1750	1	00868	00000306	PZE,0	MEMORY+2 R13 IN-INDIRECT ADDRESS
1751	1	00869	00000306	PZE,0	MEMORY+2 R13 OUT
1752	1	0086A	00008008 A	DATA	X'8008' M2 IN
1753	1	0086B	00008008 A	DATA	X'8008' M2 OUT
1754	1	0086C	3272045C	LW,7	MT1+64,1 FMT
1755	1	0086D	32D2049C	LW,13	MT2+64,1 VMT/VMTR

			SUFFIX(2)			
1756	1	0086E	32D2040B	LW,13	RT3-1,1	VRTRCH
1757	1	0086F	00000000	DATA	0	RC
1758						PLM-ABORT
1759	1	00870	FFFFFFFF0	DATA	-16	COUNT
1760	1	00871	0A0003D4	PLM,0	MEMORY	INSTRUCTION
1761	1	00872	80000244	K	8,0,0,SETPSW	PSW1 IN
1762	1	00873	70000276	K	7,0,0,LOC+2	PSW1 OUT
1763	1	00874	00000000	PZE		R12 IN
1764	1	00875	00000000	PZE		R12 OUT
1765	1	00876	00000413	PZE,0	RT3+7	M1 IN
1766	1	00877	00000413	PZE,0	RT3+7	M1 OUT
1767	1	00878	FFFFFFFF	DATA	-1	R13 IN
1768	1	00879	FFFFFFFF	DATA	-1	R13 OUT
1769	1	0087A	00008C00	DATA	X'8000'	M2 IN
1770	1	0087B	00008000	DATA	X'8000'	M2 OUT
1771	1	0087C	3272045C	LW,7	MT1+64,1	FMT
1772	1	0087D	32D2049C	LW,13	MT2+64,1	VMT/VMTR
1773	1	0087E	32D2040B	LW,13	RT3-1,1	VRTRCH
1774	1	0087F	00000000	DATA	0	RC
1775						PLM-ABORT
1776	1	00880	FFFFFFFF0	DATA	-16	COUNT
1777	1	00881	0A0003D4	PLM,0	MEMORY	INSTRUCTION
1778	1	00882	70000244	K	7,0,0,SETPSW	PSW1 IN
1779	1	00883	80000276	K	8,0,0,LOC+2	PSW1 OUT
1780	1	00884	FFFFFFFFF	DATA	-1	R12 IN
1781	1	00885	FFFFFFFFF	DATA	-1	R12 OUT
1782	1	00886	00000412	PZE,0	RT3+6	
1783	1	00887	00000412	PZE,0	RT3+6	M1 OUT
1784	1	00888	00000000	PZE		R13 IN
1785	1	00889	00000000	PZE		R13 OUT
1786	1	0088A	FFF90008	DATA	X'FFF90008'	M2 IN
1787	1	0088B	FFF90008	DATA	X'FFF90008'	M2 OUT
1788	1	0088C	3272045C	LW,7	MT1+64,1	FMT
1789	1	0088D	32D2049C	LW,13	MT2+64,1	VMT/VMTR
1790	1	0088E	32D2040B	LW,13	RT3-1,1	VRTRCH
1791	1	0088F	00000000	DATA	0	RC
1792						PLM-ABORT
1793	1	00890	FFFFFFFF0	DATA	-16	COUNT

Address	Op	Op Code	Op Data	Op Name	Op Comment	Op Description
1794	1	00891	0A0003D4	PLM,0	MEMORY	INSTRUCTION
1795	1	00892	50000244	K	5,0,0,SETPSW	PSW1 IN
1796	1	00893	A0000276	K	10,0,0,L6C+2	PSW1 OUT
1797	1	00894	00000000	PZE		R12 IN
1798	1	00895	00000000	PZE		R12 OUT
1799	1	00896	00000410	PZE,0	RT3+4	M1 IN
1800	1	00897	00000410	PZE,0	RT3+4	M1 OUT
1801	1	00898	FFFFFFFF	DATA	-1	R13 IN
1802	1	00899	FFFFFFFF	DATA	-1	R13 OUT
1803	1	0089A	FFFB8004	DATA	X'FFFB8004'	M2 IN
1804	1	0089B	FFFB8004	DATA	X'FFFB8004'	M2 OUT
1805	1	0089C	3272045C	LW,7	MT1+64,1	FMT
1806	1	0089D	32D2049C	LW,13	MT2+64,1	VMT/VMTR
1807	1	0089E	32D2040B	LW,13	RT3-1,1	VRTRCH
1808	1	0089F	00000000	DATA	0	RC
1809						PLM-ABORT
1810	1	008A0	FFFFFFFF	DATA	-16	COUNT
1811	1	008A1	0A0003D4	PLM,0	MEMORY	INSTRUCTION
1812	1	008A2	40000244	K	4,0,0,SETPSW	PSW1 IN
1813	1	008A3	B0000276	K	11,0,0,L6C+2	PSW1 OUT
1814	1	008A4	FFFFFFFF	DATA	-1	R12 IN
1815	1	008A5	FFFFFFFF	DATA	-1	R12 OUT
1816	1	008A6	0000040F	PZE,0	RT3+3	M1 IN
1817	1	008A7	0000040F	PZE,0	RT3+3	M1 OUT
1818	1	008A8	00000000	PZE		R13 IN
1819	1	008A9	00000000	PZE		R13 OUT
1820	1	008AA	FFFC8000	DATA	X'FFFC8000'	M2 IN
1821	1	008AB	FFFC8000	DATA	X'FFFC8000'	M2 OUT
1822	1	008AC	3272045C	LW,7	MT1+64,1	FMT
1823	1	008AD	32D2049C	LW,13	MT2+64,1	VMT/VMTR
1824	1	008AE	32D2040B	LW,13	RT3-1,1	VRTRCH
1825	1	008AF	00000000	DATA	0	RC
1826						PLM-TRAP
1827	1	008B0	FFFFFFFF	DATA	-16	COUNT
1828	1	008B1	0A0003D4	PLM,0	MEMORY	INSTRUCTION
1829	1	008B2	D730018E	K	13,7,3,SLSW	PSW1 IN
1830	1	008B3	D730007D	K	13,7,3,SLRET+1	PSW1 OUT
1831	1	008B4	00000000	PZE		R12 IN

Address	Count	Hex	Label	SUFFIX(2)	Register/Field	
1870	1	008D9	00000000	A	PZE	
1871	1	008DA	00000008	A	DATA	8
1872	1	008DB	00000008	A	DATA	8
1873	1	008DC	3272045C		LW,7	MT1+04,1
1874	1	008DD	3202049C		LW,13	MT2+04,1
1875	1	008DE	3202040B		LW,13	RT3-1,1
1876	1	008DF	00000000	A	DATA	0
1877						RC
1878	1	008E0	FFFFFFFF0	A	DATA	-16
1879	1	008E1	0A1003D4		PLN,1	MEMORY
1880	1	008E2	873C018C		K	8,7,3:SLSW
1881	1	008E3	873C007D		K	8,7,3:SLRET+1
1882	1	008E4	00000000	A	PZE	
1883	1	008E5	00000000	A	PZE	
1884	1	008E6	00000413		PZE,0	RT3+7
1885	1	008E7	00000413		PZE,0	RT3+7
1886	1	008E8	FFFFFFFF	A	DATA	-1
1887	1	008E9	FFFFFFFF	A	DATA	-1
1888	1	008EA	00000300	A	PZE	
1889	1	008EB	00000000	A	PZE	
1890	1	008EC	3272045C		LW,7	MT1+04,1
1891	1	008ED	3202049C		LW,13	MT2+04,1
1892	1	008EE	3202040B		LW,13	RT3-1,1
1893	1	008EF	00000000	A	DATA	0
1894						RC
1895	1	008F0	FFFFFFFF0	A	DATA	-16
1896	1	008F1	0A2003D4		PLN,2	MEMORY
1897	1	008F2	7730034E		K	7,7,3:SLSW
1898	1	008F3	7730007D		K	7,7,3:SLRET+1
1899	1	008F4	FFFFFFFF	A	DATA	-1
1900	1	008F5	FFFFFFFF	A	DATA	-1
1901	1	008F6	00000413		PZE,0	RT3+6
1902	1	008F7	00000413		PZE,0	RT3+6
1903	1	008F8	00000000	A	PZE	
1904	1	008F9	00000000	A	PZE	
1905	1	008FA	7FF90008	A	DATA	X'7FF90008
1906	1	008FB	7FF90008	A	DATA	X'7FF90008
1907	1	008FC	3272045C		LW,7	MT1+04,1

R13 OUT
M2 IN
M2 OUT
FMT
VMT/VMTR
VRTRCH
RC
PLM-TRAP
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
M1 IN
M1 OUT
R13 IN
R13 OUT
M2 IN
M2 OUT
FMT
VMT/VMTR
VRTRCH
RC
PLM-TRAP
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
M1 IN
M1 OUT
R13 IN
R13 OUT
M2 IN
M2 OUT
FMT

Year	Hex	Address	Label	SUFFIX(2)	Register/Field
1909	1 008FD	32D2049C		LW,13	MT2+64,1
1909	1 008FE	32D2040B		LW,13	RT3-1,1
1910	1 008FF	00000000	A	DATA	0
1911					
1912	1 00900	FFFFFFF0	A	DATA	-16
1913	1 00901	0A3003D4		PLM,3	MEMORY
1914	1 00902	5730018E		K	5,7,3,SLSW
1915	1 00903	5730007D		K	5,7,3,SLRRT+1
1916	1 00904	00000000	A	PZE	
1917	1 00905	00000000	A	PZE	
1918	1 00906	00000010		PZE,0	RT3+4
1919	1 00907	00000010		PZE,0	RT3+4
1920	1 00908	FFFFFFFF	A	DATA	-1
1921	1 00909	FFFFFFFF	A	DATA	-1
1922	1 0090A	7FFB0004	A	DATA	X'7FFB0004
1923	1 0090B	7FFB0004	A	DATA	X'7FFB0004
1924	1 0090C	3272048C		LW,7	MT1+64,1
1925	1 0090D	32D2048C		LW,13	MT2+64,1
1926	1 0090E	32D2040B		LW,13	RT3-1,1
1927	1 0090F	00000000	A	DATA	0
1928					
1929	1 00910	FFFFFFF0	A	DATA	-16
1930	1 00911	0A4003D4		PLM,3	MEMORY
1931	1 00912	4730018E		K	4,7,3,SLSW
1932	1 00913	4730007D		K	4,7,3,SLRRT+1
1933	1 00914	FFFFFFFF	A	DATA	-1
1934	1 00915	FFFFFFFF	A	DATA	-1
1935	1 00916	00000010		PZE,0	RT3+3
1936	1 00917	00000010		PZE,0	RT3+3
1937	1 00918	00000000	A	PZE	
1938	1 00919	00000000	A	PZE	
1939	1 0091A	7FFC0000	A	DATA	X'7FFC0000
1940	1 0091B	7FFC0000	A	DATA	X'7FFC0000
1941	1 0091C	3272048C		LW,7	MT1+64,1
1942	1 0091D	32D2048C		LW,13	MT2+64,1
1943	1 0091E	32D2040B		LW,13	RT3-1,1
1944	1 0091F	00000000	A	DATA	0
1945					

VMT/VMTR
 VRTRCH
 RC
 PLM-TRAP
 COUNT
 INSTRUCTION
 PSW1 IN
 PSW1 OUT
 R12 IN
 R12 OUT
 M1 IN
 M1 OUT
 R13 IN
 R13 OUT
 M2 IN
 M2 OUT
 FMT
 VMT/VMTR
 VRTRCH
 RC
 PLM-TRAP
 COUNT
 INSTRUCTION
 PSW1 IN
 PSW1 OUT
 R12 IN
 R12 OUT
 M1 IN
 M1 OUT
 R13 IN
 R13 OUT
 M2 IN
 M2 OUT
 FMT
 VMT/VMTR
 VRTRCH
 RC
 PSW

			SUFFIX(2)	
1946	1 00920	FFFFFFFF0 A	DATA -16	COUNT
1947	1 00921	0B0003D4	PSM,0 MEMORY	INSTRUCTION
1948	1 00922	00300244	K 0,0,3,SETPSW	PSW1 IN
1949	1 00923	40300276	K 4,0,3,LOC+2	PSW1 OUT
1950	1 00924	01234567 A	DATA X'1234567'	R12 IN
1951	1 00925	01234567 A	DATA X'1234567'	R12 OUT
1952	1 00926	0000052D	PZE,0 VRT-1	M1 IN
1953	1 00927	0000053D	PZE,0 VRT+15	M1 OUT
1954	1 00928	FEDCBA98 A	DATA X'FEDCBA98'	R13 IN
1955	1 00929	FEDCBA98 A	DATA X'FEDCBA98'	R13 OUT
1956	1 0092A	00100000 A	DATA X'100000'	M2 IN
1957	1 0092B	00000010 A	DATA 16	M2 OUT
1958	1 0092C	3272045C	LW,7 MT1+64,1	FMT
1959	1 0092D	32D2049C	LW,13 MT2+64,1	VMT/VMTR
1960	1 0092E	32D203E8	LW,13 RT1-1,1	VRTRCH
1961	1 0092F	00000010 A	DATA 16	RC
1962				PSM
1963	1 00930	FFFFFFFF0 A	DATA -16	COUNT
1964	1 00931	0B0003D4	PSM,0 MEMORY	INSTRUCTION
1965	1 00932	07000244	K 0,7,0,SETPSW	PSW1 IN
1966	1 00933	07000276	K 0,7,0,LOC+2	PSW1 OUT
1967	1 00934	01234567 A	DATA X'1234567'	R12 IN
1968	1 00935	01234567 A	DATA X'1234567'	R12 OUT
1969	1 00936	0000052D	PZE,0 VRT-1	M1 IN
1970	1 00937	0000053D	PZE,0 VRT+15	M1 OUT
1971	1 00938	FEDCBA98 A	DATA X'FEDCBA98'	R13 IN
1972	1 00939	FEDCBA98 A	DATA X'FEDCBA98'	R13 OUT
1973	1 0093A	00110000 A	DATA X'110000'	M2 IN
1974	1 0093B	00010010 A	DATA X'10010'	M2 OUT
1975	1 0093C	3272045C	LW,7 MT1+64,1	FMT
1976	1 0093D	32D2049C	LW,13 MT2+64,1	VMT/VMTR
1977	1 0093E	32D203E8	LW,13 RT1-1,1	VRTRCH
1978	1 0093F	00000010 A	DATA 16	RC
1979				PSM-INDEXING
1980	1 00940	FFFFFFFF0 A	DATA -16	COUNT
1981	1 00941	0B028EA4	PSM,0 MEMORY+X'68AD01,1	INSTRUCTION
1982	1 00942	B0000244	K 11,0,0,SETPSW	PSW1 IN
1983	1 00943	00000276	K 0,0,0,LOC+2	PSW1 OUT

Year	Op	Op	Op	SUFFIX(2)	Op
1984	1	00944	FEDCBA98	A	DATA X'FEDCBA98' R12 IN-INDEX
1985	1	00945	FEDCBA98	A	DATA X'FEDCBA98' R12 OUT
1986	1	00946	0000052D		PZE,0 VRT-1 M1 IN
1987	1	00947	00000538		PZE,0 VRT+10 M1 OUT
1988	1	00948	01234567	A	DATA X'1234567' R13 IN
1989	1	00949	01234567	A	DATA X'1234567' R13 OUT
1990	1	0094A	7FFF7FF4	A	DATA X'7FFF7FF4' M2 IN
1991	1	0094B	7FF47FFF	A	DATA X'7FF47FFF' M2 OUT
1992	1	0094C	3272045C		LW,7 MT1+64,1 FMT
1993	1	0094D	32D2049C		LW,13 MT2+64,1 VMT/VMTR
1994	1	0094E	32D203EB		LW,13 RT1-1,1 VRTRCH
1995	1	0094F	0000000B	A	DATA 11 RC
1996					PSM-INDIRECT ADDRESSING
1997	1	00950	FFFFFFFF0	A	DATA -16 COUNT
1998	1	00951	8B0002EB		PSM,0 *WKIA INSTRUCTION
1999	1	00952	C0000244		K 12,0,0,SETPSW PSW1 IN
2000	1	00953	40000276		K 4,0,0,L0C+2 PSW1 OUT
2001	1	00954	FEDCBA98	A	DATA X'FEDCBA98' R12 IN
2002	1	00955	FEDCBA98	A	DATA X'FEDCBA98' R12 OUT
2003	1	00956	0000052D		PZE,0 VRT-1 M1 IN
2004	1	00957	00000539		PZE,0 VRT+11 M1 OUT
2005	1	00958	000003D4		PZE,0 MEMORY R13 IN-INDIRECT ADDRESS
2006	1	00959	01234567	A	DATA X'1234567' R13 OUT
2007	1	0095A	000C0001	A	DATA X'00001' M2 IN
2008	1	0095B	0000000D	A	DATA 13 M2 OUT
2009	1	0095C	3272045C		LW,7 MT1+64,1 FMT
2010	1	0095D	32D2049C		LW,13 MT2+64,1 VMT/VMTR
2011	1	0095E	32D203EB		LW,13 RT1-1,1 VRTRCH
2012	1	0095F	0000000C	A	DATA 12 RC
2013					PSM-INDIRECT ADDRESSING-INDEXING
2014	1	00960	FFFFFFFF0	A	DATA -16 COUNT
2015	1	00961	8B0202EB		PSM,0 *WKIA,1 INSTRUCTION
2016	1	00962	B0000244		K 11,0,0,SETPSW PSW1 IN
2017	1	00963	00000276		K 0,0,0,L0C+2 PSW1 OUT
2018	1	00964	FEDCBA98	A	DATA X'FEDCBA98' R12 IN-INDEX
2019	1	00965	FEDCBA98	A	DATA X'FEDCBA98' R12 OUT
2020	1	00966	0000052D		PZE,0 VRT-1 M1 IN
2021	1	00967	00000538		PZE,0 VRT+10 M1 OUT

			SUFFIX(2)			
2022	1	00968	00008EA4	PZE,0	MEMORY+X'68AD0'	R13 IN-INDIRECT ADDRESS
2023	1	00969	01234567 A	DATA	X'1234567'	R13 OUT
2024	1	0096A	000C0000 A	DATA	X'C0000'	M2 IN
2025	1	0096B	0001000B A	DATA	X'1000B'	M2 OUT
2026	1	0096C	3272045C	LW,7	MT1+64,1	FMT
2027	1	0096D	32D2049C	LW,13	MT2+64,1	VMT/VMTR
2028	1	0096E	32D203EB	LW,13	RT1-1,1	VRTRCH
2029	1	0096F	0000000B A	DATA	11	RC
2030						PSM-ABORT
2031	1	00970	FFFFFFFF0 A	DATA	-16	COUNT
2032	1	00971	0BC003D4	PSM,12	MEMORY	INSTRUCTION
2033	1	00972	D0000244	K	13,0,0,SETPSW	PSW1 IN
2034	1	00973	20000276	K	2,0,0,L0C+2	PSW1 OUT
2035	1	00974	FFFFFFFF A	DATA	-1	R12 IN
2036	1	00975	FFFFFFFF A	DATA	-1	R12 OUT
2037	1	00976	0000052D	PZE,0	VRT-1	M1 IN
2038	1	00977	0000052D	PZE,0	VRT-1	M1 OUT
2039	1	00978	00000000 A	PZE		R13 IN
2040	1	00979	00000000 A	PZE		R13 OUT
2041	1	0097A	000EFFFF3 A	DATA	X'000EFFFF3'	M2 IN
2042	1	0097B	000EFFFF3 A	DATA	X'000EFFFF3'	M2 OUT
2043	1	0097C	3272045C	LW,7	MT1+64,1	FMT
2044	1	0097D	32D2049C	LW,13	MT2+64,1	VMT/VMTR
2045	1	0097E	32D203EB	LW,13	RT1-1,1	VRTRCH
2046	1	0097F	00000000 A	DATA	0	RC
2047						PSM-INDIRECT ADDRESSING-ABORT
2048	1	00980	FFFFFFFF0 A	DATA	-16	COUNT
2049	1	00981	8BD002EB	PSM,13	*WKIA	INSTRUCTION
2050	1	00982	70000244	K	7,0,0,SETPSW	PSW1 IN
2051	1	00983	80000276	K	8,0,0,L0C+2	PSW1 OUT
2052	1	00984	00000000 A	PZE		R12 IN
2053	1	00985	00000000 A	PZE		R12 OUT
2054	1	00986	0000052D	PZE,0	VRT-1	M1 IN
2055	1	00987	0000052D	PZE,0	VRT-1	M1 OUT
2056	1	00988	000003D4	PZE,0	MEMORY	R13 IN
2057	1	00989	000003D4	PZE,0	MEMORY	R13 OUT
2058	1	0098A	80067FF8 A	DATA	X'80067FF8'	M2 IN
2059	1	0098B	80067FF8 A	DATA	X'80067FF8'	M2 OUT

Address	Count	Hex	Label	SUFFIX(2)	Operation
2060	1	0098C	3272045C	LW,7	MT1+64,1 FMT
2061	1	0098D	32D2049C	LW,13	MT2+64,1 VMT/VMTR
2062	1	0098E	32D203EB	LW,13	RT1-1,1 VRTRCH
2063	1	0098F	00000000	DATA	0 RC
2064					PSM-INDEXING-ABORT
2065	1	00990	FFFFFFF0	DATA	-16 COUNT
2066	1	00991	0B1203D2	PSM,1	MEMORY-2,1 INSTRUCTION
2067	1	00992	60000244	K	6,0,0,SETPSW PSW1 IN
2068	1	00993	90000276	K	9,0,0,LOC+2 PSW1 OUT
2069	1	00994	00000001	DATA	1 R12 IN-INDEX
2070	1	00995	00000001	DATA	1 R12 OUT
2071	1	00996	0000052D	PZE,0	VRT-1 M1 IN
2072	1	00997	0000052D	PZE,0	VRT-1 M1 OUT
2073	1	00998	FFFFFFFF	DATA	-1 R13 IN
2074	1	00999	FFFFFFFF	DATA	-1 R13 OUT
2075	1	0099A	80050000	DATA	X'80050000' M2 IN
2076	1	0099B	80050000	DATA	X'80050000' M2 OUT
2077	1	0099C	3272045C	LW,7	MT1+64,1 FMT
2078	1	0099D	32D2049C	LW,13	MT2+64,1 VMT/VMTR
2079	1	0099E	32D203EB	LW,13	RT1-1,1 VRTRCH
2080	1	0099F	00000000	DATA	0 RC
2081					PSM-INDIRECT ADDRESSING-INDEXING
2082	1	009A0	FFFFFFF0	DATA	-16 COUNT
2083	1	009A1	8B2202E2	PSM,2	*WKIA,1 INSTRUCTION ABORT
2084	1	009A2	50000244	K	5,0,0,SETPSW PSW1 IN
2085	1	009A3	A0000276	K	10,0,0,LOC+2 PSW1 OUT
2086	1	009A4	FFFFFFFF	DATA	-64 R12 IN-INDEX
2087	1	009A5	FFFFFFFF	DATA	-64 R12 OUT
2088	1	009A6	0000052D	PZE,0	VRT-1 M1 IN
2089	1	009A7	0000052D	PZE,0	VRT-1 M1 OUT
2090	1	009A8	00000454	PZE,0	MEMORY+128 R13 IN-INDIRECT ADDRESS
2091	1	009A9	00000454	PZE,0	MEMORY+128 R13 OUT
2092	1	009AA	8004FFFF	DATA	X'8004FFFF' M2 IN
2093	1	009AB	8004FFFF	DATA	X'8004FFFF' M2 OUT
2094	1	009AC	3272045C	LW,7	MT1+64,1 FMT
2095	1	009AD	32D2049C	LW,13	MT2+64,1 VMT/VMTR
2096	1	009AE	32D203EB	LW,13	RT1-1,1 VRTRCH
2097	1	009AF	00000000	DATA	0 RC

SUFFIX(2)

2098									
2099	1	00980	FFFFFFFF0	A	DATA	-16		PSM-ABORT	COUNT
2100	1	00981	0B3003D4		PSM,3	MEMORY			INSTRUCTION
2101	1	00982	30000244		K	3,0,0,SETPSW			PSW1 IN
2102	1	00983	C0000276		K	12,0,0,L0C+2			PSW1 OUT
2103	1	00984	FFFFFFFF	A	DATA	-1			R12 IN
2104	1	00985	FFFFFFFF	A	DATA	-1			R12 OUT
2105	1	00986	0000052D		PZE,0	VRT-1			M1 IN
2106	1	00987	0000052D		PZE,0	VRT-1			M1 OUT
2107	1	00988	00000000	A	PZE				R13 IN
2108	1	00989	00000000	A	PZE				R13 OUT
2109	1	0098A	80007FFC	A	DATA	X'80007FFC'			M2 IN
2110	1	0098B	80007FFC	A	DATA	X'80007FFC'			M2 OUT
2111	1	0098C	3272045C		LW,7	MT1+64,1			FMT
2112	1	0098D	32D2049C		LW,13	MT2+64,1			VMT/VMTX
2113	1	0098E	32D203EB		LW,13	RT1+1,1			VRTXCH
2114	1	0098F	00000000	A	DATA	0			RC
2115									PSM-ABORT
2116	1	009C0	FFFFFFFF0	A	DATA	-16		PSM-ABORT	COUNT
2117	1	009C1	0B4003D4		PSM,4	MEMORY			INSTRUCTION
2118	1	009C2	20000244		K	2,0,0,SETPSW			PSW1 IN
2119	1	009C3	D0000276		K	13,0-0,1,6,3,2			PSW1 OUT
2120	1	009C4	00000000	A	PZE				R12 IN
2121	1	009C5	00000000	A	PZE				R12 OUT
2122	1	009C6	0000052D		PZE,0	VRT-1			M1 IN
2123	1	009C7	0000052D		PZE,0	VRT-1			M1 OUT
2124	1	009C8	FFFFFFFF	A	DATA	-1			R13 IN
2125	1	009C9	FFFFFFFF	A	DATA	-1			R13 OUT
2126	1	009CA	80000000	A	DATA	X'80000000'			M2 IN
2127	1	009CB	80000000	A	DATA	X'80000000'			M2 OUT
2128	1	009CC	3272045C		LW,7	MT1+64,1			FMT
2129	1	009CD	32D2049C		LW,13	MT2+64,1			VMT/VMTX
2130	1	009CE	32D203EB		LW,13	RT1+1,1			VRTXCH
2131	1	009CF	00000000	A	DATA	0			RC
2132									PSM-ABORT
2133	1	009D0	FFFFFFFF0	A	DATA	-16		PSM-ABORT	COUNT
2134	1	009D1	0B5003D4		PSM,5	MEMORY			INSTRUCTION
2135	1	009D2	10000244		K	1,0,0,SETPSW			PSW1 IN

Address	Count	Hex	Symbol	Disassembly	Comment
2136	1	009D3	E0000276	SUFFIX(2)	
2137	1	009D4	FFFFFFFF A	K	14,0,0,LOC+2
2138	1	009D5	FFFFFFFF A	DATA	-1
2139	1	009D6	0000052D	DATA	-1
2140	1	009D7	0000052D	PZE,0	VRT-1
2141	1	009D8	00000000 A	PZE,0	VRT-1
2142	1	009D9	00000000 A	PZE	
2143	1	009DA	80000000 A	PZE	
2144	1	009DB	80000000 A	DATA	X'80000000'
2145	1	009DC	3272045C	DATA	X'80000000'
2146	1	009DD	32D2049C	LW,7	MT1+64,1
2147	1	009DE	32D203EB	LW,13	MT2+64,1
2148	1	009DF	00000000 A	LW,13	RT1-1,1
2149				DATA	0
2150	1	009E0	FFFFFFFF0 A		
2151	1	009E1	0B6003D4	DATA	-16
2152	1	009E2	D730018F	PSM,6	MEMORY
2153	1	009E3	D730007D	K	13,7,3,SECRET
2154	1	009E4	00000000 A	K	13,7,3,SECRET+1
2155	1	009E5	00000000 A	PZE	
2156	1	009E6	0000052D	PZE	
2157	1	009E7	0000052D	PZE,0	VRT-1
2158	1	009E8	FFFFFFFF A	PZE,0	VRT-1
2159	1	009E9	FFFFFFFF A	DATA	-1
2160	1	009EA	00007FF3 A	DATA	-1
2161	1	009EB	00007FF3 A	DATA	X'00007FF3'
2162	1	009EC	3272045C	DATA	X'00007FF3'
2163	1	009ED	32D2049C	LW,7	MT1+64,1
2164	1	009EE	32D203EB	LW,13	MT2+64,1
2165	1	009EF	00000000 A	LW,13	RT1-1,1
2166				DATA	0
2167	1	009F0	FFFFFFFF0 A		
2168	1	009F1	8B7202EB	DATA	-16
2169	1	009F2	7730018F	PSM,7	SECRET+1
2170	1	009F3	7730007D	K	7,7,3,SECRET
2171	1	009F4	FFFFFFFF A	K	7,7,3,SECRET+1
2172	1	009F5	FFFFFFFF A	DATA	-1
2173	1	009F6	0000052D	DATA	-1
				PZE,0	VRT-1

PSW1 OUT
R12 IN
R12 OUT
M1 IN
M1 OUT
R13 IN
R13 OUT
M2 IN
M2 OUT
FMT
VMT/VMTB
VRTCH
RC
PSM-TRAP
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
M1 IN
M1 OUT
R13 IN
R13 OUT
M2 IN
M2 OUT
FMT
VMT/VMTB
VRTCH
RC
PSM-INDIRECT ADDRESSING-INDEXED
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN-INDIRECT
R12 OUT
M1 IN

TRAP

			SUFFIX(2)			
2174	1	009F7	0000052D	PZE,0	VRT-1	M1 OUT
2175	1	009F8	000003D6	PZE,0	MEMORY+2	R13 IN-INDIRECT ADDRESS
2176	1	009F9	000003D6	PZE,0	MEMORY+2	R13 OUT
2177	1	009FA	00067FF9 A	DATA	X'00067FF9'	M2 IN
2178	1	009FB	00067FF9 A	DATA	X'00067FF9'	M2 OUT
2179	1	009FC	3272045C	LW,7	MT1+64,1	FMT
2180	1	009FD	32D2049C	LW,13	MT2+64,1	VMT/VMTR
2181	1	009FE	32D203EB	LW,13	RT1-1,1	VRTRCH
2182	1	009FF	00000000 A	DATA	0	RC
2183						PSM-TRAP
2184	1	00A00	FFFFFFFF0 A	DATA	-16	COUNT
2185	1	00A01	0B7003D4	PSM,7	MEMORY	INSTRUCTION
2186	1	00A02	6730018E	K	6,7,3,SLSW	PSW1 IN
2187	1	00A03	6730007D	K	6,7,3,SLRET+1	PSW1 OUT
2188	1	00A04	00000000 A	PZE		R12 IN
2189	1	00A05	00000000 A	PZE		R12 OUT
2190	1	00A06	0000052D	PZE,0	VRT-1	M1 IN
2191	1	00A07	0000052D	PZE,0	VRT-1	M1 OUT
2192	1	00A08	FFFFFFFF A	DATA	-1	R13 IN
2193	1	00A09	FFFFFFFF A	DATA	-1	R13 OUT
2194	1	00A0A	00050000 A	DATA	X'00050000'	M2 IN
2195	1	00A0B	00050000 A	DATA	X'00050000'	M2
2196	1	00A0C	3272045C	LW,7	MT1+64,1	FMT
2197	1	00A0D	32D2049C	LW,13	MT2+64,1	VMT/VMTR
2198	1	00A0E	32D203EB	LW,13	RT1-1,1	VRTRCH
2199	1	00A0F	00000000 A	DATA	0	RC
2200						PSM-TRAP
2201	1	00A10	FFFFFFFF0 A	DATA	-16	COUNT
2202	1	00A11	0B8003D4	PSM,8	MEMORY	INSTRUCTION
2203	1	00A12	5730018E	K	5,7,3,SLSW	PSW1 IN
2204	1	00A13	5730007D	K	5,7,3,SLRET+1	PSW1 OUT
2205	1	00A14	FFFFFFFF A	DATA	-1	R12 IN
2206	1	00A15	FFFFFFFF A	DATA	-1	R12 OUT
2207	1	00A16	0000052D	PZE,0	VRT-1	M1 IN
2208	1	00A17	0000052D	PZE,0	VRT-1	M1 OUT
2209	1	00A18	00000000 A	PZE		R13 IN
2210	1	00A19	00000000 A	PZE		R13 OUT
2211	1	00A1A	00047FFB A	DATA	X'00047FFB'	M2 IN

			SUFFIX(2)			
2212	1	00A1B	00047FFB	A	DATA X'00047FFB'	M2 OUT
2213	1	00A1C	3272045C		LW,7 MT1+64,1	FMT
2214	1	00A1D	32D2049C		LW,13 MT2+64,1	VMT/VMTR
2215	1	00A1E	32D203EB		LW,13 RT1-1,1	VRTRCH
2216	1	00A1F	00000000	A	DATA 0	RC
2217						
2218	1	00A20	FFFFFFFF0	A	DATA -16	PSM-TRAP
2219	1	00A21	0B9003D4		PSM,9 MEMORY	COUNT
2220	1	00A22	3730018E		K 3,7,3,SLSW	INSTRUCTION
2221	1	00A23	3730007D		K 3,7,3,SLRET+1	PSW1 IN
2222	1	00A24	00000000	A	PZE	PSW1 OUT
2223	1	00A25	00000000	A	PZE	R12 IN
2224	1	00A26	0000052D		PZE,0 VRT-1	R12 OUT
2225	1	00A27	0000052D		PZE,0 VRT-1	M1 IN
2226	1	00A28	FFFFFFFF	A	DATA -1	M1 OUT
2227	1	00A29	FFFFFFFF	A	DATA -1	R13 IN
2228	1	00A2A	00007FFC	A	DATA X'00007FFC'	R13 OUT
2229	1	00A2B	00007FFC	A	DATA X'00007FFC'	M2 IN
2230	1	00A2C	3272045C		LW,7 MT1+64,1	M2 OUT
2231	1	00A2D	32D2049C		LW,13 MT2+64,1	FMT
2232	1	00A2E	32D203EB		LW,13 RT1-1,1	VMT/VMTR
2233	1	00A2F	00000000	A	DATA 0	VRTRCH
2234						RC
2235	1	00A30	FFFFFFFF0	A	DATA -16	PSM-TRAP
2236	1	00A31	0BA003D4		PSM,10 MEMORY	COUNT
2237	1	00A32	2730018E		K 2,7,3,SLSW	INSTRUCTION
2238	1	00A33	2730007D		K 2,7,3,SLRET+1	PSW1 IN
2239	1	00A34	FFFFFFFF	A	DATA -1	PSW1 OUT
2240	1	00A35	FFFFFFFF	A	DATA -1	R12 IN
2241	1	00A36	0000052D		PZE,0 VRT-1	R12 OUT
2242	1	00A37	0000052D		PZE,0 VRT-1	M1 IN
2243	1	00A38	00000000	A	PZE	M1 OUT
2244	1	00A39	00000000	A	PZE	R13 IN
2245	1	00A3A	00000000	A	PZE	R13 OUT
2246	1	00A3B	00000000	A	PZE	M2 IN
2247	1	00A3C	3272045C		LW,7 MT1+64,1	M2 OUT
2248	1	00A3D	32D2049C		LW,13 MT2+64,1	FMT
2249	1	00A3E	32D203EB		LW,13 RT1-1,1	VMT/VMTR
						VRTRCH

Address	Count	Hex	Label	SUFFIX(2)	Operation
2250	1	00A3F	00000000	A	RC
2251					PSM-TRAP
2252	1	00A40	FFFFFFFF	A	COUNT
2253	1	00A41	03E003D4		INSTRUCTION
2254	1	00A42	1730018E		PSW1 IN
2255	1	00A43	1730007D		PSW1 OUT
2256	1	00A44	00000000	A	R12 IN
2257	1	00A45	00000000	A	R12 OUT
2258	1	00A46	0000052D		M1 IN
2259	1	00A47	0000052D		M1 OUT
2260	1	00A48	FFFFFFFF	A	R13 IN
2261	1	00A49	FFFFFFFF	A	R13 OUT
2262	1	00A4A	00007FFF	A	M2 IN
2263	1	00A4B	00007FFF	A	M2 OUT
2264	1	00A4C	3272045C		FMT
2265	1	00A4D	32D2049C		VMT/VMTR
2266	1	00A4E	32D203EB		VRTRCH
2267	1	00A4F	00000000	A	RC
2268					CVA
2269	1	00A50	FFFFFFFF	A	COUNT
2270	1	00A51	29C00590		INSTRUCTION
2271	1	00A52	073C0244		PSW1 IN
2272	1	00A53	07300276		PSW1 OUT
2273	1	00A54	FEDCBA98	A	R12 IN
2274	1	00A55	00000000	A	R12 OUT
2275	1	00A56	FEDCBA98	A	M1 IN
2276	1	00A57	FEDCBA98	A	M1 OUT
2277	1	00A58	00000000	A	R13 IN
2278	1	00A59	00000000	A	R13 OUT
2279	1	00A5A	00000000	A	M2 IN
2280	1	00A5B	00000000	A	M2 OUT
2281	1	00A5C	3272045C		FMT
2282	1	00A5D	32D2049C		VMT/VMTR
2283					CVA-INDEXING
2284	1	00A5E	FFFFFFFF	A	COUNT
2285	1	00A5F	29C20590		INSTRUCTION
2286	1	00A60	F0300244		PSW1 IN
2287	1	00A61	D0300276		PSW1 OUT

SUFFIX(2)

DATA 0
 DATA -16
 PSM, 11 MEMORY
 K 1,7,3,SLSW
 K 1,7,3,SLRET+1
 PZE
 PZE
 PZE, 0 VRT-1
 PZE, 0 VRT-1
 DATA -1
 DATA -1
 DATA X'00007FFF'
 DATA X'00007FFF'
 LW, 7 MT1+64, 1
 LW, 13 MT2+64, 1
 LW, 13 RT1-1, 1
 DATA 0
 DATA -14
 CVA, 12 VMT
 K 0,7,3,SETPSW
 K 0,7,3,L0C+2
 DATA X'FEDCBA98'
 DATA 0
 DATA X'FEDCBA98'
 DATA X'FEDCBA98'
 DATA 0
 DATA 0
 DATA 0
 DATA 0
 LW, 7 MT1+64, 1
 LW, 13 MT2+64, 1
 DATA -14
 CVA, 12 VMT, 1
 K 15,0,3,SETPSW
 K 13,0,3,L0C+2

*

*

*

Address	Count	Hex	Mask	SUFFIX(2)	Operation
2288	1	00A62	00000020	A	DATA 32 R12 IN-INDEX
2289	1	00A63	FFFFFFFF	A	DATA -1 R12 OUT
2290	1	00A64	00000000	A	DATA 0 M1 IN
2291	1	00A65	00000000	A	DATA 0 M1 OUT
2292	1	00A66	FFFF0000	A	DATA X'FFFF0000' R13 IN
2293	1	00A67	FFFF0000	A	DATA X'FFFF0000' R13 OUT
2294	1	00A68	FFFFFFFF	A	DATA -1 M2 IN
2295	1	00A69	FFFFFFFF	A	DATA -1 M2 OUT
2296	1	00A6A	3272045C		LW,7 MT1+64,1 FMT
2297	1	00A6B	32D2049C		LW,13 MT2+64,1 VMT/VMIR
2298					CVA
2299	1	00A6C	FFFFFFFF2	A	DATA -14 COUNT
2300	1	00A6D	29C00590		CVA,12 VMT INSTRUCTION
2301	1	00A6E	A7000244		K 10,7,0,SETPSW PSW1 IN
2302	1	00A6F	17000276		K 1,7,0,LBC+2 PSW1 OUT
2303	1	00A70	00000000	A	DATA 0 R12 IN
2304	1	00A71	FFFFFFFF	A	DATA -1 R12 OUT
2305	1	00A72	FFFFFFFF	A	DATA -1 M1 IN
2306	1	00A73	FFFFFFFF	A	DATA -1 M1 OUT
2307	1	00A74	FFFFFFFF	A	DATA -1 R13 IN
2308	1	00A75	FFFFFFFF	A	DATA -1 R13 OUT
2309	1	00A76	00000000	A	DATA 0 M2 IN
2310	1	00A77	00000000	A	DATA 0 M2 OUT
2311	1	00A78	3272045C		LW,7 MT1+64,1 FMT
2312	1	00A79	32D2049C		LW,13 MT2+64,1 VMT/VMIR
2313					CVA-INDIRECT ADDRESSING
2314	1	00A7A	FFFFFFFF2	A	DATA -14 COUNT
2315	1	00A7B	A9C002E8		CVA,12 *WKIA INSTRUCTION
2316	1	00A7C	50000244		K 5,0,0,SETPSW PSW1 IN
2317	1	00A7D	60000276		K 6,0,0,LBC+2 PSW1 OUT
2318	1	00A7E	FFFFFFFF	A	DATA -1 R12 IN
2319	1	00A7F	09A00000	A	DATA X'9A00000' M1 IN
2320	1	00A80	FFFFFFFF	A	DATA -1 M1 OUT
2321	1	00A81	FFFFFFFF	A	DATA -1 M1 OUT
2322	1	00A82	00000390		PZE,0 VMT R13 IN
2323	1	00A83	00000590		PZE,0 VMT R13 OUT
2324	1	00A84	00000000	A	PZE M2 IN
2325	1	00A85	00000000	A	PZE M2 OUT

			SUFFIX(2)			
2326	1	00A86	3272045C	LW,7	MT1+64,1	FMT
2327	1	00A87	32D2049C	LW,13	MT2+64,1	VMT/VMTR
2328						CVA
2329	1	00A88	FFFFFFF2 A	DATA	-14	COUNT
2330	1	00A89	29C005B0	CVA,12	VMT+32	INSTRUCTION
2331	1	00A8A	00100244	K	0,0,1,SETPSW	PSW1 IN
2332	1	00A8B	90100276	K	9,0,1,LBC+2	PSW1 OUT
2333	1	00A8C	00000000 A	PZE		R12 IN
2334	1	00A8D	80000001 A	DATA	X'80000001'	R12 OUT
2335	1	00A8E	FFFFFFF7 A	DATA	-1	M1 IN
2336	1	00A8F	FFFFFFF7 A	DATA	-1	M1 OUT
2337	1	00A90	FFFFFFF7 A	DATA	-1	R13 IN
2338	1	00A91	FFFFFFF7 A	DATA	-1	R13 OUT
2339	1	00A92	00000000 A	PZE		M2 IN
2340	1	00A93	00000000 A	PZE		M2 OUT
2341	1	00A94	3272045C	LW,7	MT1+64,1	FMT
2342	1	00A95	32D2049C	LW,13	MT2+64,1	VMT/VMTR
2343						CVS
2344	1	00A96	FFFFFFF2 A	DATA	-14	COUNT
2345	1	00A97	28C00590	CVS,12	VMT	INSTRUCTION
2346	1	00A98	07300244	K	0,7,3,SETPSW	PSW1 IN
2347	1	00A99	07300276	K	0,7,3,LBC+2	PSW1 OUT
2348	1	00A9A	00000000 A	DATA	0	R12 IN
2349	1	00A9B	00000000 A	DATA	0	R12 OUT
2350	1	00A9C	FEDCBA98 A	DATA	X'FEDCBA98'	M1 IN
2351	1	00A9D	FEDCBA98 A	DATA	X'FEDCBA98'	M1 OUT
2352	1	00A9E	FFFFFFF7 A	DATA	-1	R13 IN
2353	1	00A9F	00000000 A	DATA	0	R13 OUT
2354	1	00AA0	FFFFFFF7 A	DATA	-1	M2 IN
2355	1	00AA1	FFFFFFF7 A	DATA	-1	M2 OUT
2356	1	00AA2	3272045C	LW,7	MT1+64,1	FMT
2357	1	00AA3	32D2045C	LW,13	MT1+64,1	VMT/VMTR
2358						CVS
2359	1	00AA4	FFFFFFF2 A	DATA	-14	COUNT
2360	1	00AA5	28C005B0	CVS,12	VMT+32	INSTRUCTION
2361	1	00AA6	00300244	K	0,0,3,SETPSW	PSW1 IN
2362	1	00AA7	20300276	K	2,0,3,LBC+2	PSW1 OUT
2363	1	00AA8	FFFFFFF7 A	DATA	X'FFFFFFF7'	R12 IN

Address	Count	Hex	Label	SUFFIX(2)	Operation
2364	1	00AA9	0696968C	A	DATA X'696968C'
2365	1	00AAA	000C0000	A	PZE
2366	1	00AAB	00000000	A	PZE
2367	1	00AAC	F0F0F0F0	A	DATA X'F0F0F0F0'
2368	1	00AAD	7539FEEE	A	DATA X'7539FEEE'
2369	1	00AAE	FFFFFFFF	A	DATA -1
2370	1	00AAF	FFFFFFFF	A	DATA -1
2371	1	00AB0	3272045C		LW,7 MT1+64,1
2372	1	00AB1	32D2049C		LW,13 MT2+64,1
2373					CVS
2374	1	00AB2	FFFFFFFF2	A	DATA -14
2375	1	00AB3	28C005B0		CVS,12 VMT+32
2376	1	00AB4	F7000244		K 15,7,0,SETPSW
2377	1	00AB5	D7000276		K 13,7,0,L0C+2
2378	1	00AB6	FFFFFFFF	A	DATA X'FFFFFFFF'
2379	1	00AB7	00000001	A	DATA 1
2380	1	00AB8	FFFFFFFF	A	DATA -1
2381	1	00AB9	FFFFFFFF	A	DATA -1
2382	1	00ABA	FFFFFFFF	A	DATA -1
2383	1	00ABB	EA73FDDD	A	DATA X'EA73FDDD'
2384	1	00ABC	000C0000	A	PZE
2385	1	00ABD	00000000	A	PZE
2386	1	00ABE	3272045C		LW,7 MT1+64,1
2387	1	00ABF	32D2049D		LW,13 MT2+65,1
2388					CVS-INDEXING
2389	1	00AC0	FFFFFFFF2	A	DATA -14
2390	1	00AC1	28C205B1		CVS,12 VMT+33,1
2391	1	00AC2	50000244		K 5,0,0,SETPSW
2392	1	00AC3	50000276		K 5,0,0,L0C+2
2393	1	00AC4	FFFFFFFF	A	DATA -1
2394	1	00AC5	00000000	A	DATA 0
2395	1	00AC6	F0F0F0F0	A	DATA X'F0F0F0F0'
2396	1	00AC7	FCF0F0F0	A	DATA X'FCF0F0F0'
2397	1	00AC8	0F0F0F0F	A	DATA X'0F0F0F0F'
2398	1	00AC9	E000AAAA	A	DATA X'E000AAAA'
2399	1	00ACA	0F0F0F0F	A	DATA X'0F0F0F0F'
2400	1	00ACB	0F0F0F0F	A	DATA X'0F0F0F0F'
2401	1	00ACC	3272045C		LW,7 MT1+64,1

R12 OUT
M1 IN
M1 OUT
R13 IN
R13 OUT
M2 IN
M2 OUT
FMT
VMT/VMTR
CVS
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
M1 IN
M1 OUT
R13 IN
R13 OUT
M2 IN
M2 OUT
FMT
VMT/VMTR
CVS-INDEXING
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN-INDEX
R12 OUT
M1 IN
M1 OUT
R13 IN
R13 OUT
M2 IN
M2 OUT
FMT

2402	1	00ACD	32D2049C
2403			
2404	1	00ACE	FFFFFFFF2 A
2405	1	00ACF	A8C002EB
2406	1	00AD0	A0000244
2407	1	00AD1	90000276
2408	1	00AD2	FFFFFFFF A
2409	1	00AD3	00000000 A
2410	1	00AD4	0F0F0F0F A
2411	1	00AD5	0F0F0F0F A
2412	1	00AD6	00000590
2413	1	00AD7	FFFFFFFF A
2414	1	00AD8	F0F0F0F0 A
2415	1	00AD9	F0F0F0F0 A
2416	1	00ADA	3272045C
2417	1	00ADB	32D2049C
2418			
2419	1	00ADC	FFFFFFFF0 A
2420	1	00ADD	61C00000 A
2421	1	00ADE	00000244
2422	1	00ADF	00000276
2423	1	00AE0	00001538
2424	1	00AE1	00001637
2425	1	00AE2	F0F0FCF0 A
2426	1	00AE3	F0F0F0F0 A
2427	1	00AE4	FF001640
2428	1	00AE5	0000173F
2429	1	00AE6	0F0F0F0F A
2430	1	00AE7	0F0F0F0F A
2431	1	00AE8	3272045C
2432	1	00AE9	32D2049C
2433	1	00AEA	00001070
2434	1	00AEB	000000FF A
2435			
2436	1	00AEC	FFFFFFFF0 A
2437	1	00AED	61C00C09 A
2438	1	00AEE	57100244
2439	1	00AEF	57100276

SUFFIX(2)	
LW,13	MT2+64,1
DATA	-14
CVS,12	*WKIA
K	10,0,0,SETPSW
K	9,0,0,L8C+2
DATA	-1
DATA	0
DATA	X'F0F0F0F0'
DATA	X'F0F0F0F0'
PZE,0	VMT
DATA	-1
DATA	X'F0F0F0F0'
DATA	X'F0F0F0F0'
LW,7	MT1+64,1
LW,13	MT2+64,1
DATA	-16
MBS,12	0
K	0,0,0,SETPSW
K	0,0,0,L8C+2
J	0,BA(FMT)
J	0,BA(FMT)+255
DATA	X'FCF0F0F0'
DATA	X'FCF0F0F0'
J	X'FF',BA(VMT)
J	0,BA(VMT)+255
DATA	X'F0FCF0F0'
DATA	X'F0F0F0F0'
LW,7	MT1+64,1
LW,13	MT2+64,1
J	0,BA(MT1)
DATA	255
DATA	-16
MBS,12	9
K	5,7,1,SETPSW
K	5,7,1,L8C+2

VMT/VMTR
CVS-INDIRECT ADDRESSING
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
M1 IN
M1 OUT
R13 IN-INDIRECT ADDRESS
R13 OUT
M2 IN
M2 OUT
FMT
VMT/VMTR
MBS
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
RO IN
RO OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/UMTR
VMTRCH
MC
MBS
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT

35 D 00 388
R12 = 3252045C
R13 = 00000000

R12 =
R13 =

Address	Op	Op Code	Op Mod	Op Type	SUFFIX(2)	Op Description	
2440	1	00AF0	0000152F		J	0,BA(FMT)-9	R12 IN
2441	1	00AF1	00001538		J	0,BA(FMT)	R12 OUT
2442	1	00AF2	FFFFFFFF	A	DATA	-1	RO IN
2443	1	00AF3	FFFFFFFF	A	DATA	-1	RO OUT
2444	1	00AF4	09001640		J	9,BA(VMT)	R13 IN
2445	1	00AF5	00001649		J	0,BA(VMT)+9	R13 OUT
2446	1	00AF6	00000000	A	DATA	0	R1 IN
2447	1	00AF7	00000000	A	DATA	0	R1 OUT
2448	1	00AF8	3272045C		LW,7	MT1+64,1	FMT
2449	1	00AF9	32D2049C		LW,13	MT2+64,1	VMT/VMTR
2450	1	00AFA	00001070		J	0,BA(MT1)	VMTRCH
2451	1	00AFB	00000009	A	DATA	9	MC
2452							MBS
2453	1	00AFC	FFFFFFFF0	A	DATA	-16	COUNT
2454	1	00AFD	61C0000A	A	MBS,12	10	INSTRUCTION
2455	1	00AFE	A3200244		K	10,3,2,SETPSW	PSW1 IN
2456	1	00AFF	A3200276		K	10,3,2,L0C+2	PSW1 OUT
2457	1	00B00	0000152E		J	0,BA(FMT)-10	R12 IN
2458	1	00B01	00001538		J	0,BA(FMT)	R12 OUT
2459	1	00B02	01234567	A	DATA	X'1234567'	RO IN
2460	1	00B03	01234567	A	DATA	X'1234567'	RO OUT
2461	1	00B04	0A001640		J	10,BA(VMT)	R13 IN
2462	1	00B05	0000164A		J	0,BA(VMT)+10	R13 OUT
2463	1	00B06	FEDCBA98	A	DATA	X'FEDCBA98'	R1 IN
2464	1	00B07	FEDCBA98	A	DATA	X'FEDCBA98'	R1 OUT
2465	1	00B08	3272045C		LW,7	MT1+64,1	FMT
2466	1	00B09	32D2049C		LW,13	MT2+64,1	VMT/VMTR
2467	1	00B0A	00001070		J	0,BA(MT1)	VMTRCH
2468	1	00B0B	0000000A	A	DATA	10	MC
2469							MBS-MOVE ZERO BYTES
2470	1	00B0C	FFFFFFFF0	A	DATA	-16	COUNT
2471	1	00B0D	61C00014	A	MBS,12	20	INSTRUCTION
2472	1	00B0E	F1300244		K	15,1,3,SETPSW	PSW1 IN
2473	1	00B0F	F1300276		K	15,1,3,L0C+2	PSW1 OUT
2474	1	00B10	00001524		J	0,BA(FMT)-20	R12 IN
2475	1	00B11	00001524		J	0,BA(FMT)-20	R12 OUT
2476	1	00B12	FFFFFFFF	A	DATA	-1	RO IN
2477	1	00B13	FFFFFFFF	A	DATA	-1	RO OUT

R12 =
R13 =

R12 =
R13 =

2478	1	00B14	00001640	
2479	1	00B15	00001640	
2480	1	00B16	00000000	A
2481	1	00B17	00000000	A
2482	1	00B18	3272045C	
2483	1	00B19	32D2049C	
2484	1	00B1A	00001640	
2485	1	00B1B	00000000	A
2486				
2487	1	00B1C	FFFFFFFF0	A
2488	1	00B1D	61DFFEF8	A
2489	1	00B1E	F0300244	
2490	1	00B1F	F0300276	
2491	1	00B20	00000000	A
2492	1	00B21	00000000	A
2493	1	00B22	FFFFFFFF	A
2494	1	00B23	FFFFFFFF	A
2495	1	00B24	FF001640	
2496	1	00B25	0000173F	
2497	1	00B26	00000000	A
2498	1	00B27	00000000	A
2499	1	00B28	3272045C	
2500	1	00B29	32D2049C	
2501	1	00B2A	00001070	
2502	1	00B2B	000000FF	A
2503				
2504	1	00B2C	FFFFFFFF0	A
2505	1	00B2D	61DFFEF8	A
2506	1	00B2E	B2000244	
2507	1	00B2F	B2000276	
2508	1	00B30	FFFFFFFF	A
2509	1	00B31	FFFFFFFF	A
2510	1	00B32	FFFFFFFF	A
2511	1	00B33	FFFFFFFF	A
2512	1	00B34	0B001640	
2513	1	00B35	0000164B	
2514	1	00B36	FFFFFFFF	A
2515	1	00B37	FFFFFFFF	A

SUFFIX(2)

J	0,BA(VMT)
J	0,BA(VMT)
PZE	
PZE	
LW,7	MT1+64,1
LW,13	MT2+64,1
J	0,BA(VMT)
DATA	0
DATA	-16
MBS,13	-264
K.	15,0,3,SETPSW
K	15,0,3,L0C+2
DATA	0
DATA	0
DATA	-1
DATA	-1
J	X'FF',BA(VMT)
J	0,BA(VMT)+255
DATA	0
DATA	0
LW,7	MT1+64,1
LW,13	MT2+64,1
J	0,BA(MT1)
DATA	255
DATA	-16
MBS,13	-264
K	11,2,0,SETPSW
K	11,2,0,L0C+2
DATA	-1
DATA	-1
DATA	-1
DATA	-1
J	11,BA(VMT)
J	0,BA(VMT)+11
DATA	-1
DATA	-1

R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC
MBS-0DD REGISTER
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
RO IN
RO OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC
MBS-0DD REGISTER
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
RO IN
RO OUT
R13 IN
R13 OUT
R1 IN
R1 OUT

R13=
R14=

R13=
R14=

Address	Op	Op	Op	SUFFIX(2)	Op
2516	1	00B33	3272045C	LW,7	MT1+64,1
2517	1	00B39	32D2049C	LW,13	MT2+64,1
2518	1	00B3A	00001070	J	0,BA(MT1).
2519	1	00B3B	0000000B	DATA	11
2520					
2521	1	00B3C	FFFFFFF0	DATA	-16
2522	1	00B3D	6100153B	MBS,0	BA(FMT)+3
2523	1	00B3E	04000244	K	0,4,0,SETPSW
2524	1	00B3F	04000276	K	0,4,0,L8C+2
2525	1	00B40	FFFFFFFF	DATA	-1
2526	1	00B41	FFFFFFFF	DATA	-1
2527	1	00B42	FFFFFFFF	DATA	-1
2528	1	00B43	FFFFFFFF	DATA	-1
2529	1	00B44	00000000	DATA	0
2530	1	00B45	00000000	DATA	0
2531	1	00B46	05001640	J	5,BA(VMT)
2532	1	00B47	00001645	J	0,BA(VMT)+5
2533	1	00B48	3272045C	LW,7	MT1+64,1
2534	1	00B49	32D2049C	LW,13	MT2+64,1
2535	1	00B4A	0000129C	J	0,BA(MT4)+36
2536	1	00B4B	00000005	DATA	5
2537					
2538	1	00B4C	FFFFFFF0	DATA	-16
2539	1	00B4D	6100153B	MBS,0	BA(FMT)+3
2540	1	00B4E	C0000244	K	12,0,0,SETPSW
2541	1	00B4F	C0000276	K	12,0,0,L8C+2
2542	1	00B50	00000000	DATA	0
2543	1	00B51	00000000	DATA	0
2544	1	00B52	00000000	DATA	0
2545	1	00B53	00000000	DATA	0
2546	1	00B54	00000000	DATA	0
2547	1	00B55	00000000	DATA	0
2548	1	00B56	0CC01640	J	12,BA(VMT)
2549	1	00B57	0000164C	J	0,BA(VMT)+12
2550	1	00B58	3272045C	LW,7	MT1+64,1
2551	1	00B59	32D2049C	LW,13	MT2+64,1
2552	1	00B5A	0000129C	J	C,BA(MT4)+36
2553	1	00B5B	0000000C	DATA	12

FMT
VMT/VMTR
VMTRCH
MC
MBS-REGISTER 0
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
R0 IN
R0 OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC
MBS-REGISTER 0
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
R0 IN
R0 OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC

RO =
RI =

RO =
RI =

SUFFIX(2)

MBS-INDIRECT ADDRESSING-TRAP

Address	Op	Op Code	Address	Op	Op Code	Op Code	Op Code
2554							
2555	1	00B5C	FFFFFFF0	A	DATA	-16	COUNT
2556	1	00B5D	E1C002E9	A	DATA	X'E1C002E9'	INSTRUCTION
2557	1	00B5E	17300185		K	1,7,3,SI9NA0	PSW1 IN
2558	1	00B5F	9730006B		K	9,7,3,NEIRET+1	PSW1 OUT
2559	1	00B60	00001538		J	0,BA(FMT)	R12 IN
2560	1	00B61	00001538		J	0,BA(FMT)	R12 OUT
2561	1	00B62	FFFFFFF7	A	DATA	-1	RO IN
2562	1	00B63	FFFFFFF7	A	DATA	-1	RO OUT
2563	1	00B64	00001640		J	0,BA(VMT)	R13 IN
2564	1	00B65	00001640		J	0,BA(VMT)	R13 OUT
2565	1	00B66	00000000	A	PZE		R1 IN
2566	1	00B67	00000000	A	PZE		R1 OUT
2567	1	00B68	3272045C		LW,7	MT1+64,1	FMT
2568	1	00B69	32D2049C		LW,13	MT2+64,1	VMT/VMTR
2569	1	00B6A	00001070		J	0,BA(MT1)	VMTRCH
2570	1	00B6B	00000000	A	DATA	0	MC
2571							MBS-NOT WORD BOUNDARY-OVERLAP
2572	1	00B6C	FFFFFFF0	A	DATA	-16	COUNT
2573	1	00B6D	61C00000	A	MBS,12	0	INSTRUCTION
2574	1	00B6E	00000244		K	0,0,0,SETPSW	PSW1 IN
2575	1	00B6F	00000276		K	0,0,0,LOC+2	PSW1 OUT
2576	1	00B70	00001640		J	0,BA(VMT)	R12 IN
2577	1	00B71	00001648		J	0,BA(VMT)+8	R12 OUT
2578	1	00B72	FFFFFFF7	A	DATA	-1	RO IN
2579	1	00B73	FFFFFFF7	A	DATA	-1	RO OUT
2580	1	00B74	08001642		J	8,BA(VMT)+2	R13 IN
2581	1	00B75	0000164A		J	0,BA(VMT)+10	R13 OUT
2582	1	00B76	00000000	A	PZE		R1 IN
2583	1	00B77	00000000	A	PZE		R1 OUT
2584	1	00B78	3272045C		LW,7	MT1+64,1	FMT
2585	1	00B79	32D2045C		LW,13	MT1+64,1	VMT/VMTR
2586	1	00B7A	00C012A0		J	0,BA(MT4)+40	VMTRCH
2587	1	00B7B	0000000C	A	DATA	12	MC
2588							CBS
2589	1	00B7C	FFFFFFF0	A	DATA	-16	COUNT
2590	1	00B7D	60C00000	A	CBS,12	0	INSTRUCTION
2591	1	00B7E	30000244		K	3,0,0,SETPSW	PSW1 IN

R12=
R13=

R12=
R13=

E

B

0001 0100 0100
4 5 9 0

0 DATA -16
1 MBS,12 0
2 K 0,0,0,SETPSW
3 K 0,0,0,LOC+2
4 J 0,BA(VMT)
5 J 0,BA(VMT)+8
6 DATA -1
7 DATA -1
8 J 8,BA(VMT)+2
9 J 0,BA(VMT)+10
10 PZE
11 PZE
12 LW,7 MT1+64,1
13 LW,13 MT1+64,1
14 J 0,BA(MT4)+40
15 DATA 12

			SUFFIX(2)			
2592	1	00B7F	00000276	K	0,0,0,L8C+2	PSW1 OUT
2593	1	00B80	00001538	J	0,BA(FMT)	R12 IN
2594	1	00B81	00001637	J	0,BA(FMT)+255	R12 OUT
2595	1	00B82	F0F0F0F0 A	DATA	X'F0F0F0F0'	RO IN
2596	1	00B83	F0F0F0F0 A	DATA	X'F0F0F0F0'	RO OUT
2597	1	00B84	FF001640	J	X'FF',BA(VMT)	R13 IN
2598	1	00B85	0000173F	J	0,BA(VMT)+255	R13 OUT
2599	1	00B86	0F0F0F0F A	DATA	X'F0F0F0F0'	R1 IN
2600	1	00B87	0F0F0F0F A	DATA	X'F0F0F0F0'	R1 OUT
2601	1	00B88	3272045C	LW,7	MT1+64,1	FMT
2602	1	00B89	32D2045C	LW,13	MT1+64,1	VMT/VMTR
2603	1	00B8A	00001070	J	0,BA(MT1)	VMTRCH
2604	1	00B8B	00000000 A	DATA	0	
2605						CBS-COMPARE ZERO BYTES
2606	1	00B8C	FFFFFFFF0 A	DATA	-16	COUNT
2607	1	00B8D	60C00014 A	CBS,12	20	INSTRUCTION
2608	1	00B8E	37300244	K	3,7,3,SETPSW	PSW1 IN
2609	1	00B8F	07300276	K	0,7,3,L8C+2	PSW1 OUT
2610	1	00B90	00001524	J	0,BA(FMT)-20	R12 IN
2611	1	00B91	00001524	J	0,BA(FMT)-20	R12 OUT
2612	1	00B92	00000000 A	PZE		RO IN
2613	1	00B93	00000000 A	PZE		RO OUT
2614	1	00B94	00001640	J	0,BA(VMT)	R13 IN
2615	1	00B95	00001640	J	0,BA(VMT)	R13 OUT
2616	1	00B96	FFFFFFFF A	DATA	-1	R1 IN
2617	1	00B97	FFFFFFFF A	DATA	-1	R1 OUT
2618	1	00B98	3272045C	LW,7	MT1+64,1	FMT
2619	1	00B99	32D2045C	LW,13	MT1+64,1	VMT/VMTR
2620	1	00B9A	00001070	J	0,BA(MT1)	VMTRCH
2621	1	00B9B	00000000 A	DATA	0	MC
2622						CBS-EIGHT BYTE DOESN'T COMPARE
2623	1	00B9C	FFFFFFFF0 A	DATA	-16	COUNT
2624	1	00B9D	60C00000 A	CBS,12	13	INSTRUCTION
2625	1	00B9E	E3100244	K	14,3,1,SETPSW	PSW1 INT
2626	1	00B9F	D3100276	K	13,3,1,L8C+2	PSW1 OUT
2627	1	00BA0	0000152B	J	0,BA(FMT)-13	R12 IN
2628	1	00BA1	00001532	J	0,BA(FMT)-6	R12 OUT
2629	1	00BA2	FFFFFFFF A	DATA	-1	RO IN

2630	1	00BA3	FFFFFFFF	A
2631	1	00BA4	0D001640	
2632	1	00BA5	06001647	
2633	1	00BA6	00000000	A
2634	1	00BA7	00000000	A
2635	1	00BA8	3272045C	
2636	1	00BA9	32D204DE	
2637	1	00BAA	000C1278	
2638	1	00BAB	00000000	A
2639				
2640	1	00BAC	FFFFFFFF0	A
2641	1	00BAD	60C00000	A
2642	1	00BAE	02200244	
2643	1	00BAF	12200276	
2644	1	00BB0	000C1538	
2645	1	00BB1	000C153A	
2646	1	00BB2	00000000	A
2647	1	00BB3	00C00000	A
2648	1	00BB4	FF001640	
2649	1	00BB5	FD001642	
2650	1	00BB6	FFFFFFFF	A
2651	1	00BB7	FFFFFFFF	A
2652	1	00BB8	3272045C	
2653	1	00BB9	32D204E0	
2654	1	00BBA	00001280	
2655	1	00BBB	00000000	A
2656				
2657	1	00BBC	FFFFFFFF0	A
2658	1	00BD0	60C00000	A
2659	1	00BBE	F70C0244	
2660	1	00BBF	D7000276	
2661	1	00BC0	000C1538	
2662	1	00BC1	0C001539	
2663	1	00BC2	FFFFFFFF	A
2664	1	00BC3	FFFFFFFF	A
2665	1	00BC4	FF001640	
2666	1	00BC5	FE001641	
2667	1	00BC6	00000000	A

SUFFIX(2)

DATA	-1
J	13,BA(VMT)
J	6,BA(VMT)+7
PZE	
PZE	
LW,7	MT1+64,1
LW,13	MT4+64,1
J	0,BA(MT4)
DATA	0
DATA	-16
CBS,12	0
K	0,2,2,SETPSW
K	1,2,2,L0C+2
J	0,BA(FMT)
J	0,BA(FMT)+2
PZE	
PZE	
J	X'FF',BA(VMT)
J	253,BA(VMT)+2
DATA	-1
DATA	-1
LW,7	MT1+64,1
LW,13	MT4+66,1
J	0,BA(MT4)+8
DATA	0
DATA	-16
CBS,12	0
K	15,7,0,SETPSW
K	13,7,0,L0C+2
J	0,BA(FMT)
J	0,BA(FMT)+1
DATA	-1
DATA	-1
J	X'FF',BA(VMT)
J	254,BA(VMT)+1
PZE	

RO OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
CBS-THIRD BYTE DOESN'T COMPARE
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
RO IN
RO OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC
CBS-SECOND BYTE DOESN'T COMPARE
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
RO IN
RO OUT
R13 IN
R13 OUT
R1 IN

2668 1 00BC7 00000000 A
 2669 1 00BC8 3272045C
 2670 1 00BC9 32D204E1
 2671 1 00BCA 00001284
 2672 1 00BCB 00000000 A
 2673
 2674 1 00BCC FFFFFFF0 A
 2675 1 00BCD 60C00000 A
 2676 1 00BCE 10300244
 2677 1 00BCF 10300276
 2678 1 00BD0 00001538
 2679 1 00BD1 00001538
 2680 1 00BD2 00000000 A
 2681 1 00BD3 00000000 A
 2682 1 00BD4 01001640
 2683 1 00BD5 01001640
 2684 1 00BD6 FFFFFFFF A
 2685 1 00BD7 FFFFFFFF A
 2686 1 00BD8 3272045C
 2687 1 00BD9 32D204E2
 2688 1 00BDA 00001288
 2689 1 00BDB 00000000 A
 2690
 2691 1 00BDC FFFFFFF0 A
 2692 1 00BDD 60C00000 A
 2693 1 00BDE 00000244
 2694 1 00BDF E0000276
 2695 1 00BE0 00001538
 2696 1 00BE1 00001538
 2697 1 00BE2 FFFFFFFF A
 2698 1 00BE3 FFFFFFFF A
 2699 1 00BE4 04001640
 2700 1 00BE5 01001643
 2701 1 00BE6 00000C00 A
 2702 1 00BE7 00000000 A
 2703 1 00BE8 3272045C
 2704 1 00BE9 32D204E3
 2705 1 00BEA 0000128C

SUFFIX(2)
 PZE
 LW,7 MT1+64,1
 LW,13 MT4+67,1
 J 0,BA(MT4)+12
 DATA 0
 DATA -16
 CBS,12 0
 K 1,0,3,SETPSW
 K 1,0,3,L0C+2
 J 0,BA(FMT)
 J 0,BA(FMT)
 PZE
 PZE
 J 1,BA(VMT)
 J 1,BA(VMT)
 DATA -1
 DATA -1
 LW,7 MT1+64,1
 LW,13 MT4+68,1
 J 0,BA(MT4)+16
 DATA 0
 DATA -16
 CBS,12 0
 K 13,0,0,SETPSW
 K 14,0,0,L0C+2
 J 0,BA(FMT)
 J 0,BA(FMT)+3
 DATA -1
 DATA -1
 J 4,BA(VMT)
 J 1,BA(VMT)+3
 PZE
 PZE
 LW,7 MT1+64,1
 LW,13 MT4+69,1
 J 0,BA(MT4)+20

R1 OUT
 FMT
 VMT/VMTR
 VMTRCH
 MC
 CBS-FIRST BYTE DOESN'T COMPARE
 COUNT
 INSTRUCTION
 PSW1 IN
 PSW1 OUT
 R12 IN
 R12 OUT
 R0 IN
 R0 OUT
 R13 IN
 R13 OUT
 R1 IN
 R1 OUT
 FMT
 VMT/VMTR
 VMTRCH
 RC
 CBS-FOURTH BYTE DOESN'T COMPARE
 COUNT
 INSTRUCTION
 PSW1 IN
 PSW1 OUT
 R12 IN
 R12 OUT
 R0 IN
 R0 OUT
 R13 IN
 R13 OUT
 R1 IN
 R1 OUT
 FMT
 VMT/VMTR
 VMTRCH

Address	OpCode	Hex	Mode	SUFFIX(2)	RC
2706	1 00BEB	00000000	A	DATA 0	RC
2707					CBS-THIRD BYTE DOESN'T COMPARE
2708	1 00BEC	FFFFFFF0	A	DATA -16	COUNT
2709	1 00BED	60C00000	A	CBS,12 0	INSTRUCTION
2710	1 00BEE	10000244		K 1,0,0,SETPSW	PSW IN
2711	1 00BEF	20000276		K 2,0,0,L0C+2	PSW1 OUT
2712	1 00BF0	00001538		J 0,BA(FMT)	R12 IN
2713	1 00BF1	0000153A		J 0,BA(FMT)+2	R12 OUT
2714	1 00BF2	00000000	A	PZE	RO IN
2715	1 00BF3	00000000	A	PZE	RO OUT
2716	1 00BF4	040C1640		J 4,BA(VMT)	R13 IN
2717	1 00BF5	02001642		J 2,BA(VMT)+2	R13 OUT
2718	1 00BF6	FFFFFFFF	A	DATA -1	R1 IN
2719	1 00BF7	FFFFFFFF	A	DATA -1	R1 OUT
2720	1 00BF8	3272045C		LW,7 MT1+64,1	FMT
2721	1 00BF9	32D204E4		LW,13 MT4+70,1	VMT/VMTR
2722	1 00BFA	00001290		J 0,BA(MT4)+24	VMTRCH
2723	1 00BFB	00000000	A	DATA 0	RC
2724					CBS-SECOND BYTE DOESN'T COMPARE
2725	1 00BFC	FFFFFFF0	A	DATA -16	COUNT
2726	1 00BFD	60C00000	A	CBS,12 0	INSTRUCTION
2727	1 00BFE	30000244		K 3,0,0,SETPSW	PSW1 IN
2728	1 00BFF	20000276		K 2,0,0,L0C+2	PSW1 OUT
2729	1 00C00	00001538		J 0,BA(FMT)	R12 IN
2730	1 00C01	00001539		J 0,BA(FMT)+1	R12 OUT
2731	1 00C02	FFFFFFFF	A	DATA -1	RO IN
2732	1 00C03	FFFFFFFF	A	DATA -1	RO OUT
2733	1 00C04	040C1640		J 4,BA(VMT)	R13 IN
2734	1 00C05	03001641		J 3,BA(VMT)+1	R13 OUT
2735	1 00C06	00000000	A	PZE	R1 IN
2736	1 00C07	00000000	A	PZE	R1 OUT
2737	1 00C08	3272045C		LW,7 MT1+64,1	FMT
2738	1 00C09	32D204E5		LW,13 MT4+71,1	VMT/VMTR
2739	1 00C0A	00001294		J 0,BA(MT4)+28	VMTRCH
2740	1 00C0B	00000000	A	DATA 0	RC
2741					CBS-FIRST BYTE DOESN'T COMPARE
2742	1 00C0C	FFFFFFF0	A	DATA -16	COUNT
2743	1 00C0D	60C00000	A	CBS,12 0	INSTRUCTION

Address	Op	Op Code	Operand	Op Name	Op Code	Op Name
2744	1	00C0E	20000244	K	2,0,0,SETPSW	PSW1 IN
2745	1	00C0F	20000276	K	2,0,0,L8C+2	PSW1 OUT
2746	1	00C10	00001538	J	0,BA(FMT)	R12 IN
2747	1	00C11	00001538	J	0,BA(FMT)	R12 OUT
2748	1	00C12	00000000 A	PZE		RO IN
2749	1	00C13	00000000 A	PZE		RO OUT
2750	1	00C14	04001640	J	4,BA(VMT)	R13 IN
2751	1	00C15	04001640	J	4,BA(VMT)	R13 OUT
2752	1	00C16	FFFFFFFF A	DATA	-1	R1 IN
2753	1	00C17	FFFFFFFF A	DATA	-1	R1 OUT
2754	1	00C18	3272045C	LW,7	MT1+64,1	FMT
2755	1	00C19	32D204E6	LW,13	MT4+72,1	VMT/VMTR
2756	1	00C1A	00001298	J	0,BA(MT4)+32	VMTRCH
2757	1	00C1B	00000000 A	DATA	0	RC
2758						CBS-REGISTER 0
2759	1	00C1C	FFFFFFFF0 A	DATA	-16	COUNT
2760	1	00C1D	60001538	CBS,0	BA(FMT)+3	INSTRUCTION
2761	1	00C1E	F0000244	K	15,0,0,SETPSW	PSW1 IN
2762	1	00C1F	C0000276	K	12,0,0,L8C+2	PSW1 OUT
2763	1	00C20	00000000 A	DATA	0	R12 IN
2764	1	00C21	00000000 A	DATA	0	R12 OUT
2765	1	00C22	00000C00 A	DATA	0	RO IN
2766	1	00C23	00000000 A	DATA	0	RO OUT
2767	1	00C24	FFFFFFFF A	DATA	-1	R13 IN
2768	1	00C25	FFFFFFFF A	DATA	-1	R13 OUT
2769	1	00C26	04001640	J	4,BA(VMT)	R1 IN
2770	1	00C27	00001644	J	0,BA(VMT)+4	RU OUT
2771	1	00C28	3272045C	LW,7	MT1+64,1	FMT
2772	1	00C29	32D2045C	LW,13	MT1+64,1	VMT/VMTR
2773	1	00C2A	00000F20	J	0,BA(TABLE)+64	VMTRCH
2774	1	00C2B	00000000 A	DATA	0	MC
2775						CBS-INDIRECT ADDRESSING-TRAP
2776	1	00C2C	FFFFFFFF0 A	DATA	-16	COUNT
2777	1	00C2D	E0C002E9 A	DATA	X'E0C002E9'	INSTRUCTION
2778	1	00C2E	07300185	K	0,7,3,SIGNA0	PSW1 IN
2779	1	00C2F	8730006B	K	8,7,3,NEIRET+1	PSW1 OUT
2780	1	00C30	00001538	J	0,BA(FMT)	R12 IN
2781	1	00C31	00001538	J	0,BA(FMT)	R12 OUT

Address	OpCode	Operand	Instruction	Comments
2782	1 00C32	00000000 A	PZE	RO IN
2783	1 00C33	00000000 A	PZE	RO OUT
2784	1 00C34	04001640	J	4,BA(VMT) R13 IN
2785	1 00C35	04001640	J	4,BA(VMT) R13 OUT
2786	1 00C36	FFFFFFFF A	DATA	R1 IN
2787	1 00C37	FFFFFFFF A	DATA	R1 OUT
2788	1 00C38	3272045C	LW,7	MT1+64,1 FMT
2789	1 00C39	3202045C	LW,13	MT1+64,1 VMT/VMTR
2790	1 00C3A	00001070	J	0,BA(MT1) VMTRCH
2791	1 00C3B	00000000 A	DATA	0 MC
2792				CBS-WORD BOUNDARY-OVERLAP
2793	1 00C3C	FFFFFFFF0 A	DATA	-16 COUNT
2794	1 00C3D	60C00008 A	CBS,12	8 INSTRUCTION
2795	1 00C3E	00000244	K	0,0,0,SETPSW PSW1 IN
2796	1 00C3F	20000276	K	2,0,0,L0C+2 PSW1 OUT
2797	1 00C40	00001640	J	0,BA(VMT) R12 IN
2798	1 00C41	00001678	J	0,BA(VMT)+56 R12 OUT
2799	1 00C42	00000000 A	PZE	RO IN
2800	1 00C43	00000000 A	PZE	RO OUT
2801	1 00C44	40001640	J	64,BA(VMT) R13 IN
2802	1 00C45	08001678	J	8,BA(VMT)+56 R13 OUT
2803	1 00C46	FFFFFFFF A	DATA	-1 R1 IN
2804	1 00C47	FFFFFFFF A	DATA	-1 R1 OUT
2805	1 00C48	3272045C	LW,7	MT1+64,1 FMT
2806	1 00C49	3202044C	LW,13	RT3+64,1 VMT/VMTR
2807	1 00C4A	00001030	J	0,BA(RT3) VMTRCH
2808	1 00C4B	00000000 A	DATA	0 MC
2809				TBS
2810	1 00C4C	FFFFFFFF0 A	DATA	-16 COUNT
2811	1 00C4D	41C00000 A	TBS,12	0 INSTRUCTION
2812	1 00C4E	F7300244	K	15,7,3,SETPSW PSW1 IN
2813	1 00C4F	F7300276	K	15,7,3,L0C+2 PSW1 OUT
2814	1 00C50	00001538	J	0,BA(FMT) R12 IN
2815	1 00C51	00001538	J	0,BA(FMT) R12 OUT
2816	1 00C52	0F0F0F0F A	DATA	X'F0F0F0F' RO IN
2817	1 00C53	0F0F0F0F A	DATA	X'F0F0F0F' RO OUT
2818	1 00C54	FF001640	J	X'FF',BA(VMT) R13 IN
2819	1 00C55	0000173F	J	0,BA(VMT)+255 R13 OUT

Address	Op Code	Hex	Label	Operation	Comments	Register
2820	1 00C56	F0F0F0F0	A	DATA	X'F0F0F0F0'	R1 IN
2821	1 00C57	F0F0F0F0	A	DATA	X'F0F0F0F0'	R1 OUT
2822	1 00C58	3272045C		LW,7	MT1+64,1	FMT
2823	1 00C59	32D204EC		LW,13	MT3+64,1	VMT/VMTR
2824	1 00C5A	00001070		J	0,BA(MT1)	VMTRCH
2825	1 00C5B	000000FF	A	DATA	255	MC
2826						TBS-TRANSLATE ZERO BYTES
2827	1 00C5C	FFFFFFF0	A	DATA	-16	COUNT
2828	1 00C5D	41C00000	A	TBS,12	0	INSTRUCTION
2829	1 00C5E	A3000244		K	10,3,0,SETPSW	PSW1 IN
2830	1 00C5F	A3000276		K	10,3,0,L0C+2	PSW1 OUT
2831	1 00C60	00001538		J	0,BA(FMT)	R12 IN
2832	1 00C61	00001538		J	0,BA(FMT)	R12 OUT
2833	1 00C62	00000000	A	PZE		R0 IN
2834	1 00C63	00000000	A	PZE		R0 OUT
2835	1 00C64	00001640		J	0,BA(VMT)	R13 IN
2836	1 00C65	00001640		J	0,BA(VMT)	R13 OUT
2837	1 00C66	FFFFFFFF	A	DATA	-1	R1 IN
2838	1 00C67	FFFFFFFF	A	DATA	-1	R1 OUT
2839	1 00C68	3272045C		LW,7	MT1+64,1	FMT
2840	1 00C69	32D204EC		LW,13	MT3+64,1	VMT/VMTR
2841	1 00C6A	00001070		J	0,BA(MT1)	VMTRCH
2842	1 00C6B	00000000	A	DATA	0	MC
2843						TBS-TRANSLATE ONE BYTE
2844	1 00C6C	FFFFFFF0	A	DATA	-16	COUNT
2845	1 00C6D	41C00020	A	TBS,12	32	INSTRUCTION
2846	1 00C6E	50300244		K	5,0,3,SETPSW	PSW1 IN
2847	1 00C6F	50300276		K	5,0,3,L0C+2	PSW1 OUT
2848	1 00C70	00001518		J	0,BA(FMT)-32	R12 IN
2849	1 00C71	00001518		J	0,BA(FMT)-32	R12 OUT
2850	1 00C72	FFFFFFFF	A	DATA	-1	R0 IN
2851	1 00C73	FFFFFFFF	A	DATA	-1	R0 OUT
2852	1 00C74	01C01640		J	1,BA(VMT)	R13 IN
2853	1 00C75	00001641		J	0,BA(VMT)+1	R13 OUT
2854	1 00C76	00000000	A	PZE		R1 IN
2855	1 00C77	00000000	A	PZE		R1 OUT
2856	1 00C78	3272045C		LW,7	MT1+64,1	FMT
2857	1 00C79	32D204EC		LW,13	MT3+64,1	VMT/VMTR

Address	OpCode	Hex	Suffix(2)	VMTRCH
2858	1 00C7A	00001070	J 0,BA(MT1)	VMTRCH
2859	1 00C7B	00000001 A	DATA 1	MC
2860				TBS-TRANSLATE TWO BYTES
2861	1 00C7C	FFFFFFF0 A	DATA -16	COUNT
2862	1 00C7D	41CFFFA A	TBS,12 -6	INSTRUCTION
2863	1 00C7E	02200244	K 0,2,2,SETPSW	PSW1 IN
2864	1 00C7F	02200276	K 0,2,2,L0C+2	PSW1 OUT
2865	1 00C80	0000153E	J 0,BA(FMT)+6	R12 IN
2866	1 00C81	0000153E	J 0,BA(FMT)+6	R12 OUT
2867	1 00C82	00000000 A	PZE	R0 IN
2868	1 00C83	00000000 A	PZE	R0 OUT
2869	1 00C84	02001640	J 2,BA(VMT)	R13 IN
2870	1 00C85	00001642	J 0,BA(VMT)+2	R13 OUT
2871	1 00C86	FFFFFFFF A	DATA -1	R1 IN
2872	1 00C87	FFFFFFFF A	DATA -1	R1 OUT
2873	1 00C88	3272045C	LW,7 MT1+64,1	FMT
2874	1 00C89	32D204EC	LW,13 MT3+64,1	VMT/VMTR
2875	1 00C8A	00001070	J 0,BA(MT1)	VMTRCH
2876	1 00C8B	00000002 A	DATA 2	MC
2877				TBS-TRANSLATE THREE BYTES
2878	1 00C8C	FFFFFFF0 A	DATA -16	COUNT
2879	1 00C8D	41C00000 A	TBS,12 0	INSTRUCTION
2880	1 00C8E	01100244	K 0,1,1,SETPSW	PSW1 IN
2881	1 00C8F	01100276	K 0,1,1,L0C+2	PSW1 OUT
2882	1 00C90	00001538	J 0,BA(FMT)	R12 IN
2883	1 00C91	00001538	J 0,BA(FMT)	R12 OUT
2884	1 00C92	FFFFFFFF A	DATA -1	R0 IN
2885	1 00C93	FFFFFFFF A	DATA -1	R0 OUT
2886	1 00C94	03001640	J 3,BA(VMT)	R13 IN
2887	1 00C95	00001643	J 0,BA(VMT)+3	R13 OUT
2888	1 00C96	00000000 A	PZE	R1 IN
2889	1 00C97	00000000 A	PZE	R1 OUT
2890	1 00C98	3272045C	LW,7 MT1+64,1	FMT
2891	1 00C99	32D204EC	LW,13 MT3+64,1	VMT/VMTR
2892	1 00C9A	00001070	J 0,BA(MT1)	VMTRCH
2893	1 00C9B	00000003 A	DATA 3	MC
2894				TBS- TRANSLATE FOURBYTES
2895	1 00C9C	FFFFFFF0 A	DATA -16	COUNT

Address	Op Code	Hex	Mode	SUFFIX(2)	Instruction
2896	1 00C9D	41C00000	A	TBS,12 0	INSTRUCTION
2897	1 00C9E	F0000244		K 15,0,0,SETPSW	PSW1 IN
2898	1 00C9F	F0000276		K 15,0,0,L8C+2	PSW1 OUT
2899	1 00CA0	00001538		J 0,BA(FMT)	R12 IN
2900	1 00CA1	00001538		J 0,BA(FMT)	R12 OUT
2901	1 00CA2	00000000	A	PZE	R0 IN
2902	1 00CA3	00000000	A	PZE	R0 OUT
2903	1 00CA4	04001640		J 4,BA(VMT)	R13 IN
2904	1 00CA5	00001644		J 0,BA(VMT)+4	R13 OUT
2905	1 00CA6	FFFFFFFF	A	DATA -1	R1 IN
2906	1 00CA7	FFFFFFFF	A	DATA -1	R1 OUT
2907	1 00CA8	3272045C		LW,7 MT1+64,1	FMT
2908	1 00CA9	32D204EC		LW,13 MT3+64,1	VMT/VMTR
2909	1 00CAA	00001070		J 0,BA(MT1)	VMTRCH
2910	1 00CAB	00000004	A	DATA 4	MC
2911					TBS-TRANSLATE FIVE BYTES
2912	1 00CAC	FFFFFFFF0	A	DATA -16	COUNT
2913	1 00CAD	41C00000	A	TBS,12 0	INSTRUCTION
2914	1 00CAE	10000244		K 1,0,0,SETPSW	PSW1 IN
2915	1 00CAF	10000276		K 1,0,0,L8C+2	PSW1 OUT
2916	1 00CB0	00001538		J 0,BA(FMT)	R12 IN
2917	1 00CB1	00001538		J 0,BA(FMT)	R12 OUT
2918	1 00CB2	FFFFFFFF	A	DATA -1	R0 IN
2919	1 00CB3	FFFFFFFF	A	DATA -1	R0 OUT
2920	1 00CB4	05001640		J 5,BA(VMT)	R13 IN
2921	1 00CB5	00001645		J 0,BA(VMT)+5	R13 OUT
2922	1 00CB6	00000000	A	PZE	R1 IN
2923	1 00CB7	00000000	A	PZE	R1 OUT
2924	1 00CB8	3272045C		LW,7 MT1+64,1	FMT
2925	1 00CB9	32D204EC		LW,13 MT3+64,1	VMT/VMTR
2926	1 00CBA	00001070		J 0,BA(MT1)	VMTRCH
2927	1 00CBB	00000005	A	DATA 5	MC
2928					TBS-TRANSLATE SIX BYTES
2929	1 00CBC	FFFFFFFF0	A	DATA -16	COUNT
2930	1 00CBD	41C00000	A	TBS,12 0	INSTRUCTION
2931	1 00CBE	20000244		K 2,0,0,SETPSW	PSW1 IN
2932	1 00CBF	20000276		K 2,0,0,L8C+2	PSW1 OUT
2933	1 00CC0	00001538		J 0,BA(FMT)	R12 IN

Address	Op	OpCode	Operand	OpCode	OpCode	OpCode	OpCode
2934	1	00CC1	00001538	J	0,BA(FMT)	R12	OUT
2935	1	00CC2	00000000 A	PZE		R0	IN
2936	1	00CC3	00000000 A	PZE		R0	OUT
2937	1	00CC4	06001640	J	6,BA(VMT)	R13	IN
2938	1	00CC5	00001646	J	0,BA(VMT)+6	R13	OUT
2939	1	00CC6	FFFFFFFF A	DATA	-1	R1	IN
2940	1	00CC7	FFFFFFFF A	DATA	-1	R1	OUT
2941	1	00CC8	3272045C	LW,7	MT1+64,1		FMT
2942	1	00CC9	32D204EC	LW,13	MT3+64,1		VMT/VMTR
2943	1	00CCA	00001070	J	0,BA(MT1)		VMTRCH
2944	1	00CCB	00000006 A	DATA	6		MC
2945						TBS-TRANSLATE SEVEN BYTES	
2946	1	00CCC	FFFFFFFF0 A	DATA	-16		COUNT
2947	1	00CCD	41C00000 A	TBS,12	0		INSTRUCTION
2948	1	00CCE	40000244	K	4,0,0,SETPSW		PSW1 IN
2949	1	00CCF	40000276	K	4,0,0,L0C+2		PSW1 OUT
2950	1	00CD0	00001538	J	0,BA(FMT)	R12	IN
2951	1	00CD1	00001538	J	0,BA(FMT)	R12	OUT
2952	1	00CD2	FFFFFFFF A	DATA	-1	R0	IN
2953	1	00CD3	FFFFFFFF A	DATA	-1	R0	OUT
2954	1	00CD4	07001640	J	7,BA(VMT)	R13	IN
2955	1	00CD5	00001647	J	0,BA(VMT)+7	R13	OUT
2956	1	00CD6	00000000 A	PZE		R1	IN
2957	1	00CD7	00000000 A	PZE		R1	OUT
2958	1	00CD8	3272045C	LW,7	MT1+64,1		FMT
2959	1	00CD9	32D204EC	LW,13	MT3+64,1		VMT/VMTR
2960	1	00CDA	00001070	J	0,BA(MT1)		VMTRCH
2961	1	00CDB	00000007 A	DATA	7		MC
2962						TBS-TRANSLATE EIGHT BYTES	
2963	1	00CDC	FFFFFFFF0 A	DATA	-16		COUNT
2964	1	00CDD	41C00000 A	TBS,12	0		
2965	1	00CDE	80000244	K	8,0,0,SETPSW		PSW1 IN
2966	1	00CDF	80000276	K	8,0,0,L0C+2		PSW1 OUT
2967	1	00CE0	00001538	J	0,BA(FMT)	R12	IN
2968	1	00CE1	00001538	J	0,BA(FMT)	R12	OUT
2969	1	00CE2	00000000 A	PZE		R0	IN
2970	1	00CE3	00000000 A	PZE		R0	OUT
2971	1	00CE4	08001640	J	8,BA(VMT)	R13	IN

Address	Op	Op Code	Op Mod	Op Type	Op Data	Op Comment	Op Effect
2972	1	00CE5	00001648	J	0,BA(VMT)+8	R13 OUT	
2973	1	00CE6	FFFFFFFF	A	DATA	R1 IN	
2974	1	00CE7	FFFFFFFF	A	DATA	R1 OUT	
2975	1	00CE8	3272045C	LW,7	MT1+64,1	FMT	
2976	1	00CE9	32D204EC	LW,13	MT3+64,1	VMT/VMTR	
2977	1	00CEA	00001070	J	0,BA(MT1)	VMTRCH	
2978	1	00CEB	00000008	A	DATA	MC	
2979						TBS-REGISTER 0	
2980	1	00CEC	FFFFFFFF0	A	DATA	COUNT	
2981	1	00CED	41001538	TBS,0	BA(FMT)	INSTRUCTION	
2982	1	00CEE	00000244	K	0,0,0,SETPSW	PSW1 IN	
2983	1	00CEF	00000276	K	0,0,0,L8C+2	PSW1 OUT	
2984	1	00CF0	F0F0F0F0	A	DATA	R12 IN	
2985	1	00CF1	FCF0F0F0	A	DATA	R12 OUT	
2986	1	00CF2	F0F0F0F0	A	DATA	R0 IN	
2987	1	00CF3	F0F0F0F0	A	DATA	R0 OUT	
2988	1	00CF4	0F0F0F0F	A	DATA	R13 IN	
2989	1	00CF5	0F0F0F0F	A	DATA	R13 OUT	
2990	1	00CF6	FF001640	J	X'FF',BA(VMT)	R1 IN	
2991	1	00CF7	0000173F	J	0,BA(VMT)+255	R1 OUT	
2992	1	00CF8	3272045C	LW,7	MT1+64,1	FMT	
2993	1	00CF9	32D204EC	LW,13	MT3+64,1	VMT/VMTR	
2994	1	00CFA	00001070	J	0,BA(MT.1)	VMTRCH	
2995	1	00CFB	000000FF	A	DATA	MC	
2996						TBS-INDIRECT ADDRESSING-TRAP	
2997	1	00CFC	FFFFFFFF0	A	DATA	COUNT	
2998	1	00CFD	C1C002E9	A	DATA	INSTRUCTION	
2999	1	00CFE	07300185	K	0,7,3,SIGNA0	PSW1 IN	
3000	1	00CFF	8730006B	K	8,7,3,NEIRET+1	PSW1 OUT	
3001	1	00D00	00001538	J	0,BA(FMT)	R12 IN	
3002	1	00D01	00001538	J	0,BA(FMT)	R12 OUT	
3003	1	00D02	F0F0F0F0	A	DATA	R0 IN	
3004	1	00D03	F0F0F0F0	A	DATA	R0 OUT	
3005	1	00D04	00001640	J	0,BA(VMT)	R13 IN	
3006	1	00D05	00001640	J	0,BA(VMT)	R13 OUT	
3007	1	00D06	0F0F0F0F	A	DATA	R1 IN	
3008	1	00D07	0F0F0F0F	A	DATA	R1 OUT	
3009	1	00D08	3272045C	LW,7	MT1+64,1	FMT	

Address	Mode	Hex Value	Suffix	Operation	Register/Control
3010	1	00D09	SUFFIX(2)	DATA	VMT/VMTR
3011	1	00D0A	L,13	J	VMTRCH
3012	1	00D0B	MT3+64,1	DATA	MC
3013			0,BA(MT1)		TTBS
3014	1	06D0C	0	DATA	COUNT
3015	1	00D0D	-16	TTBS,12	INSTRUCTION
3016	1	00D0E	0	K	PSW1 IN
3017	1	00D0F	2,7,3,SETPSW	K	PSW1 OUT
3018	1	00D10	2,7,3,L0C+2	J	R12 IN
3019	1	00D11	0,BA(FMT)	J	R12 OUT
3020	1	00D12	0,BA(FMT)	PZE	R0 IN
3021	1	00D13	0	PZE	R0 OUT
3022	1	00D14	X'FF',BA(VMT)	J	R13 IN
3023	1	00D15	0,BA(VMT)+255	J	R13 OUT
3024	1	00D16	DATA	DATA	R1 IN
3025	1	00D17	-1	DATA	R1 OUT
3026	1	00D18	-1	LW,7	FMT
3027	1	00D19	MT1+64,1	LW,13	VMT/VMTR
3028	1	00D1A	MT3+64,1	J	VMTRCH
3029	1	00D1B	0,BA(MT3)	DATA	
3030			0		TTBS-REGISTER ZERO
3031	1	00D1C	-16	DATA	COUNT
3032	1	00D1D	BA(FMT)	TTBS,0	INSTRUCTION
3033	1	00D1E	15,0,3,SETPSW	K	PSW1 IN
3034	1	00D1F	14,0,3,L0C+2	K	PSW1 OUT
3035	1	00D20	X'FOFOFOFO'	DATA	R12 IN
3036	1	00D21	X'FOFOFOFO'	DATA	R12 OUT
3037	1	00D22	X'FOFOFOFO'	DATA	R0 IN
3038	1	00D23	X'FOFOFOFO'	DATA	R0 OUT
3039	1	00D24	X'FOFOFOFO'	DATA	R13 IN
3040	1	00D25	X'FOFOFOFO'	DATA	R13 OUT
3041	1	00D26	X'FF',BA(VMT)	J	R1 IN
3042	1	00D27	0,BA(VMT)+255	J	R1 OUT
3043	1	00D28	0	LI,7	FMT
3044	1	00D29	MT3+64,1	LW,13	VMT/VMTR
3045	1	00D2A	0,BA(MT3)	J	VMTRCH
3046	1	00D2B	0	DATA	MC
3047					TTBS-REGISTER ZERO

Address	Count	Hex	Mask	Op	Suffix(2)	Register	Instruction
3048	1	00D2C	FFFFFFF0	A	DATA	-16	COUNT
3049	1	00D2D	40001538		TTBS,0	BA(FMT)	INSTRUCTION
3050	1	00D2E	E7200244		K	14,7,2,SETPSW	PSW1 IN
3051	1	00D2F	F7200276		K	15,7,2,L0C+2	PSW1 OUT
3052	1	00D30	FFFFFFFF	A	DATA	-1	R12 IN
3053	1	00D31	FFFFFFFF	A	DATA	-1	R12 OUT
3054	1	00D32	00000000	A	PZE		R0 IN
3055	1	00D33	00000000	A	PZE		R0 OUT
3056	1	00D34	00000000	A	PZE		R13 IN
3057	1	00D35	00000000	A	PZE		R13 OUT
3058	1	00D36	FF001640		J	X'FF',BA(VMT)	R1 IN
3059	1	00D37	FC001643		J	X'FC',BA(VMT)+3	R1 OUT
3060	1	00D38	227000FF	A	L'I,7	X'FF'	FMT
3061	1	00D39	32D204EC		LW,13	MT3+64,1	VMT/VMTR
3062	1	00D3A	000012B0		J	0,BA(MT3)	MVTRCH
3063	1	00D3B	00000000	A	DATA	0	MC
3064							TTBS-BIT 31 COMPARES-FIRST WORD
3065	1	00D3C	FFFFFFF0	A	DATA	-16	COUNT
3066	1	00D3D	40C0000A	A	TTBS,12	10	INSTRUCTION
3067	1	00D3E	03100244		K	0,3,1,SETPSW	PSW1 IN
3068	1	00D3F	13100276		K	1,3,1,L0C+2	PSW1 OUT
3069	1	00D40	FF00152E		J	X'FF',BA(FMT)-10	R12 IN
3070	1	00D41	0100152E		J	1,BA(FMT)-10	R12 OUT
3071	1	00D42	FFFFFFFF	A	DATA	-1	R0 IN
3072	1	00D43	FFFFFFFF	A	DATA	-1	R0 OUT
3073	1	00D44	04001640		J	4,BA(VMT)	R13 IN
3074	1	00D45	01001643		J	1,BA(VMT)+3	R13 OUT
3075	1	00D46	00000000	A	PZE		R1 IN
3076	1	00D47	00000000	A	PZE		R1 OUT
3077	1	00D48	3272049C		LW,7	MT2+64,1	FMT
3078	1	00D49	32D204EC		LW,13	MT3+64,1	VMT/VMTR
3079	1	00D4A	000012B0		J	0,BA(MT3)	VMTRCH
3080	1	00D4B	00000000	A	DATA	0	MC
3081							TTBS-BIT 30 COMPARES-SECOND WORD
3082	1	00D4C	FFFFFFF0	A	DATA	-16	COUNT
3083	1	00D4D	40C00000	A	TTBS,12	0	INSTRUCTION
3084	1	00D4E	11100244		K	1,1,1,SETPSW	PSW1 IN
3085	1	00D4F	11100276		K	1,1,1,L0C+2	PSW1 OUT

```

3086 1 00D50 FE001538
3087 1 00D51 02C01538
3088 1 00D52 00C00000 A
3089 1 00D53 00000000 A
3090 1 00D54 08001640
3091 1 00D55 01001647
3092 1 00D56 FFFFFFFF A
3093 1 00D57 FFFFFFFF A
3094 1 00D58 3272049C
3095 1 00D59 32D204EC
3096 1 00D5A 00001280
3097 1 00D5B 00000000 A
3098
3099 1 00D5C FFFFFFFF0 A
3100 1 00D5D 40C00000 A
3101 1 00D5E 00000244
3102 1 00D5F 10000276
3103 1 00D60 FC001538
3104 1 00D61 04001538
3105 1 00D62 FFFFFFFF A
3106 1 00D63 FFFFFFFF A
3107 1 00D64 0C001640
3108 1 00D65 0100164B
3109 1 00D66 000C0000 A
3110 1 00D67 00000000 A
3111 1 00D68 3272049C
3112 1 00D69 32D204EC
3113 1 00D6A 00001280
3114 1 00D6B 00000000 A
3115
3116 1 00D6C FFFFFFFF0 A
3117 1 00D6D 40C00000 A
3118 1 00D6E F0000244
3119 1 00D6F F0000276
3120 1 00D70 58001538
3121 1 00D71 08001538
3122 1 00D72 00000000 A
3123 1 00D73 00000000 A

```

```

SUFFIX(2)
J X'FE',BA(FMT)
J 2,BA(FMT)
PZE
PZE
J 8,BA(VMT)
J 1,BA(VMT)+7
DATA -1
DATA -1
LW,7 MT2+64,1
LW,13 MT3+64,1
J 0,BA(MT3)
DATA 0
DATA -16
TTBS,12 0
K 0,0,0,SETPSW
K 1,0,0,L0C+2
J X'FC',BA(FMT)
J 4,BA(FMT)
DATA -1
DATA -1
J 12,BA(VMT)
J 1,BA(VMT)+11
PZE
PZE
LW,7 MT2+64,1
LW,13 MT3+64,1
J 0,BA(MT3)
DATA 0
DATA -16
TTBS,12 0
K 15,0,0,SETPSW
K 15,0,0,L0C+2
J X'58',BA(FMT)
J 8,BA(FMT)
PZE
PZE

```

```

R12 IN
R12 OUT
R0 IN
R0 OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC
TTBS-BIT 29 COMPARES-THIRD WORD
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
R0 IN
R0 OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC
TTBS-BIT 28 COMPARES-FOURTH WORD
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
R0 IN
R0 OUT

```

Address	Count	Hex	Label	SUFFIX(2)	Operation
3124	1	00D74	10001640	J	R13 IN
3125	1	00D75	0100164F	J	R13 OUT
3126	1	00D76	FFFFFFF A	DATA	R1 IN
3127	1	00D77	FFFFFFF A	DATA	R1 OUT
3128	1	00D78	3272049C	LW,7	FMT
3129	1	00D79	32D204EC	LW,13	VMT/VMTR
3130	1	00D7A	00C01280	J	VMTRCH
3131	1	00D7B	00000000 A	DATA	MC
3132					TTBS-BIT 27 COMPARES-FIFTH WORD
3133	1	00D7C	FFFFFFF0 A	DATA	COUNT
3134	1	00D7D	40CC0000 A	TTBS,12	INSTRUCTION
3135	1	00D7E	00000244	K	PSW1 IN
3136	1	00D7F	10000276	K	PSW1 OUT
3137	1	00D80	F0001538	J	R12 IN
3138	1	00D81	10001538	J	R12 OUT
3139	1	00D82	FFFFFFF A	DATA	R0 IN
3140	1	00D83	FFFFFFF A	DATA	R0 OUT
3141	1	00D84	140C1640	J	R13 IN
3142	1	00D85	010C1653	J	R13 OUT
3143	1	00D86	00000000 A	PZE	R1 IN
3144	1	00D87	00000000 A	PZE	R1 OUT
3145	1	00D88	3272049C	LW,7	FMT
3146	1	00D89	32D204EC	LW,13	VMT/VMTR
3147	1	00D8A	00001280	J	VMTRCH
3148	1	00D8B	00000000 A	DATA	MC
3149					TTES-BIT 26 COMPARES-SIXTH WORD
3150	1	00D8C	FFFFFFF0 A	DATA	COUNT
3151	1	00D8D	40CC0000 A	TTBS,12	INSTRUCTION
3152	1	00D8E	00CC0244	K	PSW1 IN
3153	1	00D8F	10000276	K	PSW1 OUT
3154	1	00D90	E0001538	J	R12 IN
3155	1	00D91	20001538	J	R12 OUT
3156	1	00D92	00000000 A	PZE	R0 IN
3157	1	00D93	00000000 A	PZE	R0 OUT
3158	1	00D94	18001640	J	R13 IN
3159	1	00D95	01001657	J	R13 OUT
3160	1	00D96	FFFFFFF A	DATA	R1 IN
3161	1	00D97	FFFFFFF A	DATA	R1 OUT

Address	Count	Hex	Mask	Label	Offset	Field	Field
3162	1	00D98	3272049C		SUFFIX(2)	LW,7	MT2+64,1
3163	1	00D99	32D204EC			LW,13	MT3+64,1
3164	1	00D9A	00001280			J	0,BA(MT3)
3165	1	00D9B	00000000	A		DATA	0
3166							
3167	1	00D9C	FFFFFFF0	A		DATA	-16
3168	1	00D9D	40C00000	A		TTBS,12	0
3169	1	00D9E	F0000244			K	15,0,0,SETPSW
3170	1	00D9F	F0000276			K	15,0,0,L0C+2
3171	1	00DA0	C0001538			J	X'CO',BA(FMT)
3172	1	00DA1	40001538			J	X'40',BA(FMT)
3173	1	00DA2	FFFFFFF7	A		DATA	-1
3174	1	00DA3	FFFFFFF7	A		DATA	-1
3175	1	00DA4	1C001640			J	28,BA(VMT)
3176	1	00DA5	0100165B			J	1,BA(VMT)+27
3177	1	00DA6	00000000	A		PZE	
3178	1	00DA7	00000000	A		PZE	
3179	1	00DA8	3272049C			LW,7	MT2+64,1
3180	1	00DA9	32D204EC			LW,13	MT3+64,1
3181	1	00DAA	00001280			J	0,BA(MT3)
3182	1	00DAB	00000000	A		DATA	0
3183							
3184	1	00DAC	FFFFFFF0	A		DATA	-16
3185	1	00DAD	40C00000	A		TTBS,12	0
3186	1	00DAE	00000244			K	0,0,0,SETPSW
3187	1	00DAF	10000276			K	1,0,0,L0C+2
3188	1	00DB0	80001538			J	X'80',BA(FMT)
3189	1	00DB1	80001538			J	X'80',BA(FMT)
3190	1	00DB2	00000000	A		PZE	
3191	1	00DB3	00000000	A		PZE	
3192	1	00DB4	20001640			J	32,BA(VMT)
3193	1	00DB5	0100165F			J	1,BA(VMT)+31
3194	1	00DB6	FFFFFFF7	A		DATA	-1
3195	1	00DB7	FFFFFFF7	A		DATA	-1
3196	1	00DB8	3272049C			LW,7	MT2+64,1
3197	1	00DB9	32D204EC			LW,13	MT3+64,1
3198	1	00DBA	00001280			J	0,BA(MT3)
3199	1	00DBB	00000000	A		DATA	0

FMT
VMT/VMTR
VMTRCH
MC
TTBS-BIT 25 COMPARES-SEVENTH WORD
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
R0 IN
R0 OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC
TTBS-BIT 24 COMPARES-EIGHTH WORD
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
R0 IN
R0 OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC

```

3200
3201 1 00DBC FFFFFFFF A
3202 1 00DBD 40C00000 A
3203 1 00DBE E0000244
3204 1 00DBF F0000276
3205 1 00DC0 01001558
3206 1 00DC1 01001558
3207 1 00DC2 FFFFFFFF A
3208 1 00DC3 FFFFFFFF A
3209 1 00DC4 03001640
3210 1 00DC5 06001642
3211 1 00DC6 00000000 A
3212 1 00DC7 00000000 A
3213 1 00DC8 3272049C
3214 1 00DC9 32D204EC
3215 1 00DCA 000012B0
3216 1 00DCB 00000000 A
3217
3218 1 00DCC FFFFFFFF A
3219 1 00DCD 40C00000 A
3220 1 00DCE F0000244
3221 1 00DCF F0000276
3222 1 00DD0 02001558
3223 1 00DD1 02001558
3224 1 00DD2 00000000 A
3225 1 00DD3 00000000 A
3226 1 00DD4 03001640
3227 1 00DD5 02001646
3228 1 00DD6 FFFFFFFF A
3229 1 00DD7 FFFFFFFF A
3230 1 00DD8 3272049C
3231 1 00DD9 32D204EC
3232 1 00DDA 000012B0
3233 1 00ddb 00000000 A
3234
3235 1 00DDC FFFFFFFF A
3236 1 00DDD 40C00000 A
3237 1 00DDE 00000244

```

SUFFIX(2)

```

DATA -16
TTBS,12 0
K 14,0,0,SETPSW
K 15,0,0,L8C+2
J 1,BA(FMT)+32
J 1,BA(FMT)+32
DATA -1
DATA -1
J 8,BA(VMT)
J 6,BA(VMT)+2
PZE
PZE
LW,7 MT2+64,1
LW,13 MT3+64,1
J 0,BA(MT3)
DATA 0

DATA -16
TTBS,12 0
K 15,0,0,SETPSW
K 15,0,0,L8C+2
J 2,BA(FMT)+32
J 2,BA(FMT)+32
PZE
PZE
J 8,BA(VMT)
J 2,BA(VMT)+6
DATA -1
DATA -1
LW,7 MT2+64,1
LW,13 MT3+64,1
J 0,BA(MT3)
DATA 0

DATA -16
TTBS,12 0
K 0,0,0,SETPSW

```

```

TTBS-BIT 23 COMPARES-FIRST WORD
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
R0 IN
R0 OUT
R13 IN
RU3 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC

TTBS-BIT 22 COMPARES-SECOND WORD
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
R0 IN
R0 OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC

TTBS-BIT 21 COMPARES-THIRD WORD
COUNT
INSTRUCTION
PSW1 IN

```

Address	Op Code	Hex Value	Modifier	SUFFIX(2)	Register/Control
3238	1 00DDF	10000276		K	1,0,0,L0C+2 PSW1 OUT
3239	1 00DE0	04001558		J	4,BA(FMT)+32 R12 IN
3240	1 00DE1	04001558		J	4,BA(FMT)+32
3241	1 00DE2	FFFFFFFF A		DATA	-1 RO IN
3242	1 00DE3	FFFFFFFF A		DATA	-1 RO OUT
3243	1 00DE4	0C001640		J	12,BA(VMT) R13 IN
3244	1 00DE5	0200164A		J	2,BA(VMT)+10 R13 OUT
3245	1 00DE6	00000000 A		PZE	R1 IN
3246	1 00DE7	00000000 A		PZE	R1 OUT
3247	1 00DE8	3272049C		LW,7	MT2+64,1 FMT
3248	1 00DE9	32D204EC		LW,13	MT3+64,1 VMT/VMTR
3249	1 00DEA	000012B0		J	0,BA(MT3) VMTRCH
3250	1 00DEB	00000000 A		DATA	0 MC
3251					
3252	1 00DEC	FFFFFFFF0 A		DATA	-16 TTBS-BIT 20 COMPARES-FOURTH WORD
3253	1 00DED	40C00000 A		TTBS,12	0 COUNT
3254	1 00DEE	E0C00244		K	14,0,0,SETPSW INSTRUCTION
3255	1 00DEF	F0000276		K	15,0,0,L0C+2 PSW1 IN
3256	1 00DF0	08001558		J	8,BA(FMT)+32 PSW1 OUT
3257	1 00DF1	08001558		J	8,BA(FMT)+32 R12 IN
3258	1 00DF2	00000000 A		PZE	R12 OUT
3259	1 00DF3	00000000 A		PZE	RO IN
3260	1 00DF4	10001640		J	16,BA(VMT) RO OUT
3261	1 00DF5	0200164E		J	2,BA(VMT)+14 R13 IN
3262	1 00DF6	FFFFFFFF A		DATA	-1 R1 IN
3263	1 00DF7	FFFFFFFF A		DATA	-1 R1 OUT
3264	1 00DF8	3272049C		LW,7	MT2+64,1 FMT
3265	1 00DF9	32D204EC		LW,13	MT3+64,1 VMT/VMTR
3266	1 00DFA	000012B0		J	0,BA(MT3) VMTRCH
3267	1 00DFB	00000000 A		DATA	0 MC
3268					
3269	1 00DFC	FFFFFFFF0 A		DATA	-16 TTBS-BIT 19 COMPARES-FIFTH WORD
3270	1 00DFD	40C00000 A		TTBS,12	0 COUNT
3271	1 00DFE	10000244		K	1,0,0,SETPSW INSTRUCTION
3272	1 00DFE	10000276		K	1,0,0,L0C+2 PSW1 IN
3273	1 00E00	10001558		J	16,BA(FMT)+32 PSW1 OUT
3274	1 00E01	10001558		J	16,BA(FMT)+32 R12 IN
3275	1 00E02	FFFFFFFF A		DATA	-1 R12 OUT
					RO IN

Address	OpCode	Hex	Mode	SUFFIX(2)	Register/Control
3276	1 00E03	FFFFFFFF	A	DATA	RO OUT
3277	1 00E04	140C1640		J	R13 IN
3278	1 00E05	02001652		J	R13 OUT
3279	1 00E06	00000000	A	PZE	R1 IN
3280	1 00E07	00000000	A	PZE	R1 OUT
3281	1 00E08	3272049C		LW,7	FMT
3282	1 00E09	32D204EC		-LW,13	VMT/VMTR
3283	1 00E0A	000012B0		J	VMTRCH
3284	1 00E0B	00000000	A	DATA	MC
3285					TTBS-BIT 18 COMPARES-SIXTH WORD
3286	1 00E0C	FFFFFFFF0	A	DATA	COUNT
3287	1 00E0D	40C00000	A	TTBS,12	INSTRUCTION
3288	1 00E0E	00000244		K	PSW1 IN
3289	1 00E0F	10000276		K	PSW1 OUT
3290	1 00E10	20001558		J	R12 IN
3291	1 00E11	20001558		J	
3292	1 00E12	00000000	A	PZE	RO IN
3293	1 00E13	00000000	A	PZE	RO OUT
3294	1 00E14	18001640		J	R13 IN
3295	1 00E15	02001656		J	R13 OUT
3296	1 00E16	FFFFFFFF	A	DATA	R1 IN
3297	1 00E17	FFFFFFFF	A	DATA	R1 OUT
3298	1 00E18	3272049C		LW,7	FMT
3299	1 00E19	32D204EC		LW,13	VMT/VMTR
3300	1 00E1A	000012B0		J	VMTRCH
3301	1 00E1B	00000000	A	DATA	MC
3302					TTBS-17 COMPARES-SEVENTH WORD
3303	1 00E1C	FFFFFFFF0	A	DATA	COUNT
3304	1 00E1D	40C00000	A	TTBS,12	INSTRUCTION
3305	1 00E1E	E0000244		K	PSW1 IN
3306	1 00E1F	F0000276		K	PSW1 OUT
3307	1 00F20	40001558		J	R12 IN
3308	1 00E21	40001558		J	R12 OUT
3309	1 00E22	FFFFFFFF	A	DATA	RO IN
3310	1 00E23	FFFFFFFF	A	DATA	RO OUT
3311	1 00E24	1C001640		J	R13 IN
3312	1 00E25	0200165A		J	R13 OUT
3313	1 00E26	00000000	A	PZE	R1 IN

			SUFFIX(2)		
3314	1	00E27	00000000 A	PZE	R1 OUT
3315	1	00E28	3272049C	LW,7	FMT
3316	1	00E29	32D204EC	LW,13	VMT/VMTR
3317	1	00E2A	00001280	J	VMTRCH
3318	1	00E2B	00000000 A	DATA	MC
3319					TTBS-BIT 16 COMPARES-EIGHTH WORD
3320	1	00E2C	FFFFFFF0 A	DATA	COUNT
3321	1	00E2D	40CC0000 A	TTBS,12	INSTRUCTION
3322	1	00E2E	10000244	K	PSW1 IN
3323	1	00E2F	10000276	K	PSW1 OUT
3324	1	00E30	80C01558	J	R12 IN
3325	1	00E31	80001558	J	R12 OUT
3326	1	00E32	00000000 A	PZE	R0 IN
3327	1	00E33	00000000 A	PZE	R0 OUT
3328	1	00E34	20C01640	J	R13 IN
3329	1	00E35	0200165E	J	R13 OUT
3330	1	00E36	FFFFFFFF A	DATA	R1 IN
3331	1	00E37	FFFFFFFF A	DATA	R1 OUT
3332	1	00E38	3272049C	LW,7	FMT
3333	1	00E39	32D204EC	LW,13	VMT/VMTR
3334	1	00E3A	00001280	J	VMTRCH
3335	1	00E3B	00000000 A	DATA	MC
3336					TTBS-BITS 10 AND 15 COMPARE
3337	1	00E3C	FFFFFFF0 A	DATA	COUNT
3338	1	00E3D	40C00000 A	TTBS,12	INSTRUCTION
3339	1	00E3E	00000244	K	PSW1 IN
3340	1	00E3F	10CC0276	K	PSW1 OUT
3341	1	00E40	FF001538	J	R12 IN
3342	1	00E41	21001538	J	R12 OUT
3343	1	00E42	FFFFFFFF A	DATA	R0 IN
3344	1	00E43	FFFFFFFF A	DATA	R0 OUT
3345	1	00E44	04C01640	J	R13 IN
3346	1	00E45	03C01641	J	R13 OUT
3347	1	00E46	00000000 A	PZE	R1 IN
3348	1	00E47	00000000 A	PZE	R1 OUT
3349	1	00E48	3272052C	LW,7	FMT
3350	1	00E49	32D204EC	LW,13	VMT/VMTR
3351	1	00E4A	00001280	J	VMTRCH

```

3352 1 00E4B 00000000 A
3353
3354 1 00E4C FFFFFFFF A
3355 1 00E4D 40C00000 A
3356 1 00E4E E0000244
3357 1 00E4F F0000276
3358 1 00E50 FF001538
3359 1 00E51 F0001538
3360 1 00E52 00000000 A
3361 1 00E53 C0000000 A
3362 1 00E54 04001640
3363 1 00E55 04001640
3364 1 00E56 FFFFFFFF A
3365 1 00E57 FFFFFFFF A
3366 1 00E58 3272045C
3367 1 00E59 32D204EC
3368 1 00E5A 000012B0
3369 1 00E5B 00000000 A
3370
3371 1 00E5C FFFFFFFF A
3372 1 00E5D 40C00000 A
3373 1 00E5E 10C00244
3374 1 00E5F 10000276
3375 1 00E60 FF001538
3376 1 00E61 0F001538
3377 1 00E62 FFFFFFFF A
3378 1 00E63 FFFFFFFF A
3379 1 00E64 04001640
3380 1 00E65 04001640
3381 1 00E66 00000000 A
3382 1 00E67 00000000 A
3383 1 00E68 3272045D
3384 1 00E69 32D204EC
3385 1 00E6A 000012B0
3386 1 00E6B 00000000 A
3387
3388 1 00E6C FFFFFFFF A
3389 1 00E6D C0C002E9 A

```

```

SUFFIX(2)
DATA 0
DATA -16
TTBS,12 0
K 14,0,0,SETPSW
K 15,0,0,L0C+2
J X'FF',BA(FMT)
J X'FO',BA(FMT)
PZE
PZE
J 4,BA(VMT)
J 4,BA(VMT)
DATA -1
DATA -1
LW,7 MT1+64,1
LW,13 MT3+64,1
J 0,BA(MT3)
DATA 0
DATA -16
TTBS,12 0
K 1,0,0,SETPSW
K 1,0,0,L0C+2
J X'FF',BA(FMT)
J X'F',BA(FMT)
DATA -1
DATA -1
J 4,BA(VMT)
J 4,BA(VMT)
PZE
PZE
LW,7 MT1+65,1
LW,13 MT3+64,1
J 0,BA(MT3)
DATA 0
DATA -16
DATA X'C0C002E9'

```

```

MC
TTBS-BITS 0-3 COMPARE
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
R0 IN
R0 OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC
TTBS-BITS 4-7 COMPARE
COUNT
INSTRUCTION
PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
R0 IN
R0 OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC
TTBS-INDIRECT ADDRESSING-TRAP
COUNT
INSTRUCTION

```

3390	1	00E6E	77300185
3391	1	00E6F	F7300068
3392	1	00E70	FF001538
3393	1	00E71	FF001538
3394	1	00E72	00000000 A
3395	1	00E73	00000000 A
3396	1	00E74	04001640
3397	1	00E75	04001640
3398	1	00E76	00000000 A
3399	1	00E77	00000000 A
3400	1	00E78	3272045C
3401	1	00E79	320204EC
3402	1	00E7A	000012B0
3403	1	00E7B	00000000 A
3404	1	00E7C	00000000 A

SUFFIX(2)	
K	7,7,3,SI9NA0
K	15,7,3,NEIRET+1
J	X'FF',BA(FMT)
J	X'FF',BA(FMT)
PZE	
PZE	
J	4,BA(VMT)
J	4,BA(VMT)
PZE	
PZE	
LW,7	MT1+64,1
LW,13	MT3+64,1
J	0,BA(MT3)
DATA	0
DATA	0

PSW1 IN
PSW1 OUT
R12 IN
R12 OUT
R0 IN
R0 OUT
R13 IN
R13 OUT
R1 IN
R1 OUT
FMT
VMT/VMTR
VMTRCH
MC